DESCRIPTION
The MP8103 is a single supply, dual rail-to-rail output operational amplifier. This amplifier provides 600KHz bandwidth while consuming an incredibly low 14µA of supply current. The MP8103 can operate with a single supply voltage as low as 1.8V. The input common mode can go below the negative rail. The maximum supply voltage is 5.5V which allows the device to operate from ±0.9V to ±2.75V or a single supply. The MP8103 is available in an 8-pin 3mm x 5mm MSOP package.

FEATURES
- Single Supply Operation: 1.8V to 5.5V
- 600KHz –3dB Bandwidth
- 14µA Supply Current
- Rail-to-Rail Output
- Unity-Gain Stable
- Input Common Mode to Ground
- Drives Up to 1000pF of Capacitive Loads
- High Slew Rate: 0.1V/µs
- Available in a MSOP8 Package

APPLICATIONS
- Portable Equipment
- PDAs
- Pagers
- Cordless Phones
- Handheld GPS
- Consumer Electronics
- Smoke Detector
- Portable Medical Equipment

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### PACKAGE REFERENCE

<table>
<thead>
<tr>
<th>Part Number*</th>
<th>Package</th>
<th>Temperature</th>
</tr>
</thead>
<tbody>
<tr>
<td>MP8103DK</td>
<td>MSOP8</td>
<td>–40°C to +85°C</td>
</tr>
</tbody>
</table>

* For Tape & Reel, add suffix –Z (eg. MP8103DK–Z)
For RoHS Compliant Packaging, add suffix –LF (eg. MP8103DK–LF–Z)

### ELECTRICAL CHARACTERISTICS

\( V_+ = +5\text{V}, V_- = 0\text{V}, V_{CM} = V+/2, R_L = 10k\Omega, T_A = +25°C \), unless otherwise noted.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Condition</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Offset Voltage</td>
<td>V_{OS}</td>
<td></td>
<td>–5</td>
<td>1</td>
<td>+5</td>
<td>mV</td>
</tr>
<tr>
<td>Input Offset Voltage Temp Coefficient</td>
<td>I_B</td>
<td></td>
<td>15</td>
<td>15</td>
<td>15</td>
<td>µV/°C</td>
</tr>
<tr>
<td>Input Bias Current</td>
<td>I_{OS}</td>
<td></td>
<td>2</td>
<td>2</td>
<td>2</td>
<td>pA</td>
</tr>
<tr>
<td>Input Voltage Range</td>
<td>V_{CM}</td>
<td>CMRR &gt; 60dB</td>
<td>0</td>
<td>3.8</td>
<td>3.8</td>
<td>V</td>
</tr>
<tr>
<td>Common-Mode Rejection Ratio</td>
<td>CMRR</td>
<td>0 &lt; V_{CM} &lt; 3.5V</td>
<td>82</td>
<td>82</td>
<td>82</td>
<td>dB</td>
</tr>
<tr>
<td>Power Supply Rejection Ratio</td>
<td>PSRR</td>
<td>Supply Voltage change of 1.0V</td>
<td>80</td>
<td>80</td>
<td>80</td>
<td>dB</td>
</tr>
<tr>
<td>Large Signal Voltage Gain</td>
<td>A_{VOL}</td>
<td>R_L = 100kΩ, V_{OUT} = 5.0 Peak to Peak</td>
<td>60</td>
<td>88</td>
<td>88</td>
<td>dB</td>
</tr>
<tr>
<td>Maximum Output Voltage Swing</td>
<td>V_{OUT}</td>
<td>R_L = 10kΩ</td>
<td>V+ – 23mV</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Minimum Output Voltage Swing</td>
<td>V_{OUT}</td>
<td>R_L = 10kΩ</td>
<td>V– + 19mV</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Gain-Bandwidth Product</td>
<td>GBW</td>
<td>R_L = 200kΩ, C_L = 2pF, V_{OUT} = 0</td>
<td>200</td>
<td></td>
<td></td>
<td>KHz</td>
</tr>
<tr>
<td>–3dB Bandwidth</td>
<td>BW</td>
<td>A_V = 1, C_L = 2pF, R_L = 1MΩ</td>
<td>600</td>
<td></td>
<td></td>
<td>KHz</td>
</tr>
<tr>
<td>Slew Rate</td>
<td>SR</td>
<td>A_V = 1, C_L = 2pF, R_L = 1MΩ</td>
<td>0.1</td>
<td></td>
<td></td>
<td>V/µs</td>
</tr>
<tr>
<td>Short Circuit Current</td>
<td>I_{SC}</td>
<td>Source</td>
<td>20</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Sink</td>
<td>20</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>Supply Current</td>
<td></td>
<td>No Load</td>
<td>14</td>
<td>22</td>
<td>22</td>
<td>µA</td>
</tr>
</tbody>
</table>

**Notes:**
1) Exceeding these ratings may damage the device.
2) The device is not guaranteed to function outside of its operating conditions.
3) Measured on approximately 1” square of 1 oz copper.

**ABSOLUTE MAXIMUM RATINGS**

Supply Voltage (\( V_+ \) to \( V_- \)).......................... +6.0V
Differential Input Voltage (\( V_{IN+} – V_{IN-} \))..... +6.0V
Input Voltage (\( V_{IN+} + 0.3V \), \( V_{IN–} – 0.3V \)

**Recommended Operating Conditions**

Supply Voltage ........................................+1.8V to +5.5V
Operating Temperature ............................–40°C to +85°C

**Thermal Resistance**

\( \theta_{JA} \quad \theta_{JC} \)
MSOP8 .................................. 150..... 65... °C/W
PIN FUNCTIONS

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>OUT1</td>
<td>Output of First Op-Amp.</td>
</tr>
<tr>
<td>2</td>
<td>IN1-</td>
<td>Inverting Input of First Op-Amp.</td>
</tr>
<tr>
<td>3</td>
<td>IN1+</td>
<td>Non-Inverting Input of First Op-Amp.</td>
</tr>
<tr>
<td>4</td>
<td>V-</td>
<td>Ground or Supply Return Pin.</td>
</tr>
<tr>
<td>5</td>
<td>IN2+</td>
<td>Non-Inverting Input of Second Op-Amp.</td>
</tr>
<tr>
<td>6</td>
<td>IN2-</td>
<td>Inverting Input of Second Op-Amp.</td>
</tr>
<tr>
<td>7</td>
<td>OUT2</td>
<td>Output of Second Op-Amp.</td>
</tr>
<tr>
<td>8</td>
<td>V+</td>
<td>Supply Voltage.</td>
</tr>
</tbody>
</table>

TEST CIRCUITS

Notes: Close S3 for positive gain. Input signal to RF(+Av) connector. The gain Av = 1 + \( \frac{R_{FB}}{R_{IN}} \). For unity gain, remove \( R_{IN} \) and short \( R_{FB} \). Open S3 for negative gain. Input signal to RF(-Av) connector. The gain \( Av = -\frac{R_{FB}}{R_{IN}} \). S1 and S2 are switches for possible resistor and capacitor load connections.

Figure 1—AC Test Circuit
TEST CIRCUITS (continued)

Figure 2—Positive Power Supply Rejection Ratio Measurement
TYPICAL PERFORMANCE CHARACTERISTICS

$T_A = +25\degree C$, unless otherwise noted.

![Graphs showing typical performance characteristics](image-url)
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

$T_A = +25^\circ C$, unless otherwise noted.

**Gain Bandwidth and Phase Margin**

- $V_{+/-} = +/-1.35V$, Gain = 1, $R_L = 1M\Omega$
- $V_{+/-} = +/-2.50V$, Gain = 1, $R_L = 1M\Omega$
- $V_{+/-} = +/-1.35V$, Gain = 2, $R_L = 1M\Omega$
- $V_{+/-} = +/-2.50V$, Gain = 10, $R_L = 1M\Omega$
- $V_{+/-} = +/-1.35V$, Gain = 10, $R_L = 1M\Omega$

**Close-Loop Unity Gain Frequency Response**

- $V_{+/-} = +/-2.50V$, Gain = 1

**PSRR vs. Frequency**

- $V_- = -2.5V$, $V_+ = 2.5V$
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

$T_A = +25^\circ C$, unless otherwise noted.

**Small Signal Pulse Response**
- $A_v = 1$, $V^+ = 2.5V$, $V^- = -2.5V$
- $R_L = 1\,\Omega$, $C_L = 8\,\mu F$

**Small Signal Pulse Response**
- $A_v = 1$, $V^+ = 1.25V$, $V^- = -1.25V$
- $R_L = 1\,\Omega$, $C_L = 8\,\mu F$

**Small Signal Pulse Response**
- $A_v = 1$, $V^+ = 2.5V$, $V^- = -2.5V$
- $R_L = 1\,\Omega$, $C_L = 50\,\mu F$

**Small Signal Pulse Response**
- $A_v = 1$, $V^+ = 1.25V$, $V^- = -1.25V$
- $R_L = 1\,\Omega$, $C_L = 50\,\mu F$

**Small Signal Pulse Response**
- $A_v = -1$, $V^+ = 2.5V$, $V^- = -2.5V$
- $R_L = 5\,\kappa\Omega$, $C_L = 8\,\mu F$

**Small Signal Pulse Response**
- $A_v = -1$, $V^+ = 1.25V$, $V^- = -1.25V$
- $R_L = 5\,\kappa\Omega$, $C_L = 8\,\mu F$
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

$T_A = +25^\circ C$, unless otherwise noted.

### Large Signal Pulse Response

- $A_v = 1$, $V^+ = 2.5V$, $V^- = -2.5V$
- $R_L = 1M\Omega$, $C_L = 8pF$

### Large Signal Pulse Response

- $A_v = 1$, $V^+ = 1.25V$, $V^- = -1.25V$
- $R_L = 1M\Omega$, $C_L = 8pF$

### Rail to Rail Output Operation

- $A_v = -2$, $V^+ = 2.5V$, $V^- = -2.5V$
- $R_L = 1M\Omega$, $C_L = 8pF$

### Rail to Rail Output Operation

- $A_v = 2$, $V^+ = 2.5V$, $V^- = -2.5V$
- $R_L = 1M\Omega$, $C_L = 8pF$

### Rail to Rail Output Operation

- $A_v = 2$, $V^+ = 1.25V$, $V^- = -1.25V$
- $R_L = 1M\Omega$, $C_L = 8pF$

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**APPLICATION INFORMATION**

**Power Supply Bypassing**

Regular supply bypassing techniques are recommended. A 10µF capacitor in parallel with a 0.1µF capacitor on both the positive and negative supplies is ideal. For the best performance, all bypassing capacitors should be located as close to the op amp as possible and all capacitors should be low ESL (Equivalent Series Inductance) and low ESR (Equivalent Series Resistance). Surface mount ceramic capacitors are ideal.
PACKAGE INFORMATION

MSOP8

TOP VIEW

BOTTOM VIEW

FRONT VIEW

SIDE VIEW

NOTE:

1) CONTROL DIMENSION IS IN INCHES. DIMENSION IN BRACKET IS IN MILLIMETERS.
2) PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSION OR GATE BURR.
3) PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION.
4) LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.004" INCHES MAX.
5) PIN 1 IDENTIFICATION HAS HALF OR FULL CIRCLE OPTION.
6) DRAWING MEETS JEDEC MO-187, VARIATION AA.
7) DRAWING IS NOT TO SCALE.

RECOMMENDED LAND PATTERN