



PRODUCT RELIABILITY REPORT

Product: MPC1100C

**Reliability Department
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1. Device Information

Product:	MPC1100C
Report Date:	08/06/2023

2. Summary of Test Results

Test	Test Condition	Lot# or Date Code	Test Results (S.S./Rej)	Comment
Temperature, Bias, and Operating Life	JESD22-A108, @+125°C for 1000 hours	KA65641.8F M265075.8G MB62336.8K	30/0 30/0 30/0	
PTC	IPC9592, from -40°C to 125°C for 1000 cycles.	KA65641.8F M265075.8G MB62336.8K	3/0 3/0 3/0	
Electrostatic Discharge Human Body Model (HBM)	ANSI/ESDA/JEDEC JS-001	MB62336.8F	3/0	>2000V
Electrostatic Discharge Charged Device Model (CDM)	ANSI/ESDA/JEDEC JS-002	MB62336.8F	3/0	>750V
Latch-up (LU)	EIA/JESD78	MB62336.8F	6/0	>+/-100mA & >1.5V _{ccmax}
Moisture/Reflow Sensitivity	J-STD-020	2152 2202 2216	70/0 70/0 70/0	MSL = 3
High Temperature Storage Life	JESD22-A103, @150°C for 1000 hours	2152 2202 2216	5/0 5/0 5/0	
Low Temperature Storage Life	JESD22-A119, @-65°C for 1000 hours	2152 2202 2216	5/0 5/0 5/0	
Temperature Cycling	IPC9592, -40°C to 125°C, dwell time=15mins, ramp time=15mins, 1hour/cycle, for 1000 cycles.	2152 2202 2216	30/0 30/0 30/0	
Steady State Temperature Humidity Bias Life Test	IPC9592, @85°C/85%RH static bias at Vinmax for 1000 hours	2152 2202 2216	30/0 30/0 30/0	



3. Failure Rate Calculation

Failure Rate (FIT@60%CL): 30.3 FIT
MTBF (years): 3,767 Years

Revision / Update History

<u>Revision</u>	<u>Reason for Change</u>	<u>Date</u>	<u>Rel Engineer</u>
1.0	Initial Release	August 2023	Ramon Lei

Appendix: Description of Reliability Test and Failure Rate Calculation

High Temperature Operating Life Test

Purpose: This test is a worst-case life test that checks the integrity of the product. The high temperature testing is for acceleration of any potential failures over time. The calculation for failure rate (FIT) is completed using the Arrhenius equation.

Condition: 125°C @ Vinmax

Pass Criteria: All units must pass the min/max limits of the datasheet.

Power Temperature Cycle Test

Purpose: This test is used to determine the ability of a device to withstand alternate exposures at high and low temperature extremes with operating biases periodically applied and removed. It is intended to simulate worst case conditions encountered in typical applications.

Condition: -45°C to 125°C

Pass Criteria: All units must pass min/max limits of the datasheet

ESD Test

Purpose: The purpose of the ESD test is to guarantee that the device can withstand electrostatic voltages during handling.

Condition: Human Body Model and Charged Device Model

Pass Criteria: ESD Testing on every pin. The device must be fully functional after testing and pass the min/max limits in the datasheet.

IC Latch-Up Test

Purpose: The purpose of this specification is to establish a method for determining IC latch-up characteristics and to define latch-up failure criteria. Latch-up characteristics are extremely important in determining product reliability and minimizing No Trouble Found (NTF) and Electrical Overstress (EOS) failures due to latch-up.

Condition: Voltage and current injection

Pass criteria: All pins with the exception of “no connect” pins and timing related pins, shall be latch-up tested. The device must be fully functional after testing and pass the min/max limits in the datasheet.

Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices

Purpose: The purpose of this standard is to identify the classification level of nonhermetic solid state surface mount devices (SMDs) that are sensitive to moisture-induced stress so that they can be properly packaged, stored, and handled to avoid damage during assembly solder reflow attachment and/or repair operations.

Condition: Bake + moisture sock + 3X reflow at 260°C

Pass criteria: All units must pass the min/max limits of the datasheet

High Temperature Storage Life

Purpose: The test is typically used to determine the effect of time and temperature, under storage conditions, for thermally activated failure mechanisms of solid state electronic devices, including nonvolatile memory devices (data retention failure mechanisms).

Condition: Bake at 150°C

Pass Criteria: All units must pass min/max limits of the datasheet

Low Temperature Storage Life

Purpose: The test is typically used to determine the effects of time and temperature, under storage conditions, for thermally activated failure mechanisms and time-to-failure distributions of solid state electronic devices, including nonvolatile memory devices (data retention failure mechanisms).

Condition: Bake at -65°C

Pass Criteria: All units must pass min/max limits of the datasheet



Temperature Cycle Test

- Purpose:** This test is used to evaluate the die attach integrity and bond integrity. This is similar to the Thermal Shock test, but can generate different failure modes due to the longer dwell time and gradual temperature change.
- Condition:** -45°C to 125°C
- Pass Criteria:** All units must pass min/max limits of the datasheet

Steady State Temperature Humidity Bias Life Test

- Purpose:** This is to check the performance of the device in humid environments. This test checks the integrity of the passivation, poor metal to plastic seal and contamination level during assembly and material compatibility.
- Condition:** 85%RH at 85°C with $V_{in}=V_{inmax}$
- Pass Criteria:** All units must pass min/max limits of the datasheet