EV4835A-RD-03A

180V, 14Ω, 32-Channel Analog Switch Evaluation Board

DESCRIPTION

The EV4835A-RD-03A is an evaluation board designed for the MP4835A, a high-voltage, 32-channel analog switch. The MP4835A is designed for medical ultrasound applications. It can also be used for non-destructive tests (NDTs), as the application is very similar to medical ultrasound.

The high-voltage, analog switches are used to multiplex the transmitter and receiver circuitries to different piezoelectric transducer (PZT) elements. The MP4835A has a resistor that is connected from the ground to the output pins (SWOUTx, where x = 0–31). This ensures that a significant DC voltage does not build up across the PZT element. The switches can be turned on/off via a serial, 32-bit shift register. The serial control interface minimizes the number of input/output (I/O) connections.

The EV4835A-RD-03A has two discrete, high-voltage transmitters (TX1 and TX2) to help evaluate the performance of the MP4835A. TX1 is configured as 1:2 and TX2 is configured as 1:3, which are the two most common configurations.

The EV4835A-RD-03A is designed to work with MPS's ultrasound controller board, the EV-EVK-RD-01A. A graphical user interface (GUI) is provided to easily control the analog switch states, transmitters (TX1 and TX2), pulse widths, and pulse cycles.

PERFORMANCE SUMMARY

Specifications are at T_A = 25°C, unless otherwise noted.

Parameters	Conditions	Value
Positive input transmit voltage (+HV) range		0V to +90V
Negative input transmit voltage (-HV) range		0V to -90V
Logic voltage (V_{LL})	Onboard linear regulator output	3.3V
Internal translator (V_{DD})	Onboard linear regulator output	5V
Typical switch peak current (I_{SWPK})		±1.8A
Typical switch resistance		14Ω

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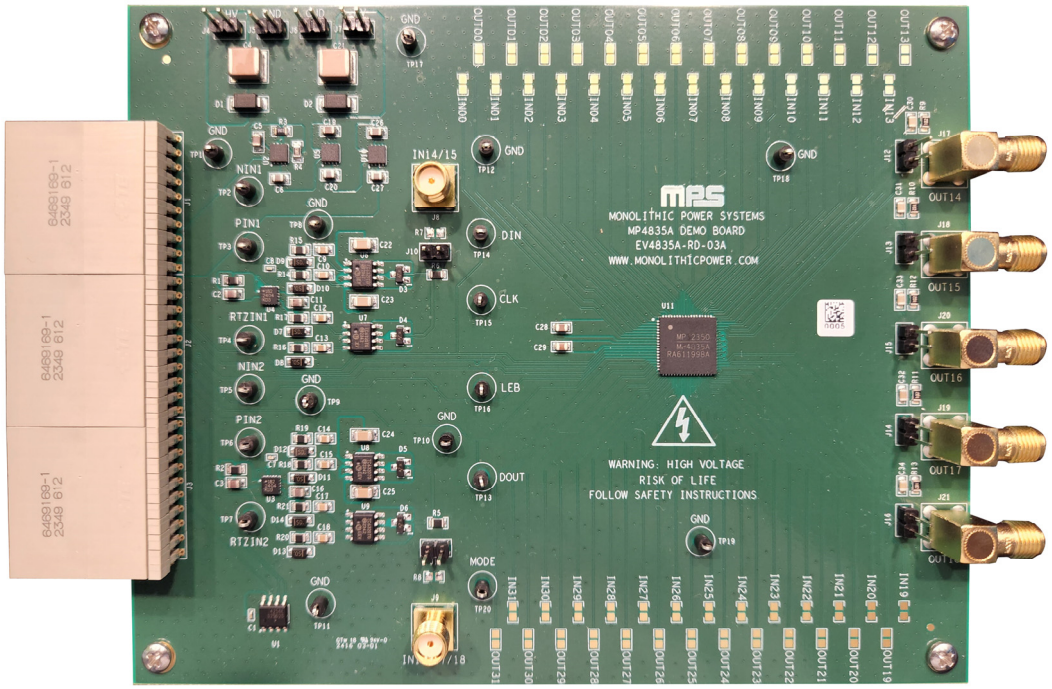
EV4835A-RD-03A EVALUATION BOARD



LxWxH (15cmx12cmx1cm)

Board Number	MPS IC Number
EV4835A-RD-03A	MP4835AGRD

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QUICK START GUIDE

The MP4835A's analog switch evaluation board (EV4835A-BD-03A) is designed to operate with MPS's ultrasound controller board (EV-EVK-RD-01A), enabling the evaluation of the MP4835A's 32-channel analog switch through MPS's Ultrasound Test Bench software (see Figure 1).

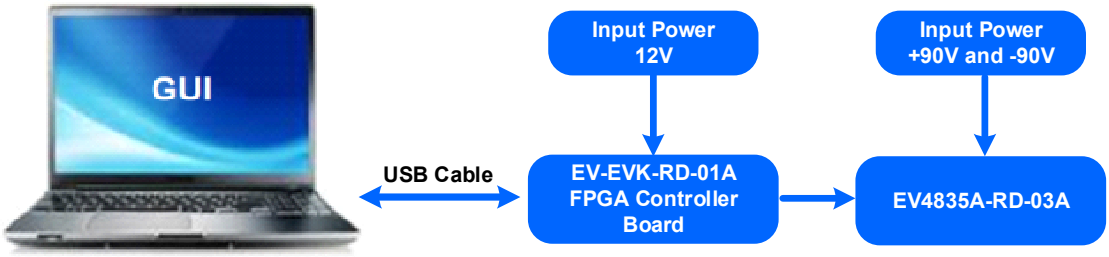

Figure 1: The Connection Block Diagram

For detailed information on the EV-EVK-RD-01A and instructions to install the MPS Ultrasound Test Bench software, refer to the EV-EVK-RD-01A's user manual. This guide provides step-by-step instructions for both the EV-EVK-RD-01A and the EV4835A-BD-03A boards, along with an overview of the software's graphical user interface (GUI).

The MPS Ultrasound Test Bench installer can be downloaded from the MPS website at www.monolithicpower.com.

Feature and Benefits

- 32-Channel, High-Voltage Analog Switch
- Designed for Direct Connection to MPS's Ultrasound Controller Board (EV-EVK-RD-01A)
- Includes Onboard 1:2 and 1:3 Mux Configurations, Driven by Onboard Integrated Discrete Pulser Circuits
- Provides Optional Dummy Loads on Outputs: SWOUT14, SWOUT15, SWOUT16, SWOUT17, and SWOUT18
- Supports Pulser On/Off Control, Pulse Frequency and Repetition Settings, and Mux Configurations via the PC-Based GUI in Coordination with MPS's Ultrasound Controller Board

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Hardware Specifications

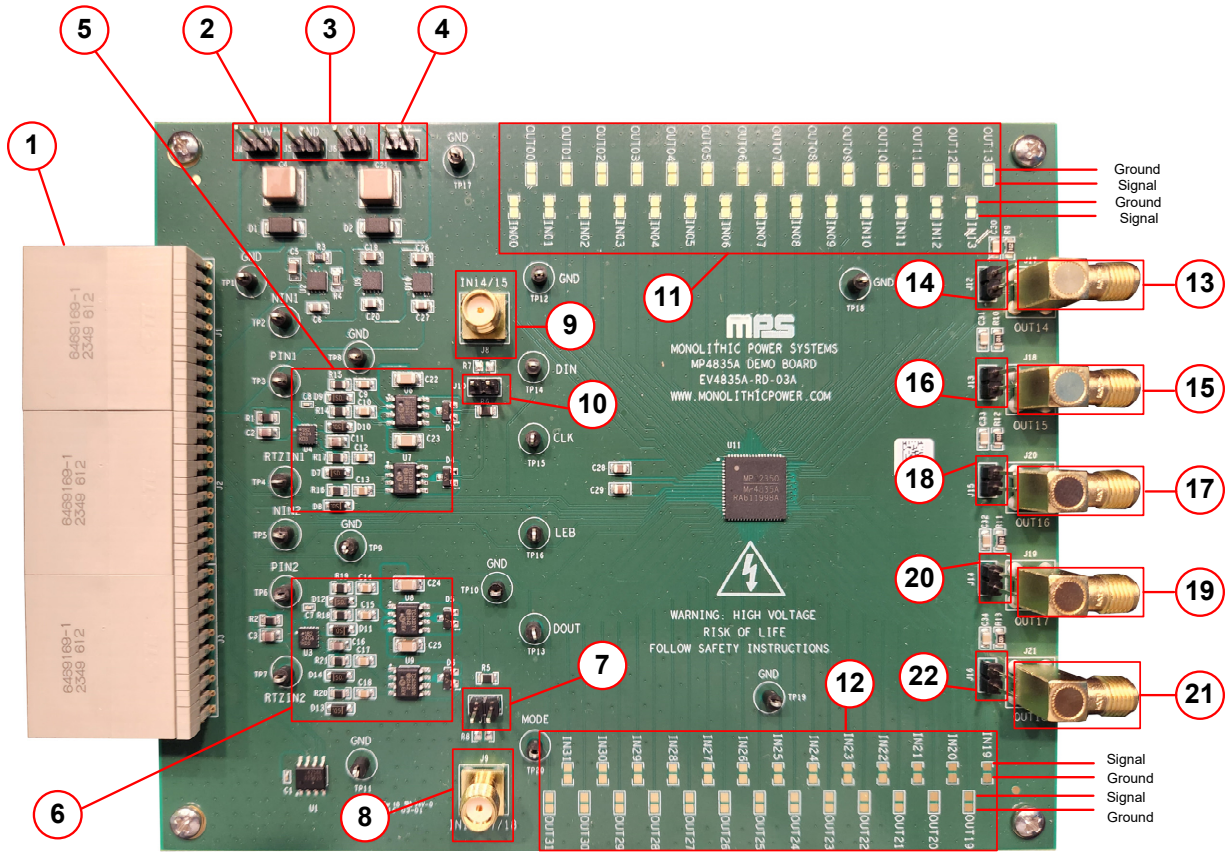

Figure 2: EV4835A-BD-03A Hardware

Table 1: EV4835A-BD-03A Hardware Description

#	Item Name	Item Description
1	J1, J2, J3	**Connector.** These high-speed connectors provide an interface to the EV-EVK-RD-01A controller board.
2	J4	**Jumper.** J4 is the connector for the +HV power supply to the onboard pulser circuit. Its voltage ranges between 0V to +90V at 40mA.
3	J5, J6	**Jumper.** Connect ground to J5 and J6.
4	J7	**Jumper.** J7 is the connector for the -HV power supply to the onboard pulser circuit. Its voltage ranges between 0V and -90V at 40mA.
5	TX1	**Pulser circuit.** TX1 is a discrete pulser circuit connected to the MP4835A's inputs (SWIN14 and SWIN15) via J10.
6	TX2	**Pulser circuit.** TX2 is a discrete pulser circuit connected to the MP4835A's inputs (SWIN16, SWIN17, and SWIN18) via J11.
7	J11	**Jumper.** J11 is a shunt to connect the TX2 output to the MP4835A's SWIN16, SWIN17, and SWIN18.
8	J9	**SMA connector.** J9 is the SMA connected to the MP4835A's SWIN16, SWIN17, and SWIN18.
9	J8	**SMA connector.** J8 is the SMA connected to the MP4835A's SWIN14 and SWIN15.
10	J10	**Jumper.** J10 is the shunt to connect TX1's output to the MP4835A's SWIN14 and SWIN15.

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Table 1: EV4835A-BD-03A Hardware Description *(continued)*

#	Item Name	Item Description
11	Top-Side Test Pads	**Top-side test pads.** The top-side test pads are 0805-format, surface-mount footprints designed to provide direct access to all input and output signals of the MP4835A. These pads enable users to customize their testing set-up by soldering wires, or probes, as needed for evaluation or signal routing. Each pad pair consists of a signal and a ground connection. Silkscreen marking indicates the signal type: I# denotes input signals to the MP4835A (e.g. I29 corresponds to SWIN29), and O# denotes output signals (e.g. O29 corresponds to SWOUT29). Figure 2 on page 4 shows which side of each pad is connected to signal and which is connected to ground.
12	Bottom-Side Test Pads	**Bottom-side test pads.** Additional 0805 test pads are located on the bottom side of the board for extended probing and customization
13	J17	**Connector.** Connected to the MP4835A's SWOUT14.
14	J12	**Jumper.** J12 is the shunt to connect the jumper to the dummy load (1kΩ in parallel with 330pF) to SWOUT14.
15	J18	**Connector.** Connected to the MP4835A's SWOUT15.
16	J13	**Jumper.** J13 is the shunt to connect the jumper to the dummy load (1kΩ in parallel with 330pF) to SWOUT15.
17	J19	**Connector.** Connected to the MP4835A's SWOUT16.
18	J14	**Jumper.** J14 is the shunt to connect the jumper to the dummy load (1kΩ in parallel with 330pF) to SWOUT16.
19	J20	**Connector.** Connected to the MP4835A's SWOUT17.
20	J15	**Jumper.** J15 is the shunt to connect the jumper to the dummy load (1kΩ in parallel with 330pF) to SWOUT17.
21	J21	**Connector.** Connected to the MP4835A's SWOUT18.
22	J16	**Jumper.** J16 is the shunt to connect the jumper to the dummy load (1kΩ in parallel with 330pF) to SWOUT18.

Evaluation Board Set-Up

1. Ensure that MPS's Ultrasound Test Bench GUI is installed on your PC. If it is not installed, refer to the User Manual for the MPS Ultrasound Controller Board (EV-EVK-RD-01A) for guidance.
2. Connect the EV4835A-BD-03A to the EV-EVK-RD-01A using the connectors J1, J2, and J3 (see Figure 3).

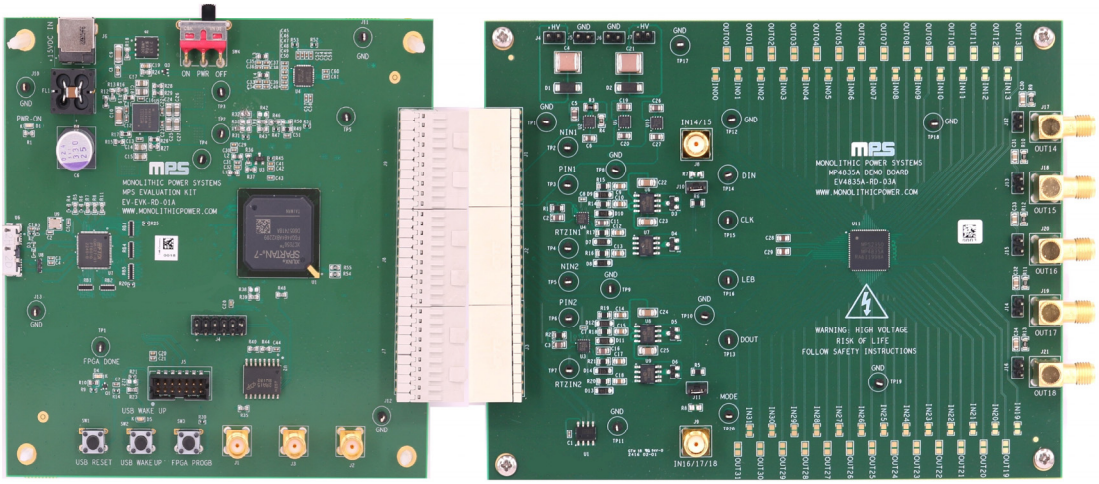

Figure 3: EV-EVK-RD-01A Attached to the Evaluation Board

3. Connect the +HV power supply to J4. The voltage range is between 0V and +90V.

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4. Connect the -HV power supply to J7. The voltage range is between 0V and -90V.
5. Connect the power supply ground to J5 and J6.
6. Plug the provided power adapter into the EV-EVK-RD-01A's power connector and slide SW4 to the "on" position.
   - The power-on LED (D1) should light up blue, indicating that there is good power.
   - The USB wake-up LED (D5) should light up green to confirm the USB connection.
   - The FPGA done LED (D4) should light up green to indicate that the firmware was loaded successfully.
7. Open the MPS Ultrasound Test Bench GUI. Select the MP4835A from the device list (see Figure 4).

Figure 4: Ultrasound Test Bench GUI Software (Device Selection Screen)

8. The EV-EVK-RD-01A may contain default firmware that is not compatible with the EV4835A-BD-03A. Upon the first launch, the GUI may prompt for a firmware update (see Figure 5). In this scenario, click "Confirm" to proceed. The update may take 1 minute to 2 minutes.

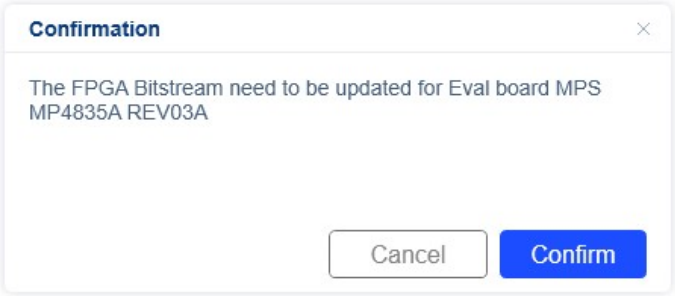

Figure 5: Firmware Update Confirmation

9. After the firmware update:
   a. Slide SW4 to the off position and wait for 5 seconds.
   b. Slide SW4 back to on.
   c. Click the "Restart button" in the GUI to reload the software.
10. Once the GUI restarts, click the MP4835A icon to begin using the GUI.

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Overview of MPS's Ultrasound Test Bench Software Functionality

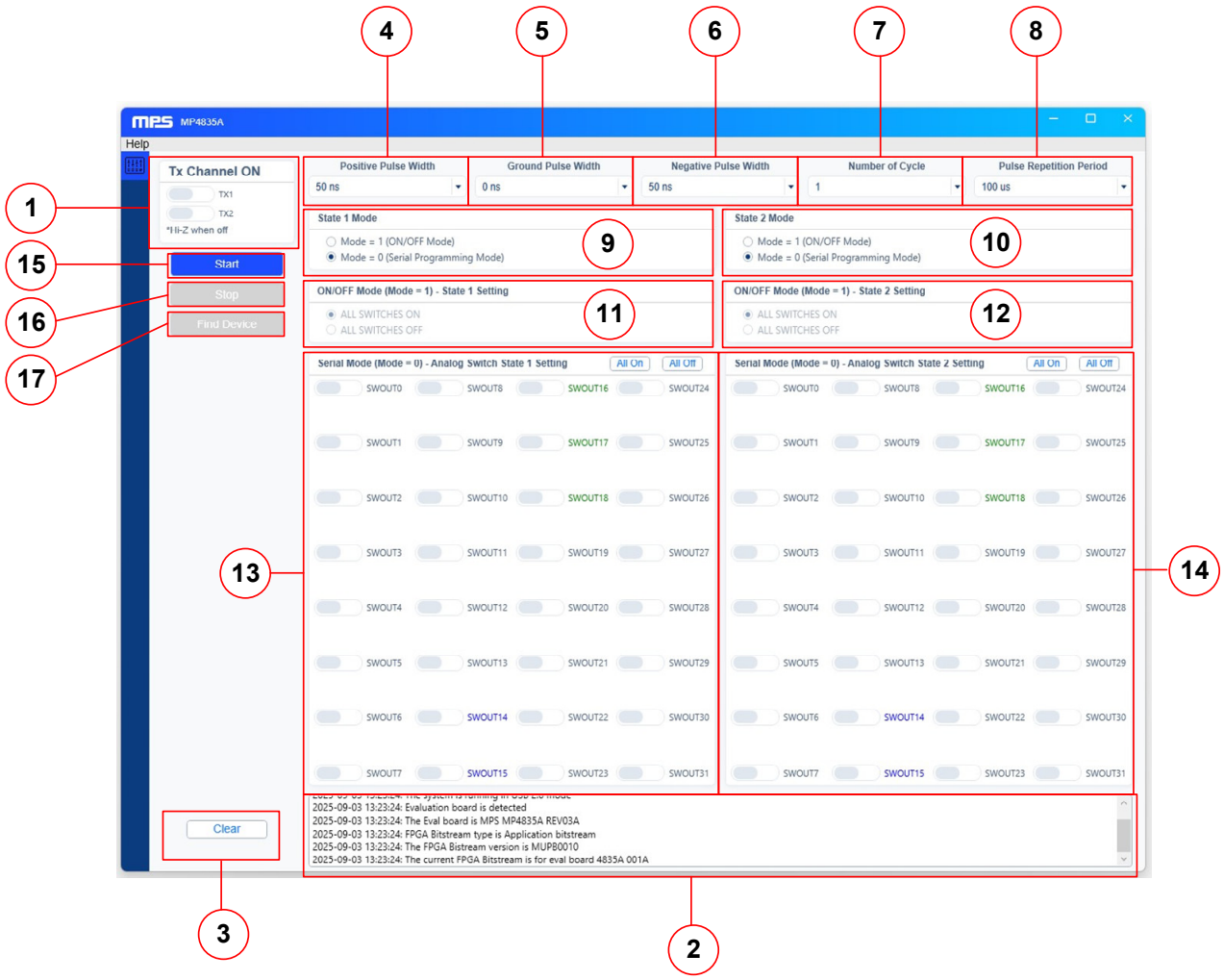

Figure 6: The EV4835A-BD-03A's GUI

Table 2: Description of EV4835A-BD-03A's GUI

#	Item Name	Item Description
1	TX Channel On/Off Control	Toggles the pulser circuits (TX1 and TX2). Click the slide switch to enable or disable each channel. A green switch indicates the channel is active; otherwise, the channel is off and held at a high-impedance (Hi-Z) state. • TX1 is connected to SWIN14 and SWIN15. • TX2 is connected to SWIN16, SWIN17, and SWIN18 on the EV4835A-BD-03A.
2	Status Window	Displays real-time status messages from the GUI. Each message has a timestamp and includes details such as the USB mode (e.g. USB 2.0 or 3.0).
3	Clear Status Window Button	Clears all messages from the status window.
4	Positive Pulse Width Menu	Selects the positive pulse width for the pulser circuits, which ranges between 50ns and 425ns. The pulses are transmitted in the following order: positive > ground > negative > ground.
5	Ground Pulse Width Menu	Selects the ground pulse width, which ranges between 0ns and 400ns.
6	Negative Pulse Width Menu	Selects the negative pulse width, which ranges between 50ns to 425ns.

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Table 2: Description of EV4835A-BD-03A's GUI *(continued)*

#	Item Name	Item Description
7	Number of Cycle Menu	Sets the number of pulse repetitions (positive > ground > negative > ground), which ranges between 1 and 8 cycles.
8	Pulse Repetition Period Menu	Defines the time interval between two states in a 2-state sequence. The sequence alternates between State 1 and State 2 continuously
9	State 1 Mode	The MP4835A includes a MODE pin that determines how the switches are controlled. • When Mode = 1, the MP4835A's MODE pin turns all switches on or off simultaneously, bypassing the serial interface. In this mode, the State 1 on/off mode window is enabled, and the State 1 mux control group is disabled. • When Mode = 0, switches are controlled individually via serial commands. The State 1 mux control group becomes active, and the on/off mode window is disabled.
10	State 2 Mode	Functions identically to State 1 mode but applies to State 2. The selected mode determines whether switches are controlled collectively via the MODE pin or individually via serial control.
11	State 1 On/Off Mode	Available when State 1 Mode = 1. Use the radio buttons to turn all switches on or off for State 1 using the MODE pin's logic level.
12	State 2 On/Off Mode	Available when State 2 Mode = 1. Use the radio buttons to turn all switches on or off for State 2 using the MODE pin's logic level.
13	State 1 Mux Control Group	Enabled when State 1 Mode = 0. Provides individual switch control for State 1 via the serial interface. • Use the slide buttons to toggle each switch (where green = on). • Use the "All On" or "All Off" buttons for bulk control.
14	State 2 Mux Control Group	Enabled when State 2 Mode = 0. Provides individual switch control for State 2 using the same interface as State 1.
11	Start Button	Initiates the pulse sequence.
12	Stop Button	Stop the pulse sequence.
13	Find Device Button	Establishes connection with the controller board. Once connected, the button becomes greyed out.

The MPS Ultrasound Test Bench Software provides comprehensive control of the EV4835A-BD-03A, which features two high-voltage pulser circuits (TX1 and TX2) capable of transmitting pulses up to ±90V.

Using the GUI, users can precisely configure the shape and timing of each transmission pulse. A single transmit cycle consists of three sequential components:

- Positive pulse
- Ground pulse
- Negative pulse

The sequence goes from positive > ground > negative > ground.

Each pulse width is individually adjustable via the GUI, and the number of pulse repetitions can be set between 1 and 8 cycles. When enabled, the configured pulse sequence is applied to both TX1 and TX2. If either channel is disabled, its output remains in a high-impedance (Hi-Z) state, regardless of the configured parameters.

J10 and J11 must be installed to connect the pulser circuit outputs (TX1 and TX2) to their corresponding SWIN inputs on the MP4835A. Without these jumpers, the outputs remain disconnected and cannot deliver signals to the MP4835A.

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The software supports two-state sequencing, allowing dynamic control of switch configurations across alternating states:

- State 1 begins with a serial control signal that configures the MP4835A using the switch settings defined in the State 1 mux control group. Once the mux is configured, the transmit pulse is initiated.
- After the defined pulse repetition period elapses, the system transitions to State 2, which uses the switch settings from the State 2 mux control group.
- Upon completing State 2's pulse repetition period, the sequence loops back to State 1, continuing in a cyclical pattern.

The pulse repetition period determines the time interval between each state transition and can be configured via the GUI.

Test Bench Examples

The examples below show the functionality of two-state sequencing and pulse waveform generation using MPS's Ultrasound Test Bench Software with the EV4835A-BD-03A. The step-by-step set-up instructions are below.

1. Follow the steps outlined in the Evaluation Board Set-Up section on page 5 to set up the EV4835A-BD-03A evaluation board.
2. Start the MPS Ultrasound Test Bench GUI on your PC.
3. Ensure Jumpers J10 and J11 are installed on the EV4835A-BD-03A board to connect the pulser circuit outputs (TX1 and TX2) to their respective SWIN inputs.
4. Configure the GUI settings (see Figure 7).

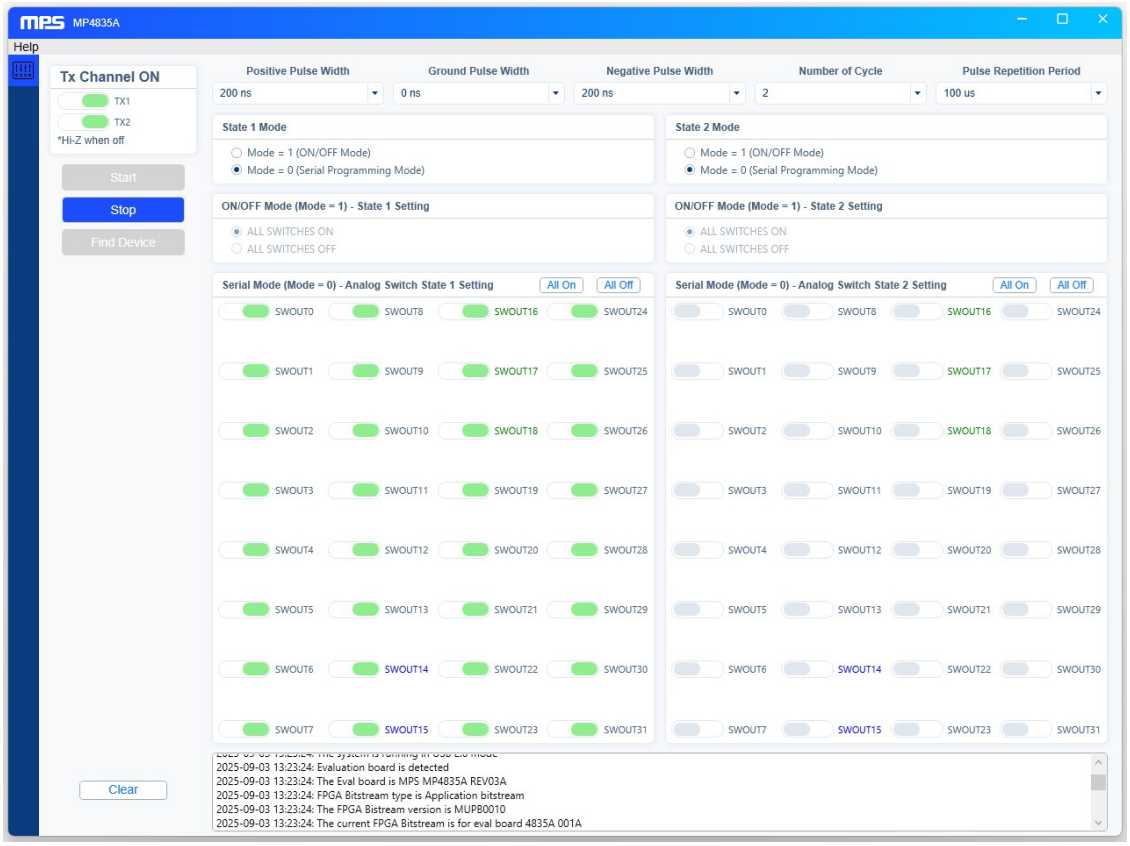

Figure 7: Test Bench Example 1

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The design guidelines for this example are listed below.

- TX Channel ON: Enable both TX1 and TX2
- Positive Pulse Width: 200ns
- Ground Pulse Width: 0ns
- Negative Pulse Width: 200ns
- Number of Cycle: 2
- Pulse Repetition Period: 100µs
- State 1 Mode: 0
- State 1 Mux Control Group: Set to All On
- State 2 Mux Control Group: Set to All Off

5. Click the Start button in the GUI to begin the pulse sequence.

Figure 8 shows the oscilloscope capture of TX1 and SWOUT14, which is based on the configurations shown in Figure 7 on page 9.

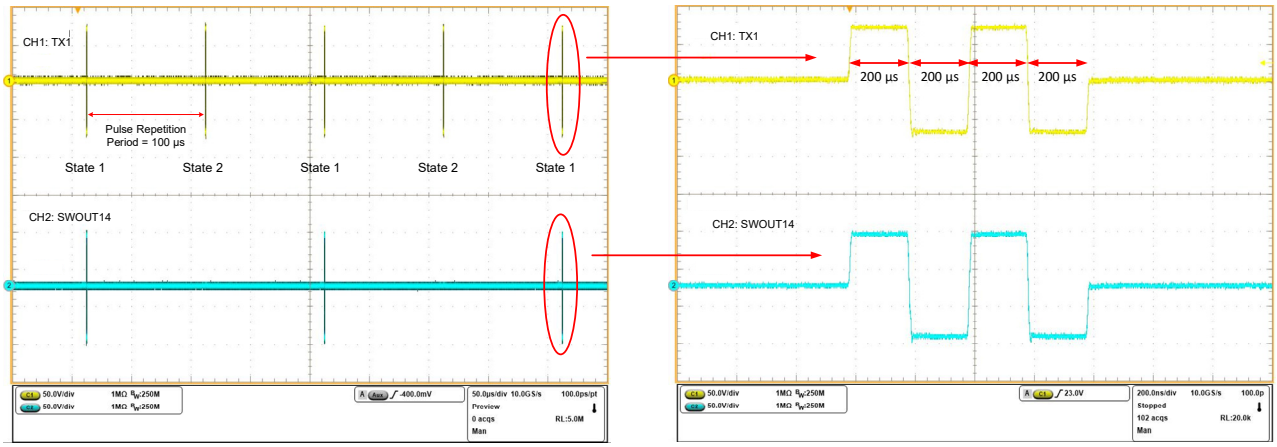

Figure 8: TX1 and SWOUT14

- In State 1, the State 1 mux control group is set to “All On.” This allows the TX1 signal to propagate through the switch and be observed at SWOUT14.
- In State 2, the State 2 mux control group is set to “All Off.” As a result, the TX1 output is isolated from SWOUT14, and no signal is present at the output during this state.
- The oscilloscope trace confirms a pulse repetition period of 100µs, as configured via the GUI. This is evident from the time interval between successive TX1 transmit pulses.
- The zoomed-in waveform in Figure 8 confirms the configured pulse shape. Since the ground pulse width is set to 0ns, each cycle consists of only a positive pulse followed directly by a negative pulse, omitting the ground phase. This sequence repeats for 2 cycles, as configured via the GUI.

Figure 9 on page 11 shows an alternative pulse configuration using MPS’s Ultrasound Test Bench Software. In this set-up, the transmit pulse is configured with the following parameters:

- Positive Pulse Width: 100ns
- Ground Pulse Width: 50ns
- Negative Pulse Width: 100ns

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- Number of Cycles: 5

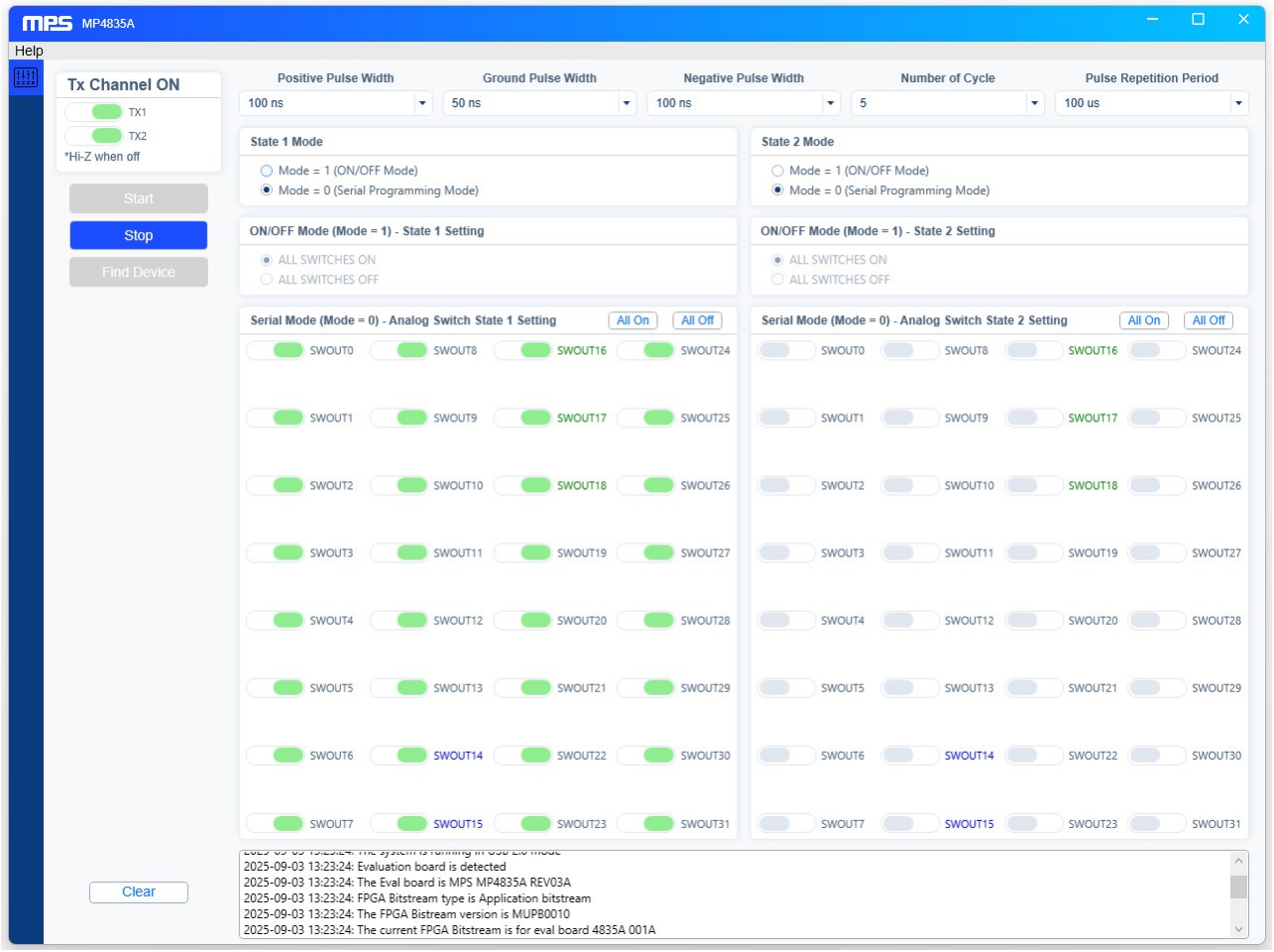

Figure 9: Test Bench Example 2

Figure 10 on page 12 shows the oscilloscope capture corresponding to the pulse configuration shown in Figure 9. The waveform confirms that the configured pulse widths are correctly applied, and the signal sequence follows the expected pattern of positive > ground > negative > ground. This sequence repeats for 5 cycles.

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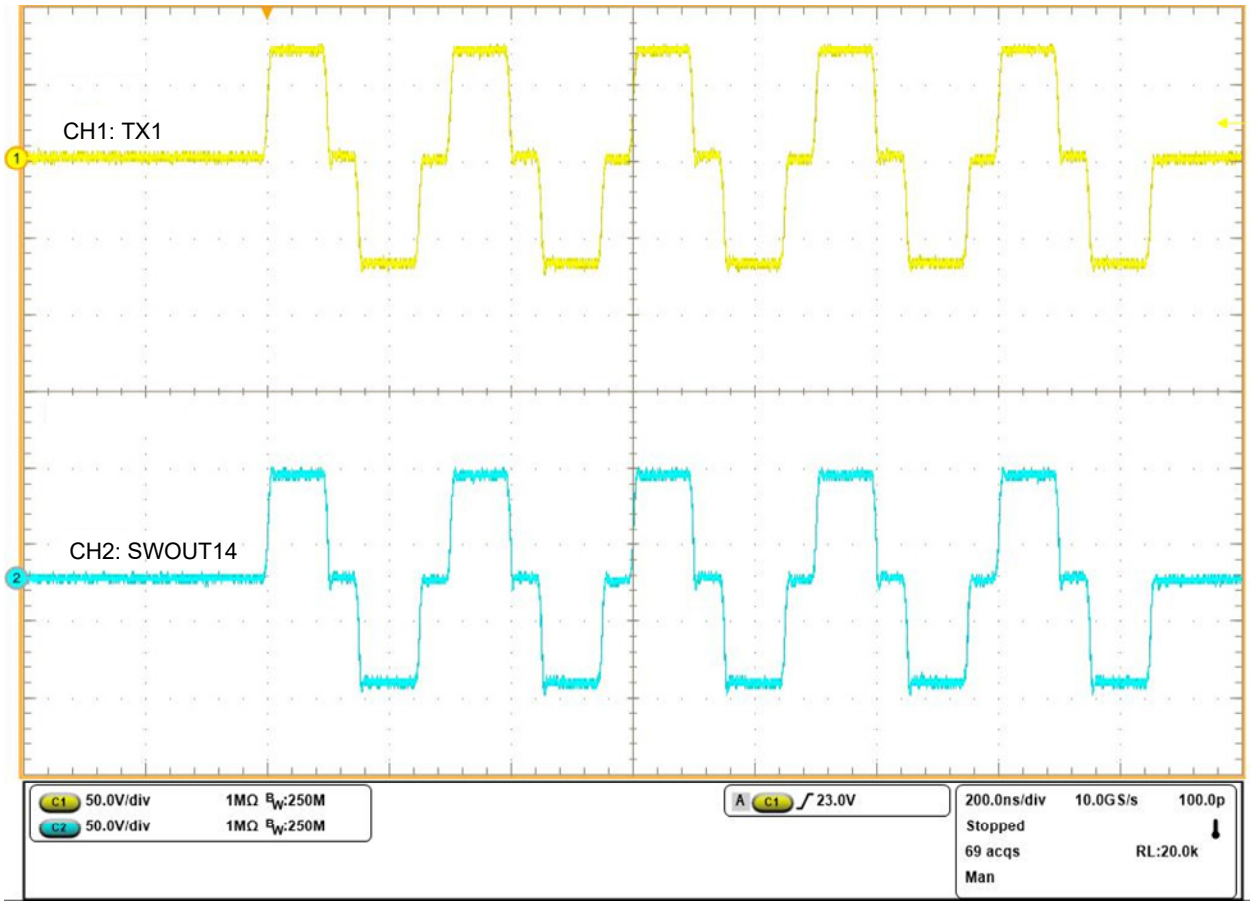

Figure 10: Test Bench Example 2 Waveform

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EVALUATION BOARD SCHEMATICS

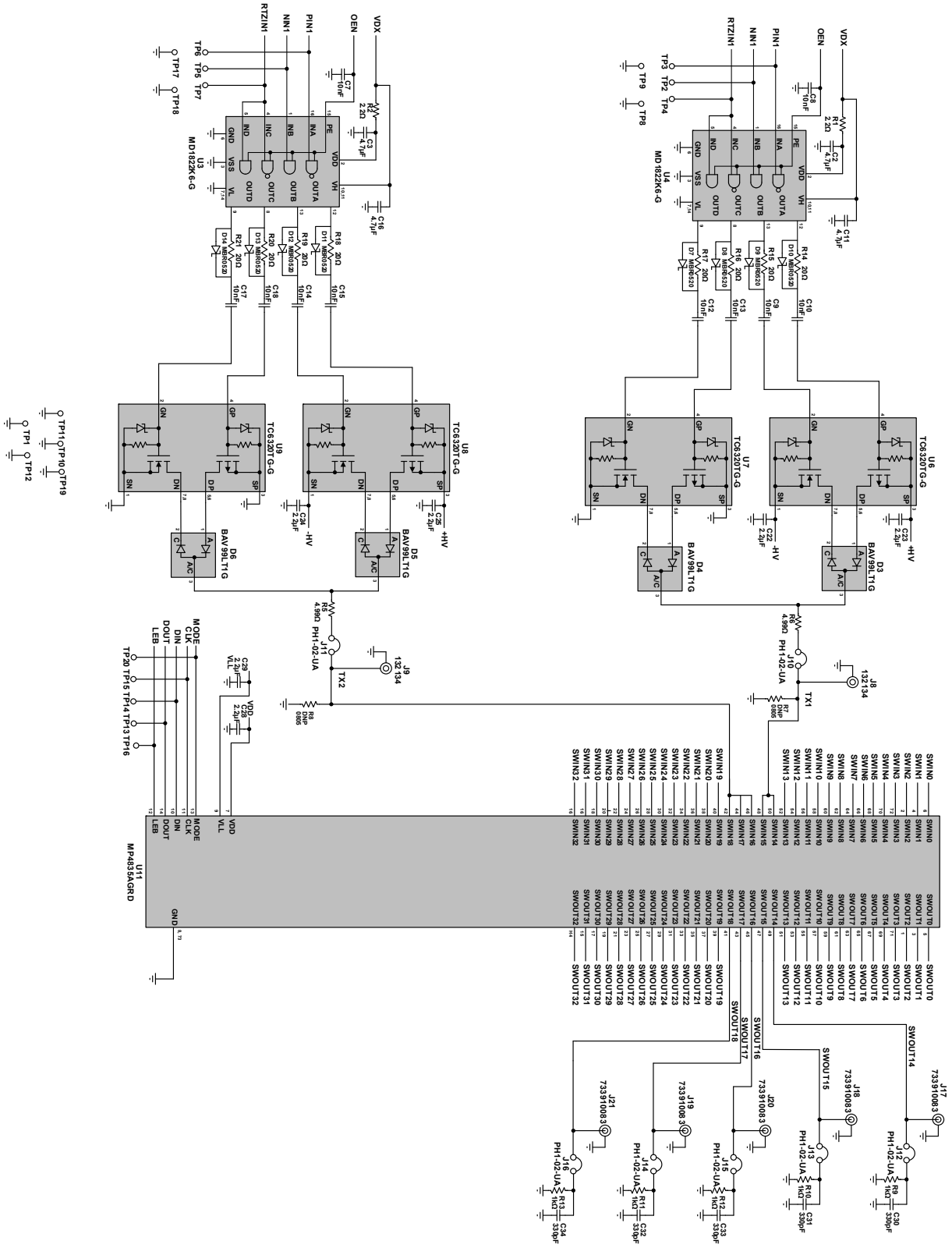

Figure 11: Evaluation Board Schematic (MP4835A and Transmitter)

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EVALUATION BOARD SCHEMATICS *(continued)*

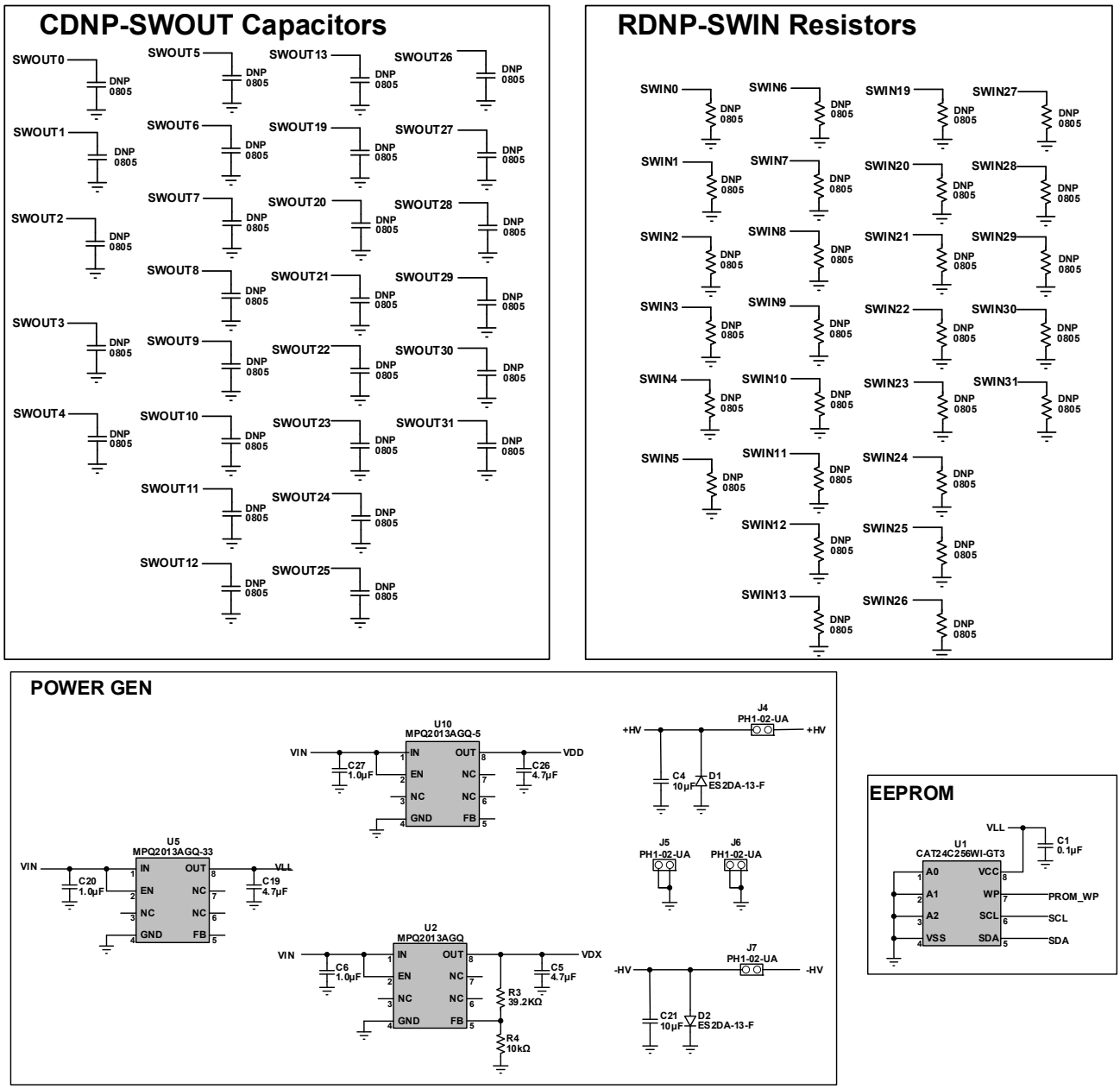

Figure 12: Evaluation Board Schematic (Power, EEPROM, and Resistors)

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EVALUATION BOARD SCHEMATICS *(continued)*

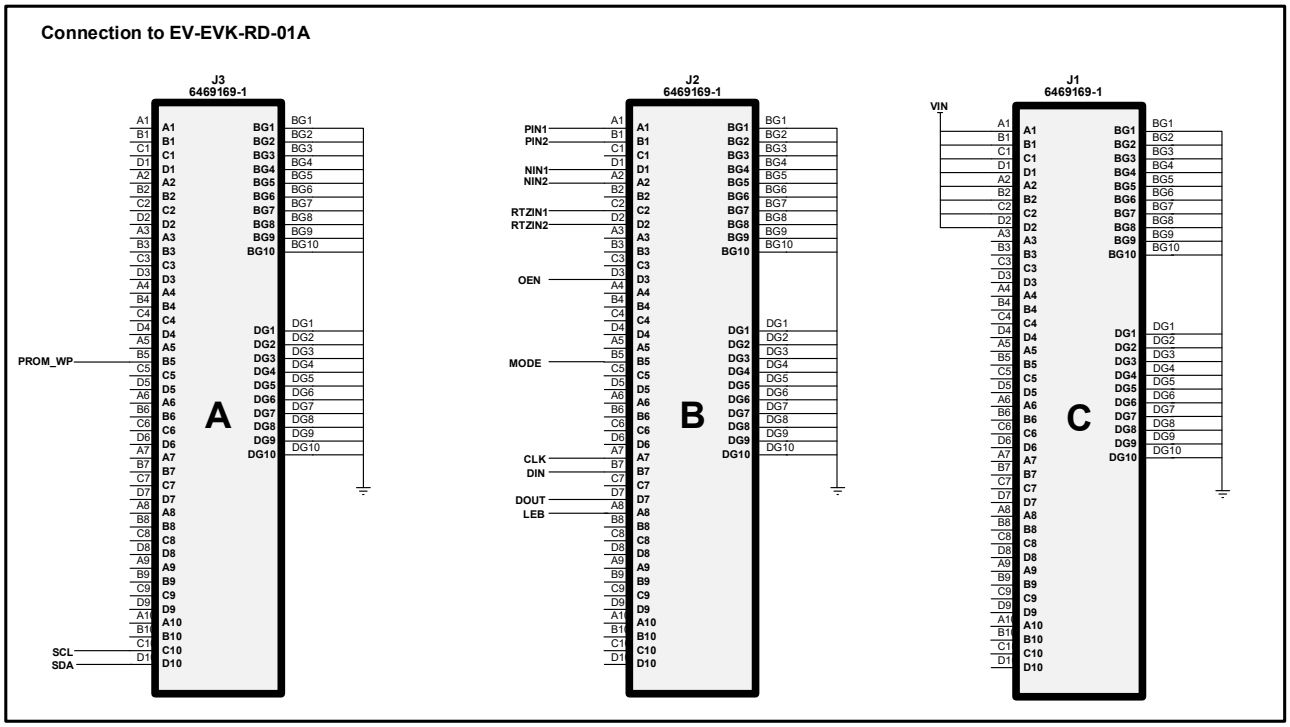
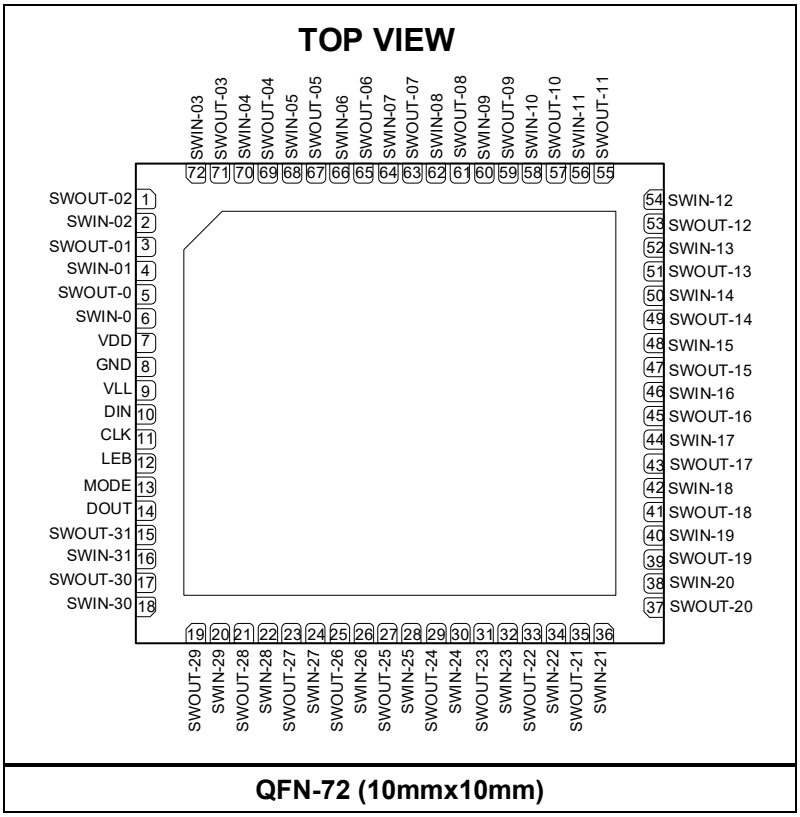


Figure 13: Evaluation Board Schematic (Connectors)

PACKAGE REFERENCE



QFN-72 (10mmx10mm)

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EV4835A-RD-03A BILL OF MATERIALS

Qty	Ref	Value	Description	Package	Manufacturer	Manufacturer PN
1	C1	0.1µF ±10%	10V, ceramic capacitor, X5R (1005 Metric)	0402	Samsung Electro-Mechanics	CL05A104KP5NNNC
7	C2, C3, C5, C11, C16, C19, C26	4.7µF ±10%	16V, ceramic capacitor, X7R (2012 metric)	0805	Kemet	C0805C475K4RAC7210
2	C4, C21	10µF ±20%	100V, ceramic capacitor, X7R (5750 metric)	2220	KYOCERA AVX	22201C106MAT2A
3	C6, C20, C27	1µF ±20%	25V ceramic capacitor, X7R (2012 metric)	0805	Kemet	C0805C105M3RAC7800
2	C7, C8	10nF ±10%	6.3V, ceramic capacitor, X7R (1005 metric)	0402	Kemet	C0402C103K9RACAUTO
8	C9, C10, C12, C13, C14, C15, C17, C18	10nF ±10%	200V, ceramic capacitor, X7R (2012 metric)	0805	Kemet	C0805C103K2RECAUTO
4	C22, C23, C24, C25	2.2µF ±10%	100V, ceramic capacitor, X7R (3216 metric)	1206	Murata Electronics	12061C225K4T2A
2	C28, C29	2.2µF ±10%	16V, ceramic capacitor X5R (2012 metric)	0805	TDK	C2012X5R1C225K125AA
5	C30, C31, C32, C33, C34	330pF ±10%	100V, ceramic capacitor, X7R (2012 metric)	0805	Yageo	CC0805KRX7R0BB331
2	D1, D2	200V, 2A	Diode, 200V, 2A, surface mount SMA	DO-214AC, SMA	Diodes, Inc.	ES2DA-13-F
4	D3, D4, D5, D6	215mA	Diode array, 100V, 215mA	SOT23	onsemi	BAV99LT1G
8	D7, D8, D9, D10, D11, D12, D13, D14	20V, 500mA	Schottky diode, 20V 500mA	SOD123	Micro Commercial Co	MBR0520L-TP
3	J1, J2, J3	60 positions	60 connector header, male pins and blades board edge, through-hole, right angle high speed	Through-hole	TE Connectivity AMP Connectors	6469169-1
11	J4, J5, J6, J7, J10, J11, J12, J13, J14, J15, J16	2 positions	Connector header through-hole, 2 position 0.100" (2.54mm)	Through-hole	Adam Tech	PH1-02-UA

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EV4835A-RD-03A BILL OF MATERIALS *(continued)*

Qty	Ref	Value	Description	Package	Manufacturer	Manufacturer PN
2	J8, J9	50Ω	SMA connector jack, female socket, 50Ω, through-hole solder	Through-hole	Amphenol RF	132134
5	J17, J18, J19, J20, J21	50Ω	SMA connector jack, female socket, 50Ω, through-hole, right angle solder	Through-hole	Molex	733910083
2	R1, R2	2.2Ω ±5%	0.125W, chip resistor (2012 metric), automotive AEC-Q200, thick film	0805	Vishay Dale	CRCW08052R20JNEA
1	R3	39.2kΩ ±1%	0.125W, chip resistor (2012 metric), automotive AEC-, thick film	0805	Stackpole Electronics Inc	RMCF0805FT39K2
1	R4	10kΩ ±1%	0.125W, chip resistor (2012 metric) automotive AEC-Q200, thick film	0805	Stackpole Electronics Inc	RMCF0805FT10K0
2	R5, R6	4.99Ω ±1%	0.5W, chip resistor (2012 metric), automotive AEC-Q200, pulse withstanding thick film	0805	Vishay Dale	RCS08054R99FKEA
5	R9, R10, R11, R12, R13	1kΩ ±5%	0.125W, chip resistor 0805 (2012 metric), anti-sulfur thick film	0805	Stackpole Electronics	RMCS0805JT1K00
8	R14, R15, R16, R17, R18, R19, R20, R21	20Ω ±1%	0.5W, chip resistor (2012 metric), automotive AEC-Q200, pulse withstanding thick film	0805	Vishay Dale	CRCW080520R0FKEAHP
20	TP1, TP2, TP3, TP4, TP5, TP6, TP7, TP8, TP9, TP10, TP11, TP12, TP13, TP14, TP15, TP16, TP17, TP18, TP19, TP20	1 position	Black PC test point, miniature phosphor bronze silver plating through hole mounting type	Through-hole	Keystone Electronics	5001
1	U1	256kbit	EEPROM memory IC, I^2C, 1MHz, 500ns	SOIC-8	onsemi	CAT24C256WI-GT3
2	U3, U4	3mmx3mm	Half-bridge gate driver IC, inverting/ non-inverting	QFN-16 (3mmx 3mm)	Microchip Technology	MD1822K6-G

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EV4835A-RD-03A BILL OF MATERIALS *(continued)*

Qty	Ref	Value	Description	Package	Manufacturer	Manufacturer PN
4	U6, U7, U8, U9	200V	MOSFET array, 200V, surface mount	SOIC-8-LG	Microchip Technology	TC6320TG-G
1	U2	MPQ2013A	Linear voltage regulator IC, positive adjustable 1 output, 150mA	QFN-8 (3mmx 3mm)	MPS	MPQ2013AGQ
1	U5	MPQ2013A-33	Linear voltage regulator IC, positive fixed 1 output, 150mA	QFN-8 (3mmx 3mm)	MPS	MPQ2013AGQ-33
1	U10	MPQ2013A	Linear voltage regulator IC, positive fixed 1 output, 150mA	QFN-8 (3mmx 3mm)	MPS	MPQ2013AGQ-5
1	U11	MP4835A	32-channel, high- voltage analog switch	QFN-72 (10mmx 10mm)	MPS	MP4835AGRD

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PCB LAYOUT (1)

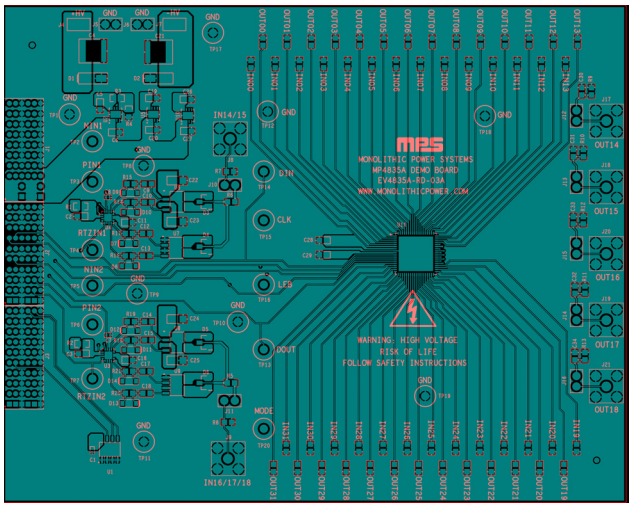

Figure 14: Top Silk and Top Layer

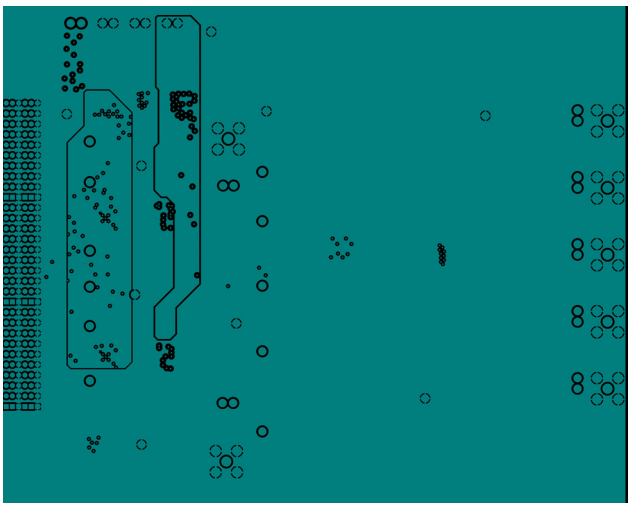

Figure 15: Mid-Layer 2

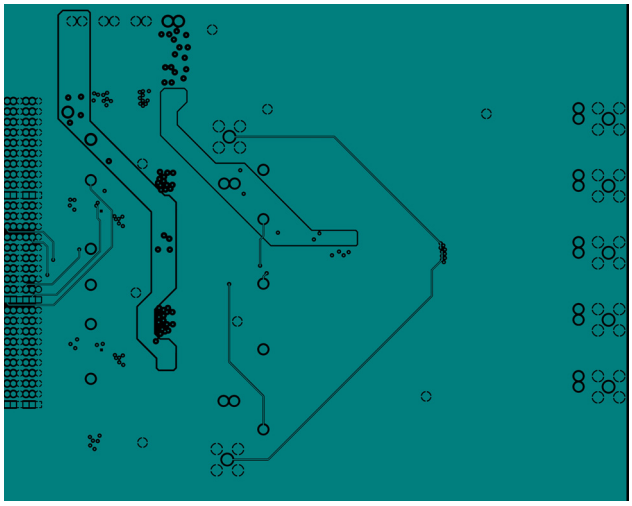

Figure 16: Mid-Layer 3

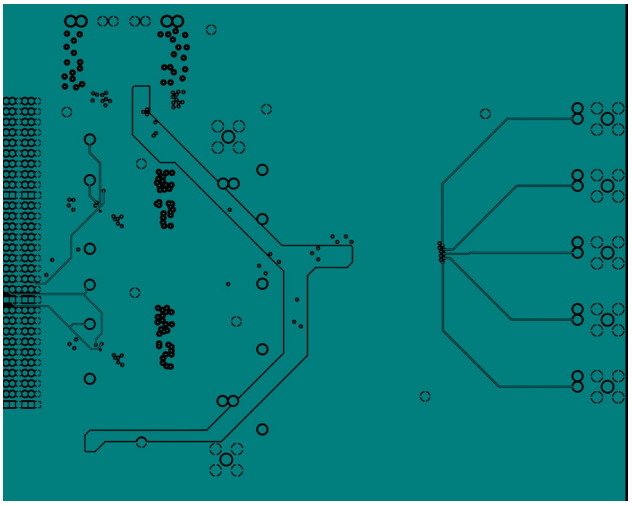

Figure 17: Bottom Layer

Note:

1) The copper thickness is 0.5 oz.

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REVISION HISTORY

Revision #	Revision Date	Description	Pages Updated
1.0	1/26/2026	Initial Release	-

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