FEATURES

- Fully Compatible with USB-Type C and USB PD Specifications and Regulations
- Each Source Only Port Supports USB PD 3.0 (PPS):
  - 5V/9V/15V/20V at 3A
  - 3.3V to 16V at 3A
  - 3.3V to 21V at 3A
- DP/DM Pin Supports BC1.2 DCP, Apple Divider Mode, QC3.0, and AFC
- Selectable Switching Frequency: 250kHz, 350kHz, or 420kHz
- Optional Short-to-Battery Protection on DM, DP, CC Lines, or USB GNDs
- Optional Power-Sharing between Two Ports
- Optional VCONN Support Up to 1.5W
- Optional VIN Monitoring
- Input Voltage Reverse-Polarity Protection
- AEC-Q100 Grade Compliant

ORDERING INFORMATION

<table>
<thead>
<tr>
<th>Part Number</th>
<th>mEZS91603A</th>
</tr>
</thead>
</table>

Note:

1) For IC and bill of materials (BOM) information, contact MPS.

ELECTRICAL CHARACTERISTICS

<table>
<thead>
<tr>
<th>Input Voltage</th>
<th>9V to 16V</th>
</tr>
</thead>
<tbody>
<tr>
<td>PDO List</td>
<td>VOUT (V)</td>
</tr>
<tr>
<td>1</td>
<td>5</td>
</tr>
<tr>
<td>2</td>
<td>9</td>
</tr>
<tr>
<td>3</td>
<td>15</td>
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<tr>
<td>4</td>
<td>20</td>
</tr>
<tr>
<td>5</td>
<td>3.3 to 16</td>
</tr>
<tr>
<td>6</td>
<td>3.3 to 21</td>
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</tbody>
</table>
SYSTEM BLOCK DIAGRAM

Battery Input
9V to 16V

Type-C Output
3.3V to 21V/3A
USBPD3.0 PPS
BC1.2/Divider
QC3.0, AFC

Type-C Output
3.3V to 21V/3A
USBPD3.0 PPS
BC1.2/Divider
QC3.0, AFC
PIN DIAGRAM

- **GND**
- **VIN**
- **SWD_IO1**
- **SWD_CLK1**
- **NC**
- **GND**
- **VCONN1**
- **Type-C1**
- **CH2_SCL**
- **CH2_SDA**
- **SWD_CLK2**
- **NC**
- **VCONN2**
- **SWD_IO2**
- **GND**

**Type-C2**
PRODUCT PACKAGE AND DIMENSIONS

(LxWxH): 8.9cm x 5.4cm x 1.8cm (2)

Note:
2) For more information, Gerber files, and PCB layout guidelines, contact mEZsupport@monolithispower.com.

QUICK START GUIDE
1. Preset the power supply output to 12V, and then turn off the power supply.
2. Connect power supply output to:
   a) Positive (+): VIN
   b) Negative (-): GND
3. Connect the PD protocol sniffer or mobile device to the USB Type-C port.
4. After power-on, the VBUS automatically starts up at the default voltage set by the PD protocol sniffer or mobile device. The PD protocol sniffer can select different PDO outputs.
5. For NTC functionality, the PDO list decreases to:
   a) 5V at 3A
   b) 9V at 3A
   c) 15V at 3A when the die temperature reaches 150°C (3)
6. Update the CCG3PA firmware using this board’s mini-programmer.

Note:
3) If the die temperature drops to 100°C, the PDO lists recover to normal state. If the die temperature reaches 180°C, the part shuts down, then recovers when the temperature falls to 120°C. There are another two optional NTC resistors that can monitor inductor or Type-C port temperatures. Contact Cypress for a firmware update.

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