DESCRIPTION

The MPM3805 is a step-down module converter with built-in power MOSFETs and inductor. The module’s integrated inductor simplifies the power system design and provides easy, efficient use. The DC-DC module comes in a small surface-mount QFN-12 (2.5mmx3.0mmx0.9mm) package and achieves 0.6A continuous output current from a 2.5V to 6V input voltage with excellent load and line regulation. The output voltage is regulated as low as 0.6V. For adjustable output, only FB resistors and input and output capacitors are needed to complete the design.

The Constant-on-time control (COT) scheme provides fast transient response and eases loop stabilization. Fault condition protection includes cycle-by-cycle current limiting and thermal shutdown (TSD).

The MPM3805 is ideal for a wide range of applications including high performance DSPs, FPGAs, PDAs, portable instruments and storage.

FEATURES

- Wide 2.5V to 6V Operating Input Range
- Fixed and Adjustable Output from 0.6V
- QFN-12 (2.5mmx3.0mmx0.9mm) Package
- Total Solution Size 6mm x 3.8mm
- Up to 0.6A Output Current
- 100% Duty Cycle in Dropout
- Ultra Low IQ: 17μA
- EN and Power Good for Power Sequencing
- Cycle-by-Cycle Over-Current Protection
- Short-Circuit Protection with Hiccup Mode
- Adjustable Output Only Needs 4 External Components: 2 Ceramic Capacitors and FB Divider Resistors
- Fixed Output only Needs Input and Output Capacitors

APPLICATIONS

- Low Voltage I/O System Power
- LDO Replacement
- Power for Portable Products
- Storage (SSD/HDD)
- Space-Limited Applications

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TYPICAL APPLICATION (Fixed Output)
ORDERING INFORMATION

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
<th>Top Marking</th>
<th>Vo Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>MPM3805GQB*</td>
<td>QFN-12</td>
<td>AGR</td>
<td>Adjustable</td>
</tr>
<tr>
<td>MPM3805GQB-12</td>
<td>QFN-12</td>
<td>AHE</td>
<td>Fixed 1.2V</td>
</tr>
<tr>
<td>MPM3805GQB-18</td>
<td>QFN-12</td>
<td>AHD</td>
<td>Fixed 1.8V</td>
</tr>
<tr>
<td>MPM3805GQB-25</td>
<td>QFN-12</td>
<td>AJK</td>
<td>Fixed 2.5V</td>
</tr>
<tr>
<td>MPM3805GQB-33</td>
<td>QFN-12</td>
<td>AJJ</td>
<td>Fixed 3.3V</td>
</tr>
</tbody>
</table>

* For Tape & Reel, add suffix –Z (e.g. MPM3805GQB–Z);

PACKAGING REFERENCE

ABSOLUTE MAXIMUM RATINGS (1)
Supplementary Voltage $V_{IN}$ ........................................... 6.5V
VSW ........................................................................ -0.3V (-5V for <10ns) to 6.5V (7V for <10ns)
Junction Temperature ......................................................... 150°C
Lead Temperature ............................................................. 260°C
Continuous Power Dissipation ($T_A = +25°C$) (2) .............................. 1.9W
Storage Temperature .................................................. -65°C to +150°C

Recommended Operating Conditions (3)
Supply Voltage $V_{IN}$ ............................................. 2.5V to 6V
Output Voltage $V_{OUT}$ ........................................ 12% x $V_{IN}$ to $V_{IN}$
Operating Junction Temp. ($T_J$) .......................... -40°C to +125°C

Thermal Resistance (4) $\theta_{JA}$ $\theta_{JC}$
QFN-12 (2.5mmX3.0mmX0.9mm) .................. 65 13 °C/W

Notes:
1) Exceeding these ratings may damage the device.
2) The maximum allowable power dissipation is a function of the maximum junction temperature $T_J$ (MAX), the junction-to-ambient thermal resistance $\theta_{JA}$, and the ambient temperature $T_A$. The maximum allowable continuous power dissipation at any ambient temperature is calculated by $P_D$ (MAX) = ($T_J$ (MAX) - $T_A$)/$\theta_{JA}$. Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown. Internal thermal shutdown circuitry protects the device from permanent damage.
3) The device is not guaranteed to function outside of its operating conditions.
4) Measured on JESD51-7, 4-layer PCB.
ELECTRICAL CHARACTERISTICS

$V_{IN} = 5V$, $T_J = -40°C$ to $+125°C$. Typical value is tested at $T_J = +25°C$. The limit over temperature is guaranteed by characterization, unless otherwise noted.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Condition</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Feedback Voltage (MPM3805GQB Only)</td>
<td>$V_{FB}$</td>
<td>$2.5V \leq V_{IN} \leq 6V, T_J=+25°C$</td>
<td>588</td>
<td>600</td>
<td>612</td>
<td>mV</td>
</tr>
<tr>
<td>Fixed Output Voltage</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>Only for MPM3805GQB-12, $I_{OUT}=10mA, T_J=+25°C$</td>
<td>1.17</td>
<td>1.2</td>
<td>1.23</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Only for MPM3805GQB-18, $I_{OUT}=10mA, T_J=+25°C$</td>
<td>1.755</td>
<td>1.8</td>
<td>1.845</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>Only for MPM3805GQB-25, $I_{OUT}=10mA, T_J=+25°C$</td>
<td>2.437</td>
<td>2.5</td>
<td>2.563</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>Only for MPM3805GQB-33, $I_{OUT}=10mA, T_J=+25°C$</td>
<td>3.217</td>
<td>3.3</td>
<td>3.383</td>
<td></td>
</tr>
<tr>
<td>Feedback Current</td>
<td>$I_{FB}$</td>
<td>$V_{FB} = 0.63V$, Only for MPM3805GQB</td>
<td>10</td>
<td></td>
<td></td>
<td>nA</td>
</tr>
<tr>
<td>PFET Switch-On Resistance</td>
<td>$R_{DSON_P}$</td>
<td></td>
<td>110</td>
<td></td>
<td></td>
<td>mΩ</td>
</tr>
<tr>
<td>NFET Switch-On Resistance</td>
<td>$R_{DSON_N}$</td>
<td></td>
<td>70</td>
<td></td>
<td></td>
<td>mΩ</td>
</tr>
<tr>
<td>Inductor L Value</td>
<td>L</td>
<td>Inductance value at 1MHz</td>
<td>0.47</td>
<td></td>
<td></td>
<td>μH</td>
</tr>
<tr>
<td>Inductor DC Resistance</td>
<td>$R_{DCR}$</td>
<td></td>
<td>125</td>
<td></td>
<td></td>
<td>mΩ</td>
</tr>
<tr>
<td>Dropout Resistance</td>
<td>$R_{DR}$</td>
<td>100% on duty</td>
<td>235</td>
<td></td>
<td></td>
<td>mΩ</td>
</tr>
<tr>
<td>Switch Leakage</td>
<td></td>
<td>$V_{EN} = 0V, V_{IN} = 6V, V_{SW} = 0V$ and 6V, $T_J=+25°C$</td>
<td>0</td>
<td>1</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>PFET Current Limit</td>
<td></td>
<td></td>
<td>1.0</td>
<td></td>
<td></td>
<td>A</td>
</tr>
<tr>
<td>On Time</td>
<td>$T_{ON}$</td>
<td>$V_{IN}=5V, V_{OUT}=1.2V$</td>
<td>70</td>
<td></td>
<td></td>
<td>ns</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_{IN}=3.6V, V_{OUT}=1.2V$</td>
<td>100</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Switching Frequency</td>
<td>$F_s$</td>
<td>$V_{OUT}=1.2V$</td>
<td>2800</td>
<td>3500</td>
<td>4200</td>
<td>kHz</td>
</tr>
<tr>
<td>Minimum Off Time</td>
<td>$T_{MIN-OFF}$</td>
<td></td>
<td>60</td>
<td></td>
<td></td>
<td>ns</td>
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<tr>
<td>Soft-Start Time</td>
<td>$T_{SS-ON}$</td>
<td></td>
<td>1.5</td>
<td></td>
<td></td>
<td>ms</td>
</tr>
<tr>
<td>Power Good Upper Trip Threshold</td>
<td>$P_{GH}$</td>
<td>FB Voltage in Respect to the Regulation</td>
<td>+10</td>
<td></td>
<td></td>
<td>%</td>
</tr>
<tr>
<td>Power Good Lower Trip Threshold</td>
<td>$P_{GL}$</td>
<td></td>
<td>-10</td>
<td></td>
<td></td>
<td>%</td>
</tr>
<tr>
<td>Power Good Delay</td>
<td>$P_{GD}$</td>
<td></td>
<td>50</td>
<td></td>
<td></td>
<td>μs</td>
</tr>
<tr>
<td>Power Good Sink Current Capability</td>
<td>$V_{PG-L}$</td>
<td>Sink 1mA</td>
<td></td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Power Good Logic High Voltage</td>
<td>$V_{PG-H}$</td>
<td>$V_{IN}=5V, V_{FB}=0.6V$</td>
<td>4.9</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Power Good Internal Pull Up Resistor</td>
<td>$R_{PG}$</td>
<td></td>
<td>550</td>
<td></td>
<td></td>
<td>kΩ</td>
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<tr>
<td>Under Voltage Lockout Threshold Rising</td>
<td></td>
<td></td>
<td>2.15</td>
<td>2.3</td>
<td>2.48</td>
<td>V</td>
</tr>
<tr>
<td>Under Voltage Lockout Threshold Hysteresis</td>
<td></td>
<td></td>
<td>300</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
</tbody>
</table>
ELECTRICAL CHARACTERISTICS *(continued)*

$V_{IN} = 5V$, $T_J = -40^\circ C$ to $+125^\circ C$, Typical value is tested at $T_J = +25^\circ C$. The limit over temperature is guaranteed by characterization, unless otherwise noted.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Condition</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>EN Input Logic Low Voltage</td>
<td></td>
<td></td>
<td>0.4 V</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>EN Input Logic High Voltage</td>
<td></td>
<td></td>
<td>1.2 V</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>EN Input Current</td>
<td></td>
<td>$V_{EN}=2V$</td>
<td>1.5 μA</td>
<td></td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_{EN}=0V$</td>
<td>0.1 μA</td>
<td>1 μA</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>Supply Current (Shutdown)</td>
<td></td>
<td>$V_{EN}=0V$, $T_J=+25^\circ C$</td>
<td>1 μA</td>
<td></td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>Supply Current (Quiescent)</td>
<td></td>
<td>$V_{EN}=2V, V_{FB}=0.63V, V_{IN}=5V, T_J=+25^\circ C$</td>
<td>17 μA</td>
<td>21 μA</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>Thermal Shutdown <em>(5)</em></td>
<td></td>
<td></td>
<td>150 °C</td>
<td></td>
<td></td>
<td>°C</td>
</tr>
<tr>
<td>Thermal Hysteresis <em>(5)</em></td>
<td></td>
<td></td>
<td>30 °C</td>
<td></td>
<td></td>
<td>°C</td>
</tr>
</tbody>
</table>

**Notes:**

5) Not production test, guaranteed by design.
**TYPICAL CHARACTERISTICS**

$V_{IN} = 5V$, $V_{OUT} = 1.2V$, $C_{IN}=10\mu F$, $C_{OUT}=20\mu F$, $T_a = +25^\circ C$, unless otherwise noted.

- **Quiescent Current vs. Input Voltage**
- **Shutdown Current vs. Input Voltage**
- **Efficiency vs. Output Current**

- **Efficiency vs. Output Current**
- **Load Regulation**
  - $V_{IN}=3.3V$
- **Load Regulation**
  - $V_{IN}=3.3V$

- **Load Regulation**
- **Line Regulation**
TYPICAL CHARACTERISTICS (continued)

$V_{\text{IN}} = 5V$, $V_{\text{OUT}} = 1.2V$, $C_{\text{IN}}=10\mu F$, $C_{\text{OUT}}=20\mu F$, $T_A = +25^\circ C$, unless otherwise noted.

![Graphs showing Reference Voltage vs. Temperature, Thermal Rising vs. Load Current, Radiated Emission](image)
TYPICAL PERFORMANCE CHARACTERISTICS

Vin = 5V, Vout = 1.2V, Cin=10µF, Cout=20µF, TA = +25ºC, unless otherwise noted.

Output Ripple

Vin = 3.3V, Vout = 1.2V, Iout=0A

Vin = 5V, Vout = 1.2V, Iout=0.6A

Vin = 5V, Vout = 1.2V, Iout=0A

Output Ripple

Vin = 3.3V, Vout = 1.2V, Iout=0.6A

Vin = 5V, Vout = 3.3V, Iout=0A

Vin = 5V, Vout = 3.3V, Iout=0.6A

Vin Power Up
without Load

Vin Power Up
with 0.5A Load

Vin Shutdown
without Load
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

\( V_{IN} = 5V, \ V_{OUT} = 1.2V, \ C_{IN}=10\mu F, \ C_{OUT}=20\mu F, \ T_A = +25^\circ C, \) unless otherwise noted.

VIN Shutdown with 0.5A Load

EN Startup without Load

EN Startup with 0.5A Load

EN Shutdown without Load

EN Shutdown with 0.5A Load

Power Good On without Load

Power Good On with 0.5A Load

Power Good Off without Load

Power Good Off with 0.5A Load
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

$V_{IN} = 5V$, $V_{OUT} = 1.2V$, $C_{IN}=10\mu F$, $C_{OUT}=20\mu F$, $T_A = +25^\circ C$, unless otherwise noted.
## PIN FUNCTIONS

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1, 2</td>
<td>PGND</td>
<td>Power Ground.</td>
</tr>
<tr>
<td>3, 4</td>
<td>NC</td>
<td>Internal SW Pad. Connected with copper pad for thermal sink.</td>
</tr>
<tr>
<td>5, 6</td>
<td>OUT</td>
<td>Output Voltage Power Rail. Connect load to OUT. Output capacitor is needed.</td>
</tr>
<tr>
<td>7</td>
<td>VIN</td>
<td>Supply Voltage. The MPM3805 operates from a +2.5V to +6V unregulated input. A decouple capacitor is needed to prevent large voltage spikes from appearing at the input. Place the decoupling capacitor as close to VIN as possible.</td>
</tr>
<tr>
<td>8</td>
<td>PG</td>
<td>Power Good Indicator. The output of PG is an open drain with an internal pull up resistor to VIN. PG is pulled up to VIN when the FB voltage is within 10% of the regulation level. If FB voltage is out of that regulation range, PG is low.</td>
</tr>
<tr>
<td>9</td>
<td>EN</td>
<td>On/Off Control.</td>
</tr>
<tr>
<td>10</td>
<td>FB (MPM3805GQB only)</td>
<td>Feedback. An external resistor divider from the output to GND (tapped to the FB), sets the output voltage.</td>
</tr>
<tr>
<td></td>
<td>NC (Fixed Output Version only)</td>
<td>Internal Test Pad. Do Not Connect.</td>
</tr>
<tr>
<td>11</td>
<td>AGND</td>
<td>Analogy Ground for Internal Control Circuit.</td>
</tr>
<tr>
<td>12</td>
<td>OUT_S</td>
<td>Output Voltage Sense.</td>
</tr>
</tbody>
</table>

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OPERATION

The DC-DC module has a small surface-mount QFN-12 (2.5mmx3.0mmx0.9mm) package. The module’s integrated inductor simplifies the schematic and layout design. Only FB resistors and input and output capacitors are needed to complete the design. MPM3805 uses constant on-time control (COT) with input voltage feed forward to stabilize the switching frequency over a full-input range. At light load, MPM3805 employs a proprietary control of the low-side switch and inductor current to improve efficiency.

Constant On-Time Control (COT)

Compared to a fixed-frequency PWM control, constant on-time control (COT) offers the advantage of a simpler control loop and faster transient response. Using input voltage feed forward, the MPM3805 maintains a nearly constant switching frequency across the input and output voltage range. The on-time of the switching pulse is estimated as follows:

\[ T_{\text{ON}} = \frac{V_{\text{OUT}}}{V_{\text{IN}}} \times 0.28\mu s \]

To prevent inductor current run away during load transition, MPM3805 fixes the minimum off time to 60ns. However, this minimum-off time limit does not affect operation in a steady state.

Light Load Operation

In a light-load condition, MPM3805 uses a proprietary control scheme to save power and improve efficiency. The MPM3805 turns off the low-side switch when the inductor current begins to reverse. Then MPM3805 works in discontinuous conduction mode (DCM) operation.

A zero current cross circuit detects if the inductor current begins to reverse. Considering the internal circuit propagation time, the typical delay time is 30ns. This means the inductor current continues to fall after the ZCD is triggered. If the inductor current falling slew rate is fast (Vo voltage is high or close to Vin), the low-side MOSFET turns off (this means the inductor current may be negative). This does not allow the MPM3805 to enter DCM operation. If DCM is required, the off-time of the low-side MOSFET in continuous conduction mode (CCM) should be longer than 60ns. For example, if Vin is 3.6V and Vo is 3.3V, the off-time in CCM is 24ns. It is difficult to enter DCM at light load.

Enable (EN)

If the input voltage is greater than the under-voltage lockout threshold (UVLO), typically 2.3V, MPM3805 is enabled by pulling EN above 1.2V. Leaving EN to float or be pulled down to ground disables MPM3805. There is an internal 1MΩ resistor from EN to ground.

Soft-Start (SS)

MPM3805 has a built-in soft-start that ramps up the output voltage in a controlled slew rate. This avoids overshoot at startup. The soft-start time is about 1.5ms typically.

Power GOOD Indictor (PGOOD)

MPM3805 has an open drain with a 550kΩ pull-up resistor pin for the power good indicator (PGOOD). When FB is within +/-10% of regulation voltage (i.e. 0.6V), PGOOD is pulled up to IN by the internal resistor. If FB pin voltage is out of the +/-10% window, PGOOD is pulled down to ground by an internal MOS FET. The MOS FET has a maximum R_{\text{ds(on)}} of less than 400Ω.

Current Limit

MPM3805 has a minimum 1A current limit for the high-side switch. When the high-side switch reaches the current limit, MPM3805 hits the hiccup threshold until the current decreases. This prevents the inductor current from continuing to build, which results in damage to the components.

Short Circuit and Recovery

MPM3805 enters short-circuit protection (SCP) mode when the current limit is reached, then it tries to recover from the short circuit with hiccup mode. In SCP, MPM3805 disables the output power stage, discharges the soft-start cap and then automatically tries to soft-start again. If the short circuit remains after the soft-start ends, MPM3805 repeats the cycle until the short circuit disappears, and the output rises back to the regulation level.
Figure 1: Functional Block Diagram
APPLICATION INFORMATION

COMPONENT SELECTION

Setting the Output Voltage
The external resistor divider is used to set the output voltage (see Typical Application on page 16). The feedback resistor R1 cannot be too large or too small considering the trade-off for stability and dynamics. Choose R1 between 40kΩ to 80kΩ. R2 is given by:

$$R_2 = \frac{R_1 \cdot V_{\text{out}}}{0.6} - 1$$

The feedback circuit is shown in Figure 2.

For higher output voltage, a 22μF may be needed to enhance system stability.

Since the input capacitor absorbs the input switching current it requires an adequate ripple current rating. The RMS current in the input capacitor can be estimated by:

$$I_{C1} = \frac{I_{\text{LOAD}} \cdot V_{\text{OUT}}}{V_{\text{IN}} \cdot \frac{1 - V_{\text{OUT}}}{V_{\text{IN}}}}$$

The worst case condition occurs at $V_{\text{IN}} = 2V_{\text{OUT}}$, where:

$$I_{C1} = \frac{I_{\text{LOAD}}}{2}$$

For simplification, choose the input capacitor that has a RMS current rating greater than half of the maximum load current.

The input capacitor can be electrolytic, tantalum or ceramic. When using electrolytic or tantalum capacitors, a small, high quality ceramic capacitor (i.e. 0.1μF), should be placed as close to the IC as possible. When using ceramic capacitors, check that they have enough capacitance to provide sufficient charge to prevent an excessive voltage ripple at input. The input-voltage ripple caused by capacitance is estimated by:

$$\Delta V_{\text{IN}} = \frac{I_{\text{LOAD}}}{f_s \cdot C_1} \cdot \frac{V_{\text{OUT}}}{V_{\text{IN}}} \left(1 - \frac{V_{\text{OUT}}}{V_{\text{IN}}}ight)$$

Selecting the Output Capacitor
The output capacitor (C_{OUT}) is required to maintain the DC output voltage. Ceramic capacitors are recommended. Low ESR capacitors are preferred to keep the output-voltage ripple low. The output voltage ripple is estimated by:

$$\Delta V_{\text{OUT}} = \frac{V_{\text{OUT}}}{f_s \cdot L_1} \left(1 - \frac{V_{\text{OUT}}}{V_{\text{IN}}}ight) \left(\frac{R_{\text{ESR}} + \frac{1}{8 \cdot f_s \cdot C_2}}{8 \cdot f_s \cdot C_2}ight)$$

Where $L_1$ is the inductor value, and $R_{\text{ESR}}$ is the equivalent series resistance (ESR) value of the output capacitor ($L_1$ is 0.47μH).

When using ceramic capacitors, the impedance at the switching frequency is dominated by the
capacitance. The output-voltage ripple is mainly caused by the capacitance. For simplification, the output-voltage ripple is estimated by:

\[
\Delta V_{\text{OUT}} = \frac{V_{\text{OUT}}}{8 f_s L_1 C_2} \left( 1 - \frac{V_{\text{OUT}}}{V_{\text{IN}}} \right)
\]

When using tantalum or electrolytic capacitors, the ESR dominates the impedance at the switching frequency. For simplification, the output ripple can be approximated by:

\[
\Delta V_{\text{OUT}} = \frac{V_{\text{OUT}}}{f_s L_1} \left( 1 - \frac{V_{\text{OUT}}}{V_{\text{IN}}} \right) \times R_{\text{ESR}}
\]

The characteristics of the output capacitor affect the stability of the regulation system.

**PCB Layout**

The module’s integrated inductor simplifies the schematic and layout design (see Figures 3 and 4). Only FB resistors and input and output capacitors are needed to complete the design. The high-current paths (PGND, IN and OUT) should be placed very close to the device with short, direct, and wide traces. The input capacitor needs to be as close to IN and PGND as possible. The external feedback resistors should be placed next to FB. Keep the switching node away from the feedback network. For additional device applications, please refer to related evaluation board datasheets (EVB).
Typical Application Circuits (ADJUSTABLE OUTPUT)

Figure 5: Typical Application Circuit
PACKAGE INFORMATION

QFN-12 (2.5mmX3.0mmX0.9mm)

**TOP VIEW**

**BOTTOM VIEW**

**SIDE VIEW**

**NOTE:**

1) ALL DIMENSIONS ARE IN MILLIMETERS
2) SHADED AREA IS THE KEEPOUT ZONE. THE EXPOSED BOTTOM METAL PADS ENCLOSED BY THIS ZONE IS NOT TO BE CONNECTED TO ANY PCB METAL TRACES VIA ELECTRICALLY OR MECHANICALLY.
3) EXPOSED PADDLE SIZE DOES NOT INCLUDE MOLD FLASH.
4) LEAD COPLANARITY SHALL BE 0.10 MILLIMETERS MAX
5) JEDEC REFERENCE IS MO220
6) DRAWING IS NOT TO SCALE

**RECOMMENDED LAND PATTERN**

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