MP2615C
2.1A, 1-Cell or 2-Cell Li-Ion Battery Charger
in (3mmx3mm) Package

DESCRIPTION

The MP2615C is a high-efficiency, switch-mode battery charger suitable for single-cell or dual-cell Li-ion or Li-polymer applications. The device is capable of delivering 2.1A of charge current, which can be configured via an accurate current-sense resistor across the entire input range.

The MP2615C regulates the charge current and full battery voltage using two control loops to achieve high-accuracy constant current (CC) charge and constant voltage (CV) charge.

Constant-off-time (COT) control allows the device to operate up to 99% duty cycle when the battery voltage is close to the input voltage. This maintains a relatively high charging current.

The battery temperature and charging statuses are always monitored during each charging cycle. Two status monitor output pins are provided to indicate the battery charging status and input power status. The MP2615C also features internal reverse blocking protection.

The MP2615C is available in a QFN-16 (3mmx3mm) package.

FEATURES

- 4.75V to 18V Operating Input Voltage
- Up to 99% Duty Cycle Operation
- Up to 2.1A Configurable Charging Current
- ±0.75% Full Battery Voltage Accuracy
- 4.1V/Cell and 4.2V/Cell Selection for Full Battery Voltage
- Fully Integrated Power Switches
- Internal Loop Compensation
- No External Reverse Blocking Diode Required
- Preconditioning for Fully Depleted Battery
- Charging Operation Indicator
- Configurable Safety Timer
- Thermal Shutdown Protection
- Cycle-by-Cycle Over-Current Protection
- Battery Temperature Monitoring and Protection
- Available in a QFN-16 (3mmx3mm) Package

APPLICATIONS

- Smartphones
- Portable Handheld Solutions
- Portable Media Players

TYPICAL APPLICATION

All MPS parts are lead-free, halogen-free, and adhere to the RoHS directive. For MPS green status, please visit the MPS website under Quality Assurance. “MPS”, the MPS logo, and “Simple, Easy Solutions” are trademarks of Monolithic Power Systems, Inc. or its subsidiaries.
**ORDERING INFORMATION**

<table>
<thead>
<tr>
<th>Part Number*</th>
<th>Package</th>
<th>Top Marking</th>
<th>MSL Rating</th>
</tr>
</thead>
<tbody>
<tr>
<td>MP2615CGQ</td>
<td>QFN-16 (3mmx3mm)</td>
<td>See Below</td>
<td>1</td>
</tr>
</tbody>
</table>

* For Tape & Reel, add suffix –Z (e.g. MP2615CGQ–Z).

**TOP MARKING**

BMRY

LLLL

BMR: Product code of MP2615CGQ
Y: Year code
LLLL: Lot number

**PACKAGE REFERENCE**

**TOP VIEW**

QFN-16 (3mmx3mm)
## PIN FUNCTIONS

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>SW</td>
<td>Switch output.</td>
</tr>
<tr>
<td>2</td>
<td>VIN</td>
<td>Power supply voltage.</td>
</tr>
<tr>
<td>3</td>
<td>VCC</td>
<td>Coarse regulator output. Internally generated 4.5V. Bypass VCC with a 1μF capacitor to AGND. VCC can be used as a low-side switch driver and a pull-up bias voltage NTC resistor divider. Do not connect any external load to VCC.</td>
</tr>
<tr>
<td>4</td>
<td>CELL</td>
<td>Command input for the number of Li-ion cells. Connect this pin to VCC for single-cell applications. Float the CELL pin or short it to AGND for dual-cell applications.</td>
</tr>
<tr>
<td>5</td>
<td>SEL</td>
<td>Input pin for setting terminal battery voltage. If SEL is pulled low or floating, V_BATT is 4.2V/cell. If SEL is pulled high, V_BATT is 4.1V/cell.</td>
</tr>
<tr>
<td>6</td>
<td>EN</td>
<td>On/off control input. This pin is pulled to GND with a 1MΩ internal resistor. It is recommended to connect a 100kΩ resistor in series with the EN pin.</td>
</tr>
<tr>
<td>7</td>
<td>N/C</td>
<td>No connection. Leave this pin floating.</td>
</tr>
<tr>
<td>8</td>
<td>AGND</td>
<td>Analog ground.</td>
</tr>
<tr>
<td>9</td>
<td>BATT</td>
<td>Positive battery terminal.</td>
</tr>
<tr>
<td>10</td>
<td>CSP</td>
<td>Battery current-sense positive input. Connect a resistor (R_S1) between CSP and BATT.</td>
</tr>
<tr>
<td>11</td>
<td>CHGOK</td>
<td>Charging completion indicator. When this pin goes logic low, the device is charging. The pin becomes an open drain once the charge is completed or suspended.</td>
</tr>
<tr>
<td>12</td>
<td>ACOK</td>
<td>Valid input supply indicator. A logic low on this pin indicates the presence of a valid input power supply.</td>
</tr>
<tr>
<td>13</td>
<td>NTC</td>
<td>Thermistor input. Connect a resistor from this pin to the VCC pin. Connect the thermistor from this pin to ground.</td>
</tr>
<tr>
<td>14</td>
<td>TMR</td>
<td>Internal safety timer control. Connect a capacitor from this node to AGND to set the timer. Disable the timer by connecting this pin directly to AGND.</td>
</tr>
<tr>
<td>15</td>
<td>BST</td>
<td>Bootstrap pin. Use a capacitor to drive the power switch’s gate above the supply voltage. The capacitor should be connected between the SW and BST pins to form a floating supply across the power switch driver.</td>
</tr>
<tr>
<td>16</td>
<td>PGND</td>
<td>Power ground.</td>
</tr>
</tbody>
</table>
**ABSOLUTE MAXIMUM RATINGS** *(1)*

- \( V_{SW} \): -0.3V to +23V
- \( V_{IN}, V_{ACOK}, V_{CHGOK} \): -0.3V to +23V
- \( V_{BATT}, V_{CSP} \): -0.3V to +12V
- \( V_{BST} \): \( V_{SW} + 6V \)
- All other pins: -0.3V to +6V
- Junction temperature: 150°C
- Lead temperature: 260°C
- Continuous power dissipation \( (T_A = 25°C) \) *(2)*: 2.5W
- Operating temperature: -40°C to +85°C

**ESD Ratings**

- Charged device model (CDM): 1250V

**Recommended Operating Conditions** *(3)*

- \( V_{IN} \): 4.75V to 18V
- \( V_{BATT} \): 2V to 8.4V
- Operating junction temp \( (T_J) \): -40°C to +85°C

**Thermal Resistance** *(4)*

<table>
<thead>
<tr>
<th>QFN-16 (3mmx3mm)</th>
<th>( \theta_{JA} )</th>
<th>( \theta_{JC} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>50 ( °C/W )</td>
<td>12 ( °C/W )</td>
<td></td>
</tr>
</tbody>
</table>

**Notes:**

1. Exceeding these ratings may damage the device.
2. The maximum allowable power dissipation is a function of the maximum junction temperature, \( T_J \) (MAX), the junction-to-ambient thermal resistance, \( \theta_{JA} \), and the ambient temperature, \( T_A \). The maximum allowable continuous power dissipation at any ambient temperature is calculated by \( P_D \) (MAX) = \( (T_J \) (MAX) - \( T_A \) / \( \theta_{JA} \). Exceeding the maximum allowable power dissipation can cause excessive die temperature, and the regulator may go into thermal shutdown. Internal thermal shutdown circuitry protects the device from permanent damage.
3. The device is not guaranteed to function outside of its operating conditions.
4. Measured on JESD51-7, 4-layer PCB.
ELECTRICAL CHARACTERISTICS

\( V_{\text{IN}} = 12\text{V}, V_{\text{CELL}} = 0\text{V}, V_{\text{SEL}} = 0\text{V}, C_1 = 22\mu\text{F}, C_2 = 22\mu\text{F}, T_A = 25^\circ\text{C}, \) unless otherwise noted.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Condition</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Input Voltage and Current</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input voltage</td>
<td>( V_{\text{IN}} )</td>
<td>( V_{\text{CELL}} = 4\text{V} )</td>
<td>4.5</td>
<td>5</td>
<td>18</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{\text{CELL}} = 0\text{V} )</td>
<td>8.75</td>
<td>12</td>
<td>18</td>
<td>V</td>
</tr>
<tr>
<td>Under-voltage lockout rising threshold</td>
<td>( V_{\text{UVLO}} )</td>
<td></td>
<td>3.55</td>
<td>3.75</td>
<td>3.95</td>
<td>V</td>
</tr>
<tr>
<td>Under-voltage lockout threshold hysteresis</td>
<td></td>
<td></td>
<td>225</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>Supply current</td>
<td>( I_{\text{SHDN}} )</td>
<td>( \text{EN} = 4\text{V}, \text{shutdown current} )</td>
<td>0.27</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td>( I_0 )</td>
<td>( \text{EN} = 0\text{V}, \text{quiescent current} )</td>
<td>1.1</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Power MOSFET</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>High-side switch on resistance</td>
<td>( R_{\text{H,DS(ON)}} )</td>
<td>Measured from ( V_{\text{IN}} ) to SW</td>
<td>110</td>
<td></td>
<td></td>
<td>mΩ</td>
</tr>
<tr>
<td>Low-side switch on resistance</td>
<td>( R_{\text{L,DS(ON)}} )</td>
<td></td>
<td>110</td>
<td></td>
<td></td>
<td>mΩ</td>
</tr>
<tr>
<td>Switch leakage</td>
<td>( I_{\text{SHDN}} )</td>
<td>( \text{EN} = 4\text{V}, V_{SW} = 0\text{V} )</td>
<td>0</td>
<td>1</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td><strong>Frequency and Time Parameter</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Switching frequency</td>
<td>( f_{\text{SW}} )</td>
<td>( V_{\text{BATT}} = 7.5\text{V} )</td>
<td>760</td>
<td></td>
<td></td>
<td>kHz</td>
</tr>
<tr>
<td>Foldback frequency</td>
<td></td>
<td>( V_{\text{BATT}} = 0\text{V} )</td>
<td>160</td>
<td></td>
<td></td>
<td>kHz</td>
</tr>
<tr>
<td>Minimum off time (^{(5)})</td>
<td>( t_{\text{OFF}} )</td>
<td>( V_{\text{BATT}} = 9\text{V} )</td>
<td>200</td>
<td></td>
<td></td>
<td>ns</td>
</tr>
<tr>
<td><strong>Charging Parameter</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Terminal battery voltage</td>
<td>( V_{\text{BATT, FULL}} )</td>
<td>( V_{\text{SEL}} = 0\text{V} )</td>
<td>4.168</td>
<td>4.2</td>
<td>4.231</td>
<td>V/cell</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{\text{SEL}} = 4\text{V} )</td>
<td>4.069</td>
<td>4.1</td>
<td>4.131</td>
<td>V/cell</td>
</tr>
<tr>
<td>Battery over-voltage threshold</td>
<td>( V_{\text{BOVP}} )</td>
<td>( V_{\text{CELL}} = 0\text{V}, V_{\text{SEL}} = 0\text{V} )</td>
<td>8.34</td>
<td>8.71</td>
<td>9.08</td>
<td>V/cell</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{\text{CELL}} = 0\text{V}, V_{\text{SEL}} = 4\text{V} )</td>
<td>8.14</td>
<td>8.51</td>
<td>8.88</td>
<td>V/cell</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{\text{CELL}} = 4\text{V}, V_{\text{SEL}} = 0\text{V} )</td>
<td>4.17</td>
<td>4.36</td>
<td>4.54</td>
<td>V/cell</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{\text{CELL}} = 4\text{V}, V_{\text{SEL}} = 4\text{V} )</td>
<td>4.07</td>
<td>4.26</td>
<td>4.44</td>
<td>V/cell</td>
</tr>
<tr>
<td>Recharge threshold at ( V_{\text{BATT}} )</td>
<td>( V_{\text{RECH}} )</td>
<td>( V_{\text{SEL}} = 0\text{V} )</td>
<td>4.0</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{\text{SEL}} = 4\text{V} )</td>
<td>3.9</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Recharge hysteresis</td>
<td></td>
<td></td>
<td>150</td>
<td></td>
<td></td>
<td>mV/cell</td>
</tr>
<tr>
<td>Trickle-charge voltage threshold</td>
<td>( V_{\text{TC}} )</td>
<td>( V_{\text{SEL}} = 0\text{V} )</td>
<td>3</td>
<td></td>
<td></td>
<td>V/cell</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{\text{SEL}} = 4\text{V} )</td>
<td>2.91</td>
<td></td>
<td></td>
<td>V/cell</td>
</tr>
<tr>
<td>Trickle charge hysteresis</td>
<td></td>
<td></td>
<td>225</td>
<td></td>
<td></td>
<td>mV/Cell</td>
</tr>
<tr>
<td>Peak current limit</td>
<td></td>
<td>Constant current charge</td>
<td>3.2</td>
<td></td>
<td></td>
<td>A</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Trickle</td>
<td>2.2</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CC current</td>
<td>( I_{\text{CC}} )</td>
<td>( RS1 = 25\text{mΩ} )</td>
<td>1.9</td>
<td>2.1</td>
<td>2.3</td>
<td>A</td>
</tr>
<tr>
<td>Trickle charge current</td>
<td>( I_{\text{TCC}} )</td>
<td></td>
<td>5%</td>
<td>10%</td>
<td>15%</td>
<td>( I_{\text{CC}} )</td>
</tr>
<tr>
<td>Termination current threshold</td>
<td>( I_{\text{BF}} )</td>
<td></td>
<td>5%</td>
<td>10%</td>
<td>15%</td>
<td>( I_{\text{CC}} )</td>
</tr>
<tr>
<td>( V_{\text{IN}} ) minimum headroom (reverse blocking)</td>
<td>( V_{\text{IN}} - V_{\text{BATT}} )</td>
<td></td>
<td>300</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
</tbody>
</table>
### ELECTRICAL CHARACTERISTICS (continued)

$V_{\text{IN}} = 12\, \text{V}$, $V_{\text{CELL}} = 0\, \text{V}$, $V_{\text{SEL}} = 0\, \text{V}$, $C_1 = 22\, \mu\text{F}$, $C_2 = 22\, \mu\text{F}$, $T_A = 25^\circ \text{C}$, unless otherwise noted.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Condition</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximum current-sense voltage (CSP to BATT)</td>
<td>$V_{\text{SENSE}}$</td>
<td></td>
<td>45</td>
<td>50</td>
<td>55</td>
<td>mV</td>
</tr>
<tr>
<td>CSP, BATT current</td>
<td>$I_{\text{CSP}, \text{IBATT}}$</td>
<td>Charging disabled</td>
<td>3</td>
<td></td>
<td></td>
<td>$\mu\text{A}$</td>
</tr>
<tr>
<td>ACOK/CHGOK open-drain sink current</td>
<td>$V_{\text{DRAIN}} = 0.3, \text{V}$</td>
<td></td>
<td>5</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
</tbody>
</table>

#### VCC Regulator Output

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Condition</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>VCC output voltage</td>
<td>$V_{\text{CC}}$</td>
<td></td>
<td>4.2</td>
<td>4.5</td>
<td>4.75</td>
<td>V</td>
</tr>
<tr>
<td>VCC load regulation</td>
<td>$\Delta V_{\text{CC}}$</td>
<td>$I_{\text{LOAD}} = 0$ to $10, \text{mA}$</td>
<td>10</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
</tbody>
</table>

#### EN Control

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\bar{E}N$ input low voltage</td>
<td></td>
<td></td>
<td></td>
<td>0.4</td>
<td>V</td>
</tr>
<tr>
<td>$\bar{E}N$ input high voltage</td>
<td></td>
<td></td>
<td></td>
<td>1.8</td>
<td>V</td>
</tr>
<tr>
<td>$\bar{E}N$ input current</td>
<td>$I_{\bar{E}N}$</td>
<td>$EN = 4, \text{V}$</td>
<td>4</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$EN = 0, \text{V}$</td>
<td>0.2</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

#### Logic

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>CELL input low voltage</td>
<td>$V_L$</td>
<td></td>
<td>0.4</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>CELL input high voltage</td>
<td>$V_H$</td>
<td></td>
<td>1.8</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>SEL input low voltage</td>
<td>$V_L$</td>
<td></td>
<td>0.4</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>SEL input high voltage</td>
<td>$V_H$</td>
<td></td>
<td>1.8</td>
<td></td>
<td>V</td>
</tr>
</tbody>
</table>

#### Timer Protection

| Parameter              | Symbol | Min   | Units | |
|------------------------|--------|-------|-------|
| Trickle charge time    | $T_{\text{TRICKLE, TMR}}$ | 30    | min   |
| CC/CV charge time      | $T_{\text{TOTAL, TMR}}$ | 165   |       |

#### NTC Protection

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
<th>% of $V_{\text{CC}}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>NTC cold temp rising threshold</td>
<td>$V_{\text{COLD}}$</td>
<td>$R_{\text{NTC}} = \text{NCP18X103}, 0^\circ \text{C}$</td>
<td>72</td>
<td>73.3</td>
<td>74.6</td>
<td></td>
</tr>
<tr>
<td>NTC cold temp rising threshold hysteresis</td>
<td></td>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>NTC hot temp falling threshold</td>
<td>$V_{\text{HOT}}$</td>
<td>$R_{\text{NTC}} = \text{NCP18X103}, 50^\circ \text{C}$</td>
<td>29.5</td>
<td>31.1</td>
<td>32.2</td>
<td></td>
</tr>
<tr>
<td>NTC hot temp falling threshold hysteresis</td>
<td></td>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

#### Thermal Protection

| Parameter              | Symbol | Min   | Units | |
|------------------------|--------|-------|-------|
| Thermal shutdown (5)   | $T_{\text{SHDN}}$ | 150   | $^\circ \text{C}$ |
| Thermal shutdown hysteresis (5) | | 20 | $^\circ \text{C}$ |

#### Reverse Leakage Blocking

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Min</th>
<th>units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Battery reverse leakage current</td>
<td>$I_{\text{LEAKAGE}}$</td>
<td>$V_{\text{CELL}} = 0, \text{V}$</td>
<td>3</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_{\text{CELL}} = 4, \text{V}$</td>
<td>0.5</td>
</tr>
</tbody>
</table>

### Notes:

5) Guaranteed by design.
TYPICAL PERFORMANCE CHARACTERISTICS

$V_{IN} = 12V$, $C1 = C2 = 22\mu F$, SEL = float/high, CELL = float/high, $RS1 = 25m\Omega$, unless otherwise noted.

Battery-Full Voltage vs. Temperature

1-cell

Constant Charge Current vs. Temperature

RS1 = 25mΩ

Trickle Charge Current vs. Temperature

RS1 = 25mΩ

Charge-Full Current vs. Temperature

RS1 = 25mΩ

VCC Output Voltage vs. Temperature
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

$V_{\text{IN}} = 12\text{V}$, $C1 = C2 = 22\mu\text{F}$, $\text{SEL} = \text{float/high}$, $\text{CELL} = \text{float/high}$, $RS1 = 25\text{m}\Omega$, unless otherwise noted.

**Auto-Recharge Threshold Voltage vs. Temperature**

1-cell

2-cell

- $VBATT_{\text{FULL}=4.1\text{V}}$
- $VBATT_{\text{FULL}=4.2\text{V}}$
- $VBATT_{\text{FULL}=8.2\text{V}}$
- $VBATT_{\text{FULL}=8.4\text{V}}$
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

\[ V_{IN} = 5V/9V, \quad C1 = C2 = 22\mu F, \quad SEL = \text{float/high}, \quad \text{CELL} = \text{float/high}, \quad L = 6.8\mu H, \quad RS1 = 25m\Omega, \quad \text{battery simulator}, \quad T_A = 25^\circ C, \quad \text{unless otherwise noted.} \]

**Battery Charge Curve**

- \[ V_{IN} = 5V, \quad \text{1-cell, } V_{BATTREG} = 4.2V \]

**Auto-Recharge**

- \[ V_{IN} = 5V, \quad \text{1-cell, } V_{BATTREG} = 4.2V \]

**Trickle Charge Steady State**

- \[ V_{IN} = 5V, \quad \text{1-cell, } V_{BATT} = 1.9V \]

**Trickle Charge Steady State**

- \[ V_{IN} = 9V, \quad \text{2-cell, } V_{BATT} = 5.2V \]

**CC Charge Steady State**

- \[ V_{IN} = 5V, \quad \text{1-cell, } V_{BATT} = 3.6V \]

**CC Charge Steady State (COT)**

- \[ V_{IN} = 5V, \quad \text{1-cell, } V_{BATT} = 4V \]
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

\( V_{IN} = 5V/9V, \ C_1 = C_2 = 22\mu F, \ SEL = \text{float/high}, \ \text{CELL} = \text{float/high}, \ L = 6.8\mu H, \ RS1 = 25m\Omega, \ \text{battery simulator, } T_A = 25^\circ C, \) unless otherwise noted.

**CC Charge Steady State**

\( V_{IN} = 9V, \ \text{2-cell}, \ V_{BATT} = 6.5V \)

**CC Charge Steady State (COT)**

\( V_{IN} = 9V, \ \text{2-cell}, \ V_{BATT} = 7.5V \)

**CV Charge Steady State**

\( V_{IN} = 5V, \ \text{1-cell}, \ V_{BATT} = 4.17V \)

**CV Charge Steady State**

\( V_{IN} = 9V, \ \text{2-cell}, \ V_{BATT} = 8.35V \)

**Start-Up, CC Charge Mode**

\( V_{IN} = 9V, \ \text{2-cell}, \ V_{BATT} = 7V \)

**Shutdown, CC Charge Mode**

\( V_{IN} = 9V, \ \text{2-cell}, \ V_{BATT} = 7V \)
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

$V_{\text{IN}} = 5V/9V$, $C1 = C2 = 22\mu F$, $\text{SEL} = \text{float/high}$, $\text{CELL} = \text{float/high}$, $L = 6.8\mu H$, $\text{RS1} = 25m\Omega$, battery simulator, $T_A = 25^\circ C$, unless otherwise noted.

- **VIN Hot Insertion, CC Charge Mode**
  
  $V_{\text{IN}} = 9V$, 2-cell, $V_{\text{BATT}} = 7V$

- **VIN Removal, CC Charge Mode**
  
  $V_{\text{IN}} = 9V$, 2-cell, $V_{\text{BATT}} = 7V$

- **EN On/Off**
  
  $V_{\text{IN}} = 5V$, 1-cell, $V_{\text{BATT}} = 3.6V$

- **NTC Fault Control**
  
  $V_{\text{IN}} = 9V$, 2-cell, $V_{\text{BATT}} = 7V$

- **Timeout**
  
  $V_{\text{IN}} = 9V$, 2-cell, $V_{\text{BATT}} = 7V$, $I_{\text{CC}} = 2A$, $C_{\text{TRM}} = 470pF$

- **Charge-Full Indication**
  
  $V_{\text{IN}} = 5V$, 1-cell, $V_{\text{BATT}} = 3.6V$
Figure 1: Functional Block Diagram
OPERATION

The MP2615C is a peak current mode control switching charger for single-cell or dual-cell Li-ion and Li-polymer batteries. The MP2615C integrates both the high-side and low-side MOSFETs of the synchronous buck converter to provide high efficiency and reduce PCB size.

Charge Cycle (Mode Change: TC to CC to CV)
The MP2615C regulates the charge current ($I_{CHG}$) and battery voltage ($V_{BATT}$) using two control loops to achieve highly accurate constant current (CC) charge and constant voltage (CV) charge.

If $V_{BATT}$ is below $V_{TC}$, the MP2615C remains in trickle charge mode, and the output of the charge current loop (COMPI) dominates the control (see Figure 2). The battery is charged by a trickle charge current ($I_{TC}$) until the battery voltage reaches $V_{TC}$. If the charger remains in trickle charge mode until the trickle charge timer is triggered, charging is terminated.

The MP2615C enters CC charge mode once the battery voltage exceeds $V_{TC}$. In this mode, the charge current increases from $I_{TC}$ to $I_{CC}$ to fast charge the battery.

If the battery voltage exceeds the battery-full voltage ($V_{BATT\_FULL}$), the charger enters CV mode. In CV mode, the battery voltage is regulated at $V_{BATT\_FULL}$, and the charge current falls naturally due to the existing equivalent internal resistance of the battery. Figure 4 shows an operation flowchart.

Charge Full Termination and Auto-Recharge
If the charge current drops below the termination threshold ($I_{BF}$) during the CV charge phase, the charger stops charging and the CHGOK pin becomes an open drain. The timer resets and turns off. Once the battery voltage drops below the recharge threshold ($V_{RECH}$) (4.0V/cell while the SEL pin is connected to AGND), recharging automatically begins and the timer restarts a new charge cycle.

Constant-Off-Time (COT) Charge Mode
The MP2615C uses the floating ground method to drive the buck converter’s high-side MOSFET (HS-FET). While the HS-FET is off, the BST capacitor is recharged, and the voltage across the capacitor is used as the HS-FET’s gate driver. Thus a minimum off time of 200ns is required to maintain sufficient voltage at the BST capacitor.

When the 200ns minimum off time is achieved due to a large duty cycle, the MP2615C enters constant-off-time (COT) charge mode. In this mode, the switching frequency is slightly reduced to achieve a 99% duty cycle.

Charge Status Indication
The MP2615C has two open-drain status outputs: the CHGOK and ACOK pins. The ACOK pin goes low when the input voltage exceeds the battery voltage and the under-voltage lockout (UVLO) threshold by 300mV. The CHGOK pin indicates the status of the charge cycle.

Table 1 lists the operation for both CHGOK and
Safety Timer Operation

The MP2615C has an internal safety timer to terminate charging during timeout. The capacitor (C_{TMR}) connected between the TMR pin and GND is used to set the internal oscillating period, calculated with Equation (1):

\[ t_p (\text{seconds}) = 0.46 \times C_{TMR} (\mu\text{F}) \]  

(1)

This timer limits the maximum trickle charge time to 8192 internal oscillating periods. If the current remains in trickle charge mode for longer than the maximum oscillating periods, charging is terminated and CHGOK becomes an open drain to indicate a timeout fault. If the charge cycle successfully completes trickle charge within the allowed time limit, it enters CC charge mode and the timer continues to count the oscillating periods. When the battery is fully charged, the timer turns off and clears the counter, waiting for the auto-recharge to restart.

If the charge time during CC/CV mode exceeds 49152 oscillating periods, and the battery-full condition has not been met, charging is terminated and a timeout fault is indicated by floating the CHGOK pin. The charger can exit the timeout fault state (and restart the on-chip safety timer) when one of the following conditions occurs:

- The battery voltage falls below the auto-recharge threshold (V_{RECH})
- A power-on reset (POR) event occurs
- The EN pin is toggled

The timer can be disabled by pulling the TMR pin to AGND.

The trickle mode charge time can be estimated with Equation (2):

\[ t_{TRICKLE,TMR} (\text{minutes}) = 62.8 \times C_{TMR} (\mu\text{F}) \]  

(2)

If C_{TMR} is 0.47\mu F, the trickle charge time is about 30 minutes. The CC/CV mode charge time can be calculated with Equation (3):

\[ t_{TOTAL,TMR} (\text{hours}) = 62.8 \times C_{TMR} (\mu\text{F}) \]  

(3)

In this case, if C_{TMR} is 0.47\mu F, the CC/CV charge time is 2.95 hours.

Negative Thermal Coefficient (NTC) Thermistor

The NTC pin allows the MP2615C to sense the battery temperature using the negative thermal coefficient (NTC) resistor in the battery pack. This ensures a safe operating environment for the battery. A resistor with an appropriate value should be connected from the VCC pin to the NTC pin, and the thermistor should be connected from the NTC pin to AGND. The voltage on the NTC pin is determined by the resistor divider, which has a divide ratio that depends on the battery temperature. When the voltage at the NTC pin falls out of the NTC window range, charging pauses until the battery temperature returns to within the normal operating range.

As a result, the MP2615C stops charging and reports this condition to the status pins. Charging automatically resumes after the temperature returns to within a safe range.

Short-Circuit Protection

The MP2615C has an internal comparator to check for battery short-circuit conditions. If V_{BATT} falls below 2V, the device detects a battery short status, and the cycle-by-cycle peak current limit falls to about 2.2A to limit the current spike during the battery short transition. The switching frequency also folds back to minimize the power loss.

Thermal Shutdown Protection

To prevent the chip from overheating while it charges, the MP2615C monitors the junction temperature (T_J) of the die. If T_J reaches the thermal shutdown threshold (T_{SHUTDOWN}) of 150°C, the charger converter turns off. Once T_J falls below 130°C, charging resumes.
Figure 3: Input Power Start-Up Timing Diagram
OPERATION FLOWCHART

Figure 4: Operation Flowchart
APPLICATION INFORMATION

COMPONENT SELECTION

Charge Current Setting

The MP2615C’s constant charge current (I_{CC}) can be set via the sense resistor (RS1) (see the Typical Application Circuit section on page 18). Calculate the constant current (CC) charge with Equation (4):

\[ I_{CC} = \frac{52.5\text{mV}}{RS1(\text{m}\Omega)} \text{(A)} \]  

(4)

If I_{CC} is 2.1A, choose RS1 to be 25mΩ.

The trickle charge current (I_{TC}) can be estimated with Equation (5):

\[ I_{TC} = 0.10 \times I_{CC} \]  

(5)

Selecting the Inductor

To select the optimal inductor, a tradeoff should be made between cost, size, and efficiency. An inductor with a lower-value inductance has the benefit of a smaller size, but also has higher ripple currents, magnetic hysteretic losses, and output capacitance. Conversely, a higher-value inductor lowers the ripple current and allows for the use of smaller output filter capacitors, but it also results in higher inductor DC resistance (DCR) loss.

Based on practical experience, the inductor ripple current should not exceed 30% of the maximum charge current under the worst-case conditions. For the MP2615C, with a typical 12V input voltage to charge a 2-cell battery, the maximum inductor current ripple occurs at the corner point between trickle charge and CC charge (V_{BATT} = 6V). The inductance can be calculated with Equation (6):

\[ L = \frac{V_{IN} - V_{BATT}}{\Delta I_{L\_MAX}} \times \frac{V_{BATT}}{V_{IN} \times f_{SW}} \]  

(6)

Where V_{IN} is the input voltage, V_{BATT} is the CC charge threshold, and f_{SW} is the switching frequency.

\Delta I_{L\_MAX} is the maximum inductor ripple current, which is usually 30% of the CC charge current, calculated with Equation (7):

\[ \Delta I_{L\_MAX} = 0.30 \times I_{CC} \]  

(7)

If \( V_{BATT} = 6V \) and \( f_{SW} = 760kHz \), the calculated inductance is 6.6μH. The inductor saturation current must exceed 2.6A and have some tolerance. To optimize efficiency, choose an inductor with a DC resistance below 50mΩ.

NTC Resistor Divider Selection

Figure 5 shows how an internal resistor divider sets the cold temperature threshold and hot temperature threshold at 73.3% of V_{CC} and 31.1% of V_{CC}, respectively.

![Figure 5: NTC Functional Block](image)

The thermistor (NCP18XH103) in Figure 5 has the following electrical characteristics:

- At 0°C, R_{NTC\_COLD} = 27.445kΩ
- At 50°C, R_{NTC\_HOT} = 4.1601kΩ

For a given NTC thermistor, select values for R_{T1} and R_{T2} to set the NTC window. Assuming that the NTC window is between 0°C and 50°C, the cold and hot thresholds can be calculated with Equation (8) and Equation (9), respectively:

\[ \frac{R_{T2}/R_{NTC\_COLD}}{R_{T1} + R_{T2}/R_{NTC\_COLD}} = V_{COLD} = 73.3\% \]  

(8)

\[ \frac{R_{T2}/R_{NTC\_HOT}}{R_{T1} + R_{T2}/R_{NTC\_HOT}} = V_{HOT} = 31.1\% \]  

(9)

The required battery temperature range can be paired with Equation (8) and Equation (9) to calculate R_{T1} and R_{T2}.

Selecting the Input Capacitor

The input capacitors (C1) from the typical application circuit absorb the maximum ripple current from the buck converter, which can be...
estimated with Equation (10):

\[ I_{RMS\_MAX} = I_C \frac{\sqrt{V_{TC}} \times (V_{IN\_MAX} - V_{TC})}{V_{IN\_MAX}} \]

(10)

If \( I_C = 2A, V_{TC} = 6V, \) and \( V_{IN\_MAX} = 18V, \) the maximum ripple current is 1A. Select the input capacitors so that the temperature rise due to the ripple current does not exceed 10°C. It is recommended to use ceramic capacitors with X5R or X7R dielectrics because of their low ESR and small temperature coefficients. For most applications, use a 22µF capacitor.

Selecting the Output Capacitor

The output capacitor (C2) is in parallel with the battery. C2 absorbs the high-frequency switching ripple current and smooths the output voltage. Its impedance must be below the battery’s to ensure it absorbs the ripple current. Use a ceramic capacitor because it has a lower ESR and smaller size. The output voltage ripple can be calculated with Equation (11):

\[ \Delta R_O = \frac{\Delta V_O}{V_O} = \frac{1 - V_O}{V_{IN}} \times \frac{8 \times C_O \times f_{SW}^2 \times L}{V_{IN\_MAX}} \]

(11)

To guarantee u0.5% battery-full voltage accuracy, the maximum output voltage ripple should not exceed 0.5% (e.g. 0.1%). The maximum output voltage ripple occurs at the minimum battery voltage of the CC charge and the maximum input voltage.

If \( V_{IN\_MAX} = 18V, \) \( V_{CC\_MIN} = V_{TC} = 6V, \) \( L = 6.8\mu H, \) \( f_{SW} = 760kHz, \) and \( \Delta R_{O\_MAX} = 0.1\% \), the output capacitor can be calculated with Equation (12):

\[ C_O = \frac{1 - V_{TC}}{8 \times f_{SW}^2 \times L \times \Delta R_{O\_MAX}} = 21.3\mu F \]

(12)

In this case, choose a 22µF ceramic capacitor.

PCB Layout Guidelines

Proper PCB layout is important to meet specified noise, efficiency and stability requirements. For the best results, refer to Figure 6 and follow the guidelines below:

1. Route the power stage adjacent to the grounds.
2. Aim to minimize the high-side switching node (SW, inductor) trace lengths in the high-current paths and the current-sense resistor trace.
3. Keep the switching node short, and route it away from the feedback network.
4. Connect the charge current-sense resistor to CSP (pin 10) and BATT (pin 9). Minimize the length and area of this circuit loop.
5. Place the input capacitor as close as possible to the VIN and PGND pins.
6. Place the output inductor as close as possible to the IC, and connect the output capacitor between the inductor and PGND of the IC. This minimizes the current path loop area from the SW pin through the LC filter and back to the PGND pin.
7. Connect AGND and PGND at a single point.
TYPICAL APPLICATION CIRCUIT

Figure 7: Typical Application Circuit to Charge a 2-Cell Battery with 12V $V_{IN}$
PACKAGE INFORMATION

QFN-16 (3mmx3mm)

NOTE:
1) ALL DIMENSIONS ARE IN MILLIMETERS.
2) EXPOSED PADDLE SIZE DOES NOT INCLUDE MOLD FLASH.
3) LEAD COPLANARITY SHALL BE 0.10 MILLIMETERS MAX.
4) JEDEC REFERENCE IS MO-220.
5) DRAWING IS NOT TO SCALE.

RECOMMENDED LAND PATTERN
# CARRIER INFORMATION

![Reel Diagram]

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package Description</th>
<th>Quantity/Reel</th>
<th>Quantity/Tube</th>
<th>Quantity/Tray</th>
<th>Reel Diameter</th>
<th>Carrier Tape Width</th>
<th>Carrier Tape Pitch</th>
</tr>
</thead>
<tbody>
<tr>
<td>MP2615CGQ–Z</td>
<td>QFN-16 (3mmx3mm)</td>
<td>5000</td>
<td>N/A</td>
<td>N/A</td>
<td>13in</td>
<td>12mm</td>
<td>8mm</td>
</tr>
</tbody>
</table>
**Revision History**

<table>
<thead>
<tr>
<th>Revision #</th>
<th>Revision Date</th>
<th>Description</th>
<th>Pages Updated</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.0</td>
<td>9/11/2020</td>
<td>Initial Release</td>
<td></td>
</tr>
</tbody>
</table>

**Notice:** The information in this document is subject to change without notice. Please contact MPS for current specifications. Users should warrant and guarantee that third-party Intellectual Property rights are not infringed upon when integrating MPS products into any application. MPS will not assume any legal responsibility for any said applications.