DESCRIPTION
The MP26123 is a monolithic DC-DC step-down switching charger for 2- or 3-cell Li-ion battery packs. It has an integrated high-side power MOSFET can output up to a 2A charge current. It also has peak-current-mode control for fast loop response and easy compensation.

The MP26123 uses a sense resistor to control a programmable charge current, and accurately regulates the charge current and charge voltage using two control loops.

The MP26123 has multiple fault condition protections that include cycle-by-cycle current limiting and thermal shutdown. Other safety features include battery temperature monitoring and protection, charge status indication and programmable timer to cease the charging cycle when timer out.

The MP26123 requires a minimal number of readily-available external components.

The MP26123 is available in a 4mm x 4mm 16-pin QFN package.

FEATURES
- Charges 2- or 3-Cell Li-Ion Battery Packs
- Wide Operating Input Range
- Programmable Charging Current of up to 2A
- ±0.75% \( V_{BATT} \) Accuracy
- 0.2Ω Integrated Power MOSFET
- Up to 90% Efficiency
- Fixed 600kHz Frequency
- Preconditioning for Fully Depleted Batteries
- Charging Status Indicator
- Input Supply Fault Indicator
- Thermal Shutdown
- Cycle-by-Cycle Over-Current Protection
- Battery Temperature Monitor and Protection

APPLICATIONS
- Mobile Internet Device
- Portable Media Player
- Netbook
- Charger for 2- or 3-Cell Li-Ion Batteries
- Distributed Power Systems

For MPS green status, please visit MPS website under Quality Assurance. "MPS" and "The Future of Analog IC Technology" are Registered Trademarks of Monolithic Power Systems, Inc.
### ORDERING INFORMATION

<table>
<thead>
<tr>
<th>Part Number*</th>
<th>Package</th>
<th>Top Marking</th>
<th>Free Air Temperature (TA)</th>
</tr>
</thead>
<tbody>
<tr>
<td>MP26123DR</td>
<td>QFN16 (4x4mm)</td>
<td>M26123</td>
<td>-40°C to +85°C</td>
</tr>
</tbody>
</table>

*For Tape & Reel, add suffix –Z (e.g. MP26123DR–Z)

For RoHS compliant packaging, add suffix –LF (e.g. MP MP26123DR–LF–Z)

### PACKAGING REFERENCE

**TOP VIEW**


- **EXPOSED PAD ON BACKSIDE**

### ABSOLUTE MAXIMUM RATINGS

<table>
<thead>
<tr>
<th>Supply Voltage VCC, VIN</th>
<th>-26V</th>
</tr>
</thead>
<tbody>
<tr>
<td>V&lt;sub&gt;SW&lt;/sub&gt;</td>
<td>-0.3V to (V&lt;sub&gt;IN&lt;/sub&gt; + 0.3V)</td>
</tr>
<tr>
<td>V&lt;sub&gt;BST&lt;/sub&gt;</td>
<td>V&lt;sub&gt;SW&lt;/sub&gt; + 6V</td>
</tr>
<tr>
<td>V&lt;sub&gt;CSP&lt;/sub&gt;, V&lt;sub&gt;BATT&lt;/sub&gt;</td>
<td>-0.3V to +18V</td>
</tr>
<tr>
<td>V&lt;sub&gt;ACOK&lt;/sub&gt;, V&lt;sub&gt;CHGOK&lt;/sub&gt;</td>
<td>-0.3V to +26V</td>
</tr>
<tr>
<td>All Other Pins</td>
<td>-0.3V to +6V</td>
</tr>
</tbody>
</table>

Continuous Power Dissipation: 2.7W (1) (TA = 25°C) (2)

Junction Temperature: 150°C

Lease Temperature: 260°C

Storage Temperature: -65°C to 150°C

### Recommended Operating Conditions

Supply Voltage V<sub>IN</sub>: 9V to 24V

Maximum Junction Temp. (T<sub>J</sub>): 125°C

### Thermal Resistance

QFN16 (4x4mm): 46°... 10°C/W

**θ<sub>JA</sub>** = Junction-to-Ambient Thermal Resistance

**θ<sub>JC</sub>** = Junction-to-Case Thermal Resistance

### Notes:

1) Exceeding these ratings may damage the device.

2) The maximum allowable power dissipation is a function of the maximum junction temperature T<sub>J</sub>(MAX), the junction-to-ambient thermal resistance θ<sub>JA</sub>, and the ambient temperature T<sub>A</sub>. The maximum allowable continuous power dissipation at any ambient temperature is calculated by PD(MAX)=(T<sub>J</sub>(MAX)-T<sub>A</sub>)/θ<sub>JA</sub>. Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown. Internal thermal shutdown circuitry protects the device from permanent damage.

3) The device is not guaranteed to function outside of its operating conditions.

4) Measured on JESD51-7 4-layer board.
### ELECTRICAL CHARACTERISTICS

$V_{\text{IN}} = 19\text{V}, T_A = 25^\circ\text{C}, \text{CELLS}=0\text{V}$, unless otherwise noted.

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Symbol</th>
<th>Condition</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Terminal battery voltage</td>
<td>$V_{\text{BATT}}$</td>
<td>$\text{CELLS}=0\text{V}$</td>
<td>8.337</td>
<td>8.4</td>
<td>8.463</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$\text{CELLS}=\text{Float}$</td>
<td>12.505</td>
<td>12.6</td>
<td>12.695</td>
<td>V</td>
</tr>
<tr>
<td>CSP, BATT current</td>
<td>$I_{\text{CSP},IBATT}$</td>
<td>Charging disabled</td>
<td>1</td>
<td>1</td>
<td></td>
<td>$\mu$A</td>
</tr>
<tr>
<td>Switch on resistance</td>
<td>$R_{\text{DS(ON)}}$</td>
<td></td>
<td>0.2</td>
<td></td>
<td></td>
<td>$\Omega$</td>
</tr>
<tr>
<td>Switch leakage</td>
<td>$EN=4\text{V}, V_{\text{SW}}=0\text{V}$</td>
<td></td>
<td>0</td>
<td>1</td>
<td></td>
<td>$\mu$A</td>
</tr>
<tr>
<td>Peak current limit</td>
<td>$CC^{(5)}$</td>
<td></td>
<td>4.1</td>
<td></td>
<td></td>
<td>A</td>
</tr>
<tr>
<td></td>
<td>TRICKLE</td>
<td></td>
<td>2</td>
<td></td>
<td></td>
<td>A</td>
</tr>
<tr>
<td>CC current</td>
<td>$I_{\text{CC}}$</td>
<td>$\text{RS1}=100\text{m}\Omega$</td>
<td>1.8</td>
<td>2.0</td>
<td>2.2</td>
<td>A</td>
</tr>
<tr>
<td>Trickle charge current</td>
<td>$f_{\text{SW}}$</td>
<td>$\text{CELLS}=0\text{V}, V_{\text{BATT}}=7\text{V}$</td>
<td>600</td>
<td></td>
<td></td>
<td>kHz</td>
</tr>
<tr>
<td>Trickle charge voltage threshold</td>
<td>$V_{\text{TC}}$</td>
<td>$V_{\text{BATT}}=0\text{V}$</td>
<td>3</td>
<td></td>
<td></td>
<td>V/cell</td>
</tr>
<tr>
<td>Trickle charge hysteresis</td>
<td></td>
<td></td>
<td>350</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>Termination current threshold</td>
<td>$I_{\text{BF}}$</td>
<td>$5%$, $10%$, $15%$ $I_{\text{CC}}$</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Oscillator frequency</td>
<td></td>
<td></td>
<td>90</td>
<td></td>
<td></td>
<td>%</td>
</tr>
<tr>
<td>Maximum current sense voltage (CSP to BATT)</td>
<td>$V_{\text{SENSE}}$</td>
<td></td>
<td>170</td>
<td>200</td>
<td>230</td>
<td>mV</td>
</tr>
<tr>
<td>Minimum on time</td>
<td>$t_{\text{ON}}$</td>
<td></td>
<td>100</td>
<td></td>
<td></td>
<td>ns</td>
</tr>
<tr>
<td>Under-voltage lockout threshold rising</td>
<td></td>
<td></td>
<td>3</td>
<td>3.2</td>
<td>3.4</td>
<td>V</td>
</tr>
<tr>
<td>Under-voltage lockout threshold hysteresis</td>
<td></td>
<td></td>
<td>200</td>
<td>1000</td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>Open-drain sink current</td>
<td></td>
<td>$V_{\text{DRAIN}}=0.3\text{V}$</td>
<td>5</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>Dead battery indicator</td>
<td></td>
<td>In trickle mode $C_{\text{TM}}=0.1\mu\text{F}$</td>
<td>30</td>
<td></td>
<td></td>
<td>min</td>
</tr>
<tr>
<td>Recharge threshold at $V_{\text{BATT}}$</td>
<td>$V_{\text{RECHG}}$</td>
<td></td>
<td>4.0</td>
<td></td>
<td></td>
<td>V/cell</td>
</tr>
<tr>
<td>Recharge hysteresis</td>
<td></td>
<td></td>
<td>100</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>NTC low-temp rising threshold</td>
<td>$R_{\text{NTC}}=\text{NCP18X103}, 0^\circ\text{C}$</td>
<td></td>
<td>70.5</td>
<td>73.5</td>
<td>76.5</td>
<td>% of VREF33</td>
</tr>
<tr>
<td>NTC high-temp falling threshold</td>
<td>$R_{\text{NTC}}=\text{NCP18X103}, 50^\circ\text{C}$</td>
<td></td>
<td>27.5</td>
<td>29.5</td>
<td>31.5</td>
<td>% of VREF33</td>
</tr>
<tr>
<td>VIN min head-room (reverse blocking)</td>
<td></td>
<td>$\text{VIN}−V_{\text{BATT}}$</td>
<td>180</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
</tbody>
</table>
**ELECTRICAL CHARACTERISTICS (continued)**

$V_{IN} = 19V$, $T_A = 25^\circ C$, CELLS=0V, unless otherwise noted.

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Symbol</th>
<th>Condition</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\overline{EN}$ input low voltage</td>
<td></td>
<td></td>
<td>0.4</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$\overline{EN}$ input high voltage</td>
<td></td>
<td></td>
<td>1.8</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$EN$ input current</td>
<td></td>
<td>$EN=0V$</td>
<td></td>
<td>0.2</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$EN=4V$</td>
<td></td>
<td>4</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>Supply current (shutdown)</td>
<td></td>
<td></td>
<td></td>
<td>0.5</td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$EN=4V$,</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>Consider VREF33 pin</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>output current,</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$R_3=10k, R_{NTC}=10k$</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Supply current (quiescent)</td>
<td></td>
<td>$EN=0V$, CELLS=0V</td>
<td></td>
<td>2.0</td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>Thermal shutdown $^{(5)}$</td>
<td></td>
<td></td>
<td>150</td>
<td></td>
<td></td>
<td>°C</td>
</tr>
<tr>
<td>VREF33 output voltage</td>
<td></td>
<td></td>
<td>3.3</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>VREF33 load regulation</td>
<td></td>
<td>$I_{LOAD}=0$ to $10mA$</td>
<td>30</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
</tbody>
</table>

**Notes:**

$^{(5)}$ Guaranteed by design.
# PIN FUNCTIONS

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>VCC</td>
<td>IC supply voltage.</td>
</tr>
<tr>
<td>2</td>
<td>NTC</td>
<td>Thermistor Input. Connect a resistor from this pin to VREF33, and the thermistor from this pin to ground.</td>
</tr>
<tr>
<td>3</td>
<td>ACOK</td>
<td>Valid Input Supply Indicator. Open drain output. Add pull-up resistor. Logic LOW indicates the presence of a valid input supply.</td>
</tr>
<tr>
<td>4</td>
<td>CHGOK</td>
<td>Charging Status Indicator. Open drain output. Add pull-up resistor. Logic LOW indicates normal charging. Logic HIGH indicates either a completed charge process or suspended process because of some fault.</td>
</tr>
<tr>
<td>5</td>
<td>VREF33</td>
<td>Internal Linear Regulator, 3.3V Reference Output. Bypass to GND with a 1μF ceramic capacitor.</td>
</tr>
<tr>
<td>6</td>
<td>EN</td>
<td>On/Off Control Input.</td>
</tr>
<tr>
<td>7</td>
<td>CELLS</td>
<td>Command Input for the Number of Li-ion Cells. Connect to VREF33 or float for 3-cell operation. Ground for 2-cell operation.</td>
</tr>
<tr>
<td>8</td>
<td>COMPV</td>
<td>V-LOOP Compensation. Decouple this pin with a capacitor and a resistor.</td>
</tr>
<tr>
<td>9</td>
<td>COMPI</td>
<td>I-LOOP Compensation. Decouple this pin with a capacitor and a resistor.</td>
</tr>
<tr>
<td>10</td>
<td>BATT</td>
<td>Positive Battery Terminal.</td>
</tr>
<tr>
<td>11</td>
<td>CSP</td>
<td>Battery-Charge Current-Sense—Positive Input. Connect a resistor RS1 between CSP and BATT. The full charge current is: ( I_{CHG} = \frac{200\text{mV}}{RS1(\text{mΩ})} ).</td>
</tr>
<tr>
<td>12</td>
<td>GND</td>
<td>Ground. This pin is the voltage reference for the regulated output voltage. This node should be placed outside of the switching diode (D2) to the input ground path to prevent switching current spikes from inducing voltage noise into the part.</td>
</tr>
<tr>
<td>13</td>
<td>TMR</td>
<td>Set Safe Timer Period. 0.1μA current charges and discharges the external capacitor decoupled to GND. The capacitor value programs the timer period.</td>
</tr>
<tr>
<td>14</td>
<td>BST</td>
<td>Bootstrap. Requires a charged capacitor to drive the power switch’s gate above the supply voltage. Connect a capacitor between SW and BST pins to form a floating supply across the power switch driver.</td>
</tr>
<tr>
<td>15</td>
<td>SW</td>
<td>Switch Output.</td>
</tr>
<tr>
<td>16</td>
<td>VIN</td>
<td>Regulator Input Voltage. The MP26123 regulates a 9V-to-24V input to a voltage suitable for charging either a 2- or 3-cell Li-ion battery. Requires capacitors to prevent large voltage spikes from appearing at the input.</td>
</tr>
</tbody>
</table>
TYPICAL PERFORMANCE CHARACTERISTICS

$V_{IN}=19\text{V}, \ C_1=4.7\mu\text{F}, \ C_2=22\mu\text{F}, \ L=4.7\mu\text{H}, \ RS_1=100m\Omega, \ \text{Real/Simulation Battery Load, } T_A=25^\circ\text{C}, \ \text{unless otherwise noted.}$

- **2 Cells Battery Charge Curve**
- **2 Cells Charge Current vs. Battery Voltage**
- **3 Cells Battery Charge Curve**
- **3 Cells Charge Current vs. Battery Voltage**
- **NTC Control Window**
- **Breakdown Voltage**
- **Efficiency vs. $I_{CHG}$**
- **Efficiency vs. $V_{IN}$**
MP26123 – 2A, 24V INPUT, 600kHz 2-3 CELL SWITCHING LI-ION BATTERY CHARGER

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

\( V_{\text{IN}} = 19 \text{V}, \ C_1 = 4.7 \mu \text{F}, \ C_2 = 22 \mu \text{F}, \ L = 4.7 \mu \text{H}, \ RS_1 = 100 \text{m\Omega}, \ \text{Real/Simulation Battery Load, } T_A = 25^\circ\text{C}, \) unless otherwise noted.

- **BATT Float Voltage vs. \( V_{\text{IN}} \)**
  - 2 Cells

- **BATT Charge Full Voltage vs. Temperature**
  - 2 Cells

- **BATT Charge Full Voltage vs. Temperature**
  - 3 Cells

- **RDS\_ON vs. Temperature**

- **VREF33 vs. Temperature**
  - \( V_{\text{IN}} = 19 \text{V} \)

- **Constant Current Charge vs. Temperature**
  - 500mA 2Cells
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

$V_{IN}=19\,V$, $C1=4.7\,\mu F$, $C2=22\,\mu F$, $L=4.7\,\mu H$, $RS1=100\,m\Omega$, Real/Simulation Battery Load, $T_A=25^\circ C$, unless otherwise noted.

**Steady State Waveform**
- **Trickle Charge**
  - 2 Cells, $V_{BATT}=5\,V$

**Power On Waveform**
- 2 Cells, $I_{CHO}=2\,A$, $V_{BATT}=8\,V$

**Power Off Waveform**
- 2 Cells, $I_{CHO}=2\,A$, $V_{BATT}=8\,V$

**EN On Waveform**
- 2 Cells, $I_{CHO}=2\,A$, $V_{BATT}=8\,V$

**NTC Control**
- $V_{BATT}=7.4\,V$, CV Load

**Timer Out**
- 2 Cells, Real Battery
  - $V_{BATT}=7.7\,V$, $C_{TMR}=0.47\,nF$
FUNCTIONAL BLOCK DIAGRAM

Figure 1—Functional Block Diagram
OPERATION

The MP26123 is a peak-current–mode controlled switching charger for use with Li-ion batteries.

At the beginning of a cycle, M1 is off, and the COMP voltage is higher than the output of current sense amplifier, A1. The PWM comparator’s output is low, and the rising edge of the 600kHz CLK signal sets the RS flip-flop, which turns on M1, thus connecting the SW pin and the inductor to the input supply.

As the inductor current increases, the output of A1 increases. While the sum of A1’s output and the slope compensation remains below the COMP voltage, the falled edge of the CLK resets the RS flip-flop. When this signal exceeds the COMP voltage, the RS flip-flop resets and turns M1 off. The external switching diode D2 then conducts the inductor current.

The MP26123 uses COMP to select the smaller value of GMI and GMV to implement either current loop control or voltage loop control. Current loop control occurs when the battery voltage is low, which results in the saturation of the GMV output. GMI compares the charge current (as a voltage sensed through RS1) against the reference voltage to regulate the charge current to a constant value. When the battery voltage charges up to the reference voltage, the output of GMV goes low and initiates voltage loop control to control the duty cycle to regulate the output voltage.

The MP26123 has an internal linear regulator—VREF33—to power internal circuitry. It can also power external circuitry as long as the load does not exceed the maximum current (30mA). Connect a 1μF bypass capacitor from VREF33 to GND to ensure stability.

Charge Cycle (Mode change: Trickle → CC → CV)

At the start of a charging cycle, the MP26123 monitors VBATT. If VBATT is lower than the trickle-charge threshold, VTC (typically 3.0V/cell), the charging cycle will start in “trickle-charge mode” (10% of the RS1 programmed constant-charge current, ICC) until the battery voltage reaches VTC.

If the charge stays in the “trickle-charge mode” until “time out” condition is triggered, charging terminates and will not resume until the input power or EN signal refreshes. Otherwise, GMI regulates the charge current to the level set by RS1. The charger is operating at “constant current charging mode.” The duty cycle of the switcher is determined by the COMPI voltage that is regulated by the amplifier GMI.

When the battery voltage reaches constant-voltage–mode threshold, GMV regulates the COMP pin and the duty cycle for constant voltage mode. When the charge current drops to the battery-full threshold, IBF (typical 10% CC), the battery is defined as fully charged, the charger stops charging, and CHGOK goes high to indicate the charge-full condition. If the total charge time exceeds the timer period, the charging terminates at once and will resume when either the input power or EN signal can restart the charger.

Figure 2 shows the typical charge profile of MP26123.

![Figure 2—Li-Ion Battery Charge Profile](image-url)
Automatic Recharge
After the battery has completely recharged, the charger disables all blocks except the battery voltage monitor to limit leakage current. If the battery voltage falls below 4.0V/Cell, the chip will begin recharging using soft-start. The timer will then reset to avoid timer-related charging disruptions.

Charger Status Indication
MP26123 has two open-drain status outputs: ACOK and CHGOK. The ACOK pin goes low when the IC supply voltage VCC exceeds the under-voltage lockout threshold and the regulated voltage VIN is 300mV higher than V_BATT to make sure the regulator can operate normally. CHGOK indicates charge status. Table 1 describes ACOK and CHGOK outputs under different charge conditions.

Table 1—Charging Status Indication

<table>
<thead>
<tr>
<th>ACOK</th>
<th>CHGOK</th>
<th>Charger Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>low</td>
<td>low</td>
<td>In charging</td>
</tr>
<tr>
<td>low</td>
<td>high</td>
<td>End of charge, NTC fault, timer out, thermal shutdown, EN disable</td>
</tr>
<tr>
<td>high</td>
<td>high</td>
<td>VIN - V_BATT &lt; 0.3V, V_CC &lt; UVLO, CHGOK low</td>
</tr>
</tbody>
</table>

Timer Operation
MP26123 uses the internal timer to limit the charge period during trickle charge and total charge cycle. Once the charge time exceeds the time limit the MP26123 terminates charging. A good battery should fully recharge within the allotted time period; otherwise the battery has a fault. An external capacitor at the TMR pin programs the time period.

The trickle mode charge time is:

\[ T_{\text{TRICKLE \_ TMR}} = 30 \text{min} s \times \frac{C_{\text{TMR}}}{0.1 \mu F} \]

The total charge time is:

\[ T_{\text{TOTAL \_ TMR}} = 3 \text{hours} \times \frac{C_{\text{TMR}}}{0.1 \mu F} \]

When time-out occurs, charger is suspended. And only refresh the input power or EN signal or auto-recharge (The event that V_BATT falls through 4V/cell) can restart the charge cycle.

Negative Thermal Coefficient (NTC)
Thermistor
The MP26123 has a built-in NTC-resistance window comparator that allows the MP26123 to sense the battery temperature through the thermistor included in the battery pack. Connect a resistor with an appropriate value from VREF33 to the NTC pin, and connect the thermistor from the NTC pin to GND. A resistor divider determines the voltage on the NTC pin as a function of the battery temperature. Charging halts when the NTC voltage falls below the lower NTC window threshold. Charging resumes when the voltage is within the NTC window range.

Power Path Management
MP26123 is a stand-alone switching charger. Typically, the regulated input voltage VIN receives power from the adapter input, V_IN, through a diode that blocks the battery voltage to VCC. For power path application, however, V_IN powers the system and charges the battery simultaneously so the user can start-up a device with a drained battery when it is connected to an adapter. Replace the diode from the stand-alone switching charger circuit with a MOSFET to improve system efficiency and reduce voltage drop of the block device.

An additional MOSFET between VIN and the battery allows the battery to charge even in the absence of an adapter or connection to an invalid adapter. Figure 3 shows a typical application circuit with power-path management. When the adapter input is invalid or absent, the block diode is replaced by a MOSFET controlled by ACOK signal.
Figure 3—MP26123 with Power-Path Management
OPERATION FLOW CHART

Figure 4— Normal Charging Operation Flow Chart
NORMAL OPERATION

Charger "On", CHGOK is low

Charge Mode?

V_{BATT} \geq V_{BATT FULL} \rightarrow C.V.C

Yes

Battery Full?

V_{BATT TC} \leq V_{BATT FULL} \rightarrow C.C.C

Yes

No

V_{BATT} \leq V_{BATT TC} \rightarrow T.C.C

Yes

Fault Protection

Figure 5—Fault Protection Flow Chart
APPLICATION INFORMATION

Setting the Charge Current
The charge current of MP26123 is set by the sense resistor RS1 (See Typical Application), and determined with the following equation:

\[ I_{\text{CHG}} (\text{A}) = \frac{200 \text{mV}}{RS1 (\text{m}\Omega)} \]  

(1)

Selecting the Inductor
Use a 1µH to 10µH inductor for most applications. The inductance value can be derived from the following equation.

\[ L = \frac{V_{\text{OUT}} \times (V_{\text{IN}} - V_{\text{OUT}})}{V_{\text{IN}} \times \Delta I_{L} \times f_{\text{OSC}}} \]  

(2)

Where \( \Delta I_L \) is the inductor ripple current. Choose \( \Delta I_L \) to be approximately 30% of the maximum charge current, 2A. \( V_{\text{OUT}} \) is the 2- or 3-cell battery voltage.

The maximum inductor peak current is:

\[ I_{\text{L} \text{(MAX)}} = I_{\text{CHG}} + \frac{\Delta I_L}{2} \]  

(3)

Under light load conditions below 100mA, use a larger inductor value to improve efficiency.

Select an inductor with a DC resistance of less than 200mΩ to optimize efficiency.

NTC Function
As Figure 6 shows, the low temperature threshold and high-temperature threshold are preset internally to 73.5%·VREF33 and 29.5%·VREF33 using a resistive divider. For a given NTC thermistor, we can select appropriate R3 and R6 resistors to set the NTC window.

For the thermistor (NCP18XH103) noted in above electrical characteristic,
At 0°C, \( R_{\text{NTC, Cold}} = 27.445k \);  
At 50°C, \( R_{\text{NTC, Hot}} = 4.1601k \).

Assuming that the NTC window is between 0°C and 50°C, we can derive the following equations:

\[ \frac{R6 // R_{\text{NTC, Cold}}}{R3 + R6 // R_{\text{NTC, Cold}}} = \frac{V_{\text{TH, Low}}}{V_{\text{REF33}}} = 73.5\% \]  

(4)

\[ \frac{R6 // R_{\text{NTC, Hot}}}{R3 + R6 // R_{\text{NTC, Hot}}} = \frac{V_{\text{TH, High}}}{V_{\text{REF33}}} = 29.5\% \]  

(5)

According to equation (4) and equation (5), we determine \( R3 = 9.63k \) and \( R6 = 505k \).

For simplification, select \( R3 = 10k \) and \( R6 \) no connect to approximate the specification.

Selecting the Input Capacitor
The input capacitor reduces the surge current drawn from the input and also the switching noise from the device. Chose an input capacitor with an impedance at the switching frequency less than the input source impedance to prevent high-frequency–switching current. Use ceramic capacitors with X5R or X7R dielectrics with low ESR and small temperature coefficients. A 4.7µF capacitor is sufficient for most applications.

Selecting the Output Capacitor
The output capacitor limits output voltage ripple and ensures regulation loop stability. The output capacitor impedance should be low at the switching frequency. Use ceramic capacitors with X5R or X7R dielectrics.

PC Board Layout
Connect the high frequency and high current paths (GND, IN and SW) to the device with short, wide, and direct traces. Place the input capacitor as close as possible to the IN and GND pins. Place the external feedback resistors next to the FB pin. Keep the switching node SW short and away from the feedback network.
**PACKAGE INFORMATION**

**QFN16 (4 x 4mm)**

**NOTE:**
1) ALL DIMENSIONS ARE IN MILLIMETERS.
2) EXPOSED PADDLE SIZE DOES NOT INCLUDE MOLD FLASH.
3) LEAD COPLANARITY SHALL BE 0.10 MILLIMETER MAX.
4) JEDEC REFERENCE IS MO-220, VARIATION VGGC.
5) DRAWING IS NOT TO SCALE.