DESCRIPTION

The MP20051 is a low-dropout linear regulator that supplies up to 1A current with a 140mV dropout voltage. The externally-adjustable output voltage has a range of 0.8V to 5V from an input voltage of 2.5V to 5.5V.

An internal PMOS pass element allows for a low 110μA ground current, making the MP20051 suitable for battery-powered devices. Other features include low-power shutdown, and short-circuit and thermal protection. The MP20051 is available in 3mm x 3mm 8-pin QFN and SOIC8E packages.

FEATURES

- Up to 1A Output Current
- Low 140mV Dropout at 1A
- Low 110μA Ground Current
- Output Voltage Available from 0.8V to 5V
- Low Noise: 13μVRMS typical (10Hz to 100kHz)
- 63dB PSRR @1kHz
- Stable with Ceramic Capacitor
- Excellent Load/Line Transient Response
- Current Limiting and Thermal Protection
- Available in 3mm x 3mm 8-pin QFN and SOIC8E Packages

APPLICATIONS

- Notebook Computers
- Cordless Telephones
- Cellular Phones
- Wireless Communication Equipment
- Hand-Held Instruments

All MPS parts are lead-free and adhere to the RoHS directive. For MPS green status, please visit MPS website under Quality Assurance. “MPS” and “The Future of Analog IC Technology” are Registered Trademarks of Monolithic Power Systems, Inc.
### ORDERING INFORMATION

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
<th>Top Marking</th>
</tr>
</thead>
<tbody>
<tr>
<td>MP20051DQ*</td>
<td>QFN-8 (3mm x 3mm)</td>
<td>See Below</td>
</tr>
<tr>
<td>MP20051DN**</td>
<td>SOIC8E</td>
<td>See Below</td>
</tr>
</tbody>
</table>

* For Tape & Reel, add suffix –Z (e.g. MP20051DQ–Z); For RoHS Compliant Packaging, add suffix –LF (e.g. MP20051DQ–LF–Z)

** For Tape & Reel, add suffix –Z (e.g. MP20051DN–Z); For RoHS Compliant Packaging, add suffix –LF (e.g. MP20051DN–LF–Z)

#### TOP MARKING (MP20051DQ)

- **ABR**: product code of MP20051DQ;
- **Y**: year code;
- **LLL**: lot number;

#### TOP MARKING (MP20051DN)

- **MP20051**: product code of MP20051DN;
- **MPS**: MPS prefix:
- **Y**: year code;
- **WW**: week code:
- **LLLLLLLL**: lot number;
<table>
<thead>
<tr>
<th>PACKAGE REFERENCE</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>TOP VIEW</strong></td>
</tr>
<tr>
<td>OUT 1</td>
</tr>
<tr>
<td>OUT 2</td>
</tr>
<tr>
<td>FB 3</td>
</tr>
<tr>
<td>GND 4</td>
</tr>
<tr>
<td>EXPOSED PAD ON BACKSIDE</td>
</tr>
<tr>
<td>QFN-8 (3mm x 3mm)</td>
</tr>
<tr>
<td><strong>TOP VIEW</strong></td>
</tr>
<tr>
<td>OUT 1</td>
</tr>
<tr>
<td>OUT 2</td>
</tr>
<tr>
<td>FB 3</td>
</tr>
<tr>
<td>GND 4</td>
</tr>
<tr>
<td>EXPOSED PAD ON BACKSIDE</td>
</tr>
<tr>
<td>SOIC8E</td>
</tr>
</tbody>
</table>

**ABSOLUTE MAXIMUM RATINGS** (1)

- **VIN, EN, FB to GND**: -0.3V to +6V
- **OUT to GND**: -0.5V to (VIN + 0.5V)
- **Continuous Power Dissipation**: (T_A=25°C) (2)
  - QFN8 (3x3mm): 2.0W
  - SOIC8E: 2.0W
- **Junction Temperature**: 150°C
- **Storage Temperature Range**: -65°C to 150°C
- **Lead Temperature (Soldering, 10sec)**: 260°C

**ESD SUSCEPTIBILITY** (3)

- **HBM (Human Body Mode)**: 2kV
- **MM (Machine Mode)**: 200V

**Recommended Operating Conditions** (4)

- **Supply Input Voltage**: 2.5V to 5.5V
- **Enable Input Voltage**: 0V to 5.5V
- **Operating Junction Temp. (T_J)**: -40°C to +125°C

**Thermal Resistance** (5) \( \theta_{JA} \) \( \theta_{JC} \)

- **QFN-8 (3mmx3mm)**: 50°C/W
- **SOIC8E**: 50°C/W

**Notes:**

1. Exceeding these ratings may damage the device.
2. The maximum allowable power dissipation is a function of the maximum junction temperature \( T_J \) (MAX), the junction-to-ambient thermal resistance \( \theta_{JA} \), and the ambient temperature \( T_A \). The maximum allowable continuous power dissipation at any ambient temperature is calculated by \( P_{D\ (MAX)} = (T_J\ (MAX) - T_A)/\theta_{JA} \). Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown. Internal thermal shutdown circuitry protects the device from permanent damage.
3. Devices are ESD sensitive. Handling precaution recommended.
4. The device is not guaranteed to function outside of its operating conditions.
5. Measured on JESD51-7, 4-layer PCB.
### ELECTRICAL CHARACTERISTICS

$V_{IN}=V_{OUT}+0.5V$ or $V_{IN}=2.5V$, $EN=V_{IN}$, Typical values are at $T_A=25^\circ C$, unless otherwise specified

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Condition</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Voltage</td>
<td>$V_{IN}$ rising</td>
<td>2.5</td>
<td>5.5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Input Under Voltage Lockout</td>
<td>$I_{OUT}=1mA$, $T_A=25^\circ C$</td>
<td>-2</td>
<td>2</td>
<td>%</td>
<td></td>
</tr>
<tr>
<td>Hysteresis of UVLO</td>
<td>$I_{OUT}=1mA$, $-40^\circ C \leq T_A \leq 85^\circ C$</td>
<td>-3</td>
<td>3</td>
<td>%</td>
<td></td>
</tr>
<tr>
<td>FB Voltage</td>
<td>$V_{OUT}=0.8V$, $I_{OUT}=1mA$</td>
<td>0.784</td>
<td>0.8</td>
<td>0.816</td>
<td>V</td>
</tr>
<tr>
<td>Output Voltage Accuracy</td>
<td>$V_{OUT}=0.8V$, $I_{OUT}=1mA$</td>
<td>1.95</td>
<td>2.25</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Maximum Output Current</td>
<td>Continuous, $V_{IN} \geq 2.5V$, $V_{OUT}=2.5V$</td>
<td>1</td>
<td>A</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Short-Circuit Current Limit</td>
<td>$V_{OUT}=0$, $V_{IN} \geq 2.5V$</td>
<td>1.6</td>
<td></td>
<td>A</td>
<td></td>
</tr>
<tr>
<td>In-Regulation Current Limit</td>
<td>$V_{OUT}=2.5V$, $V_{OUT}$ within 4% of normal output voltage $V_{IN}=5.5V$</td>
<td>1.4</td>
<td>2.2</td>
<td>3.0</td>
<td>A</td>
</tr>
<tr>
<td>Ground Current</td>
<td>$I_{OUT}=0.1mA$, $V_{OUT}=2.5V$</td>
<td>110</td>
<td></td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td>Dropout Voltage ($6$)</td>
<td>$I_{OUT}=1A$, $V_{OUT}=2.5V$</td>
<td>140</td>
<td>280</td>
<td>mV</td>
<td></td>
</tr>
<tr>
<td>Dropout Voltage ($6$)</td>
<td>$I_{OUT}=750mA$, $V_{OUT}=2.5V$</td>
<td>105</td>
<td>210</td>
<td>mV</td>
<td></td>
</tr>
<tr>
<td>Dropout Voltage ($6$)</td>
<td>$I_{OUT}=500mA$, $V_{OUT}=2.5V$</td>
<td>70</td>
<td>140</td>
<td>mV</td>
<td></td>
</tr>
<tr>
<td>Line Regulation ($7$)</td>
<td>$V_{OUT}=2.5V$, $V_{IN}$ from $V_{OUT}+0.5V$ to $5.5V$, $I_{OUT}=100mA$,</td>
<td>-0.15</td>
<td>0.15</td>
<td>%/V</td>
<td></td>
</tr>
<tr>
<td>Load Regulation ($8$)</td>
<td>$I_{OUT}$ from 100mA to 1A, $V_{OUT}=2.5V$</td>
<td>0.3</td>
<td></td>
<td>%</td>
<td></td>
</tr>
<tr>
<td>Output Voltage Noise</td>
<td>$I_{OUT}=100mA$, $f$ ranges from 10Hz to 100kHz</td>
<td>$V_{OUT}=1.1V$</td>
<td>13</td>
<td>μVRMS</td>
<td></td>
</tr>
<tr>
<td>PSRR</td>
<td>$V_{IN}=2.5V$, $V_{OUT}=1.1V$, $I_{OUT}=1A$</td>
<td>f=100Hz</td>
<td>65</td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td>Shutdown Supply Current</td>
<td>$V_{IN}=+5.5V$</td>
<td>0.2</td>
<td></td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td>EN Pin Current, Enabled</td>
<td>$V_{IN}=V_{EN}=+5.5V$</td>
<td>0.2</td>
<td></td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td>Feedback Pin Current</td>
<td>$V_{IN}=+5.5V$, $V_{FB}=6V$</td>
<td>0.02</td>
<td></td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td>Startup Time</td>
<td>$V_{OUT}(NOM)=2.5V$, $C_{OUT}=4.7\mu F$, $V_{OUT}=0%$ to $90%V_{OUT}(NOM)$</td>
<td>45</td>
<td></td>
<td>μs</td>
<td></td>
</tr>
<tr>
<td>EN PIN Threshold</td>
<td>$EN$ Logic High</td>
<td>1.5</td>
<td></td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>EN PIN Threshold</td>
<td>$EN$ Logic Low</td>
<td>0.4</td>
<td></td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>Thermal Shutdown Temperature</td>
<td>Typical thermal hysteresis =20°C</td>
<td>150</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Notes:**

6) Dropout Voltage is defined as the input to output differential when the output voltage drops 100mV below its nominal value.

7) Line Regulation $= \left( \frac{V_{OUT}[V_{IN\text{MAX}}] - V_{OUT}[V_{IN\text{MIN}}]}{V_{IN\text{MAX}} - V_{IN\text{MIN}}} \right) \times \% / V$

8) Load Regulation $= \left( \frac{V_{OUT}[I_{OUT\text{MAX}}] - V_{OUT}[I_{OUT\text{MIN}}]}{V_{OUT}(NOM)} \right) \times \%$
## PIN FUNCTIONS

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Name</th>
<th>Pin Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>1, 2</td>
<td>VOUT</td>
<td>Regulator output. Bypass with a standard 4.7μF ceramic capacitor to GND. Connect all the pins together externally.</td>
</tr>
<tr>
<td>3</td>
<td>FB</td>
<td>Feedback Input. Connect FB to the center point of the external resistor divider. The feedback threshold voltage is 0.8V.</td>
</tr>
<tr>
<td>4</td>
<td>GND</td>
<td>Ground. Connect exposed pad to GND plane for optimal thermal performance.</td>
</tr>
<tr>
<td>5</td>
<td>EN</td>
<td>Regulator Enable Control Input. Drive EN above 1.5V to turn on the MP20051. Drive EN below 0.4V to turn it off. Do not float the EN pin.</td>
</tr>
<tr>
<td>6</td>
<td>NC</td>
<td>No Connection. Leave this NC pin open.</td>
</tr>
<tr>
<td>7, 8</td>
<td>VIN</td>
<td>Regulator Input. Supply voltage ranges from 2.5V to 5.5V. Bypass with 2.2μF capacitor. These pins must be externally connected for proper operation even if they are internally connected.</td>
</tr>
</tbody>
</table>
TYPICAL PERFORMANCE CHARACTERISTICS

\( V_{IN} = 2.5V, V_{OUT} = 1.1V, C_{IN}=2.2\mu F, C_{OUT}=4.7\mu F, T_A = 25^\circ C, \) unless otherwise noted.

- **Quiescent Current vs. Temperature**
- **Quiescent Current vs. Input Voltage**
- **Dropout Voltage vs. Temperature**
- **Dropout Voltage vs. Output Current**
- **EN threshold vs. Input Voltage**
- **EN threshold vs. Temperature**
- **Current Limit vs. Temperature**
- **Current Limit vs. Input Voltage**
- **Line Regulation**
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

$V_{IN} = 2.5\, \text{V}$, $V_{OUT} = 1.1\, \text{V}$, $C_{IN}=2.2\, \mu\text{F}$, $C_{OUT}=4.7\, \mu\text{F}$, $T_A = 25\, ^\circ\text{C}$, unless otherwise noted.

- **Load Regulation**
- **Output Voltage Accuracy vs. Temperature**
- **PSRR Vs. Frequency**
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

$V_{IN} = 2.5V, V_{OUT} = 1.1V, C_{IN}=2.2\mu F, C_{OUT}=4.7\mu F, T_A = 25^\circ C$, unless otherwise noted.

- **Input Power Start Up**
  - $V_{IN}=2.5V, V_{OUT}=1.1V, I_{OUT}=1A$, with Resistor Load

- **Input Power Shutdown**
  - $V_{IN}=2.5V, V_{OUT}=1.1V, I_{OUT}=1A$, with Resistor Load

- **EN Start Up**
  - $V_{EN}=2.5V, V_{OUT}=1.1V, I_{OUT}=1A$, with Resistor Load

- **EN Shutdown**
  - $V_{EN}=2.5V, V_{OUT}=1.1V, I_{OUT}=1A$, with Resistor Load

- **Line Transient**
  - $V_{IN}=2.5V$ to $4V, V_{OUT}=1.1V, I_{OUT}=10mA$, with Resistor Load

- **Line Transient**
  - $V_{IN}=2.5V$ to $4V, V_{OUT}=1.1V, I_{OUT}=1A$, with Resistor Load

- **Load Transient**
  - $V_{IN}=2.5V, V_{OUT}=1.1V, I_{OUT}=10mA$ to $1A$, with Resistor Load

- **Over Current Protection Steady State**
  - $V_{IN}=2.5V, V_{OUT}=1.1V$
FUNCTIONAL BLOCK DIAGRAM

Figure 1—Functional Block Diagram
OPERATION

The MP20051 is a low-dropout linear regulator that can supply up to 1A current, which makes it suitable for very low voltage, low quiescent, low noise, and high PSRR applications such as wireless LAN transceivers, notebook computers, smartphones, and other low-power electronics.

The MP20051 uses an internal PMOS as the pass element and includes both thermal shutdown and an internal current-limiting circuit.

**Dropout Voltage**
Dropout voltage is the minimum input to output differential voltage required for the regulator to maintain an output voltage within 100mV of its nominal value. Because the PMOS pass element behaves as a low-value resistor, the dropout voltage of MP20051 is only 140mV.

**Shutdown**
The MP20051 can be switched ON or OFF by a logic input at the EN pin: Logic high turns the regulator on and logic low turns it off. Tie the EN pin to VIN if the application does not require the shutdown feature. Do not float the EN pin.

**Current Limit**
The MP20051 includes a current limit structure that monitors and controls the PMOS gate voltage to limit the guaranteed maximum output current to 1.6A.

**Thermal Protection**
Thermal protection turns off the PMOS when the junction temperature exceeds 150°C, allowing the IC to cool. When the IC’s junction temperature drops by 20°C, the PMOS will turn on again. Thermal protection limits total power dissipation in the MP20051. For reliable operation, limit the junction temperature to a maximum of 125°C.

**Load-Transient Considerations**
The output response of the load-transient consists of a transient response and DC shift—the MP20051’s excellent load regulation effectively limits the DC shift. The output voltage transient depends on the output capacitor’s value and ESR. Increasing the capacitance and decreasing the ESR will improve the transient response.
APPLICATION INFORMATION

Setting the Output Voltage

The MP20051 has an externally-set output voltage with a range of 0.8V to 5V given a 2.5V to 5.5V input. Set the output voltage using a resistive voltage divider from the output voltage to the FB pin. The result of the voltage divider at the FB pin is:

\[ V_{FB} = \frac{R2}{R1+R2} \times V_{OUT} \]

Where \( V_{FB} \) is the feedback threshold voltage (\( V_{FB} = 0.8V \)), and \( V_{OUT} \) is the output voltage. Thus the output voltage is:

\[ V_{OUT} = 0.8 \times \frac{R1+R2}{R2} \]

R2 can go as high as 100kΩ, but typical applications use 10kΩ. After selecting R2, R1 is determined by:

\[ R1 = R2 \times \left( \frac{V_{OUT} - V_{FB}}{V_{FB}} \right) \]

For example, for a 1.1V output voltage, R2 is 10kΩ, and R1 is 3.75kΩ. You can select a standard 3.75kΩ (±1%) resistor for R1.

Power Dissipation

The power dissipation for any package depends on the thermal resistance of the case and circuit board, the temperature differential between the junction and ambient air, and the rate of air flow. The power dissipation across the device can be represented by the equation:

\[ P = (V_{IN} - V_{OUT}) \times I_{OUT} \]

The allowable power dissipation can be calculated using the following equation:

\[ P_{(MAX)} = \frac{(T_{Junction} - T_{Ambient})}{\theta_{JA}} \]

Where \( (T_{Junction} - T_{Ambient}) \) is the temperature differential between the junction and the surrounding environment, \( \theta_{JA} \) is the thermal resistance from the junction to the ambient environment. Connecting the exposed GND pad to a large ground pad or plane helps to channel away heat.

Output Capacitor Selection

The MP20051 is specifically designed to work with a standard ceramic output capacitor to save space and improve performance. Use a 4.7µF ceramic capacitor for most applications. Larger output capacitors will improve load transient response and reduce noise at the cost of increased size.
PACKAGE INFORMATION

QFN-8 (3mm × 3mm)

TOP VIEW

BOTTOM VIEW

SIDE VIEW

DETAIL A

NOTE:
1) ALL DIMENSIONS ARE IN MILLIMETERS.
2) EXPOSED PADDLE SIZE DOES NOT INCLUDE MOLD FLASH.
3) LEAD COPLANARITY SHALL BE 0.08 MILLIMETER MAX.
4) DRAWING CONFORMS TO JEDEC MO-229, VARIATION VEEC-2.
5) DRAWING IS NOT TO SCALE.

RECOMMENDED LAND PATTERN
SOIC8E

TOP VIEW

BOTTOM VIEW

SIDE VIEW

DETAIL “A”

RECOMMENDED LAND PATTERN

NOTE:
1) CONTROL DIMENSION IS IN INCHES. DIMENSION IN BRACKET IS IN MILLIMETERS.
2) PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
3) PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
4) LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.004” INCHES MAX.
5) DRAWING CONFORMS TO JEDEC MS-012, VARIATION BA.
6) DRAWING IS NOT TO SCALE.

NOTICE: The information in this document is subject to change without notice. Users should warrant and guarantee that third party Intellectual Property rights are not infringed upon when integrating MPS products into any application. MPS will not assume any legal responsibility for any said applications.