



EVQ2179B-QH-00A

3A, 6V, 710kHz,
Synchronous Step-Down Converter
Evaluation Board, AEC-Q100 Qualified

DESCRIPTION

The EVQ2179B-QH-00A is an evaluation board designed to demonstrate the capabilities of the MPQ2179BGQHE-AEC1, a monolithic, step-down switch-mode converter with built-in internal power MOSFETs.

The MPQ2179B achieves 3A of output current (I_{OUT}) from a 2.5V to 6V input voltage (V_{IN}) range, with excellent load and line regulation. The output voltage (V_{OUT}) can be regulated to as low as 0.5V. A 100% maximum duty cycle can be reached in low-dropout (LDO) mode.

Constant-on-time (COT) control offers a simpler control loop and faster transient response. By using V_{IN} feed-forward, the MPQ2179B maintains a nearly constant switching frequency (f_{SW}) across the input and load ranges. Forced

continuous conduction mode (FCCM) provides a stable frequency and a lower output ripple.

An open-drain power good (PG) signal indicates a nominal voltage after the soft-start time. PG pulls high if the feedback voltage (V_{FB}) reaches 90% of the reference voltage (V_{REF}); PG pulls to GND when V_{FB} drops to 84% of V_{REF} .

Full protection features include cycle-by-cycle current limiting, short-circuit protection (SCP), reliable over-voltage protection (OVP), and thermal protection with automatic recovery.

The EVQ2179B-QH-00A is a fully assembled and tested evaluation board. The MPQ2179B is available in a QFN-8 (1.5mmx2mm) package, and it is available in AEC-Q100 Grade 1.

PERFORMANCE SUMMARY

Specifications are at $T_A = 25^\circ\text{C}$, unless otherwise noted.

Parameters	Conditions	Value
Input voltage (V_{IN}) range		2.5V to 6V
Output voltage (V_{OUT})	$V_{IN} = 2.5\text{V to } 6\text{V}$, $I_{OUT} = 0\text{A to } 3\text{A}$	0.5V
Maximum output current (I_{OUT})	$V_{IN} = 2.5\text{V to } 6\text{V}$	3A
Typical efficiency	$V_{IN} = 3.3\text{V}$, $V_{OUT} = 0.5\text{V}$, $I_{OUT} = 3\text{A}$	69%
Peak efficiency	$V_{IN} = 2.5\text{V}$, $V_{OUT} = 0.5\text{V}$, $I_{OUT} = 0.3\text{A}$	88.1%
Switching frequency (f_{SW})		710kHz

EVQ2179B-QH-00A EVALUATION BOARD



LxWxH (6.3cmx6.3cmx1cm)

Board Number	MPS IC Number
EVQ2179B-QH-00A	MPQ2179BGQHE-AEC1

QUICK START GUIDE

The EVQ2179B-QH-00A evaluation board is easy to set up and use to evaluate the MPQ2179B's performance. For proper measurement equipment set-up, refer to Figure 2 on page 4 and follow the steps below:

1. Preset the power supply (V_{IN}) between 2.5V and 6V, then turn off the power supply.
2. Set the load current between 0A and 3A. Electronic loads represent a negative impedance to the regulator, and setting a current too high may trigger cycle-by-cycle over-current protection (OCP).
3. If longer cables (>0.5m total) are used between the source and the evaluation board, place a damping capacitor at the input terminals.
4. Connect the power supply terminals to:
 - a. Positive (+): VEMI
 - b. Negative (-): GND
5. Connect the load terminals to:
 - a. Positive (+): VOUT
 - b. Negative (-): GND
6. After making the connections, turn on the power supply.
7. To use the enable function, apply a digital input to the EN pin. Drive EN above 0.9V to turn the regulator on; drive EN below 0.65V to turn the regulator off. If the enable function is not used, EN can be connected directly to V_{IN} .
8. The external resistor divider sets the output voltage (V_{OUT}) (see Figure 1).

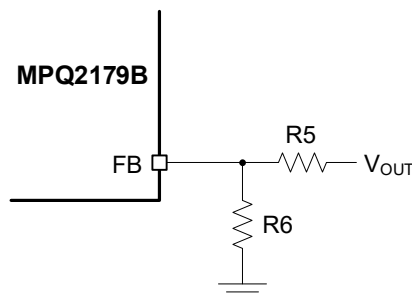


Figure 1: Feedback Divider Network with Adjustable Output

R5 is selected to be between 10k Ω and 100k Ω . R6 can then be calculated with Equation (1):

$$R6 = \frac{R5}{\frac{V_{OUT}}{0.5} - 1} \quad (1)$$

For a 0.5V V_{OUT} , R5 should be 0 Ω , and R6 should be NS.

Refer to the Application Information section in the MPQ2179B's datasheet to calculate the inductance and output capacitance for different V_{OUT} values. Figure 2 on page 4 shows the proper measurement equipment set-up.

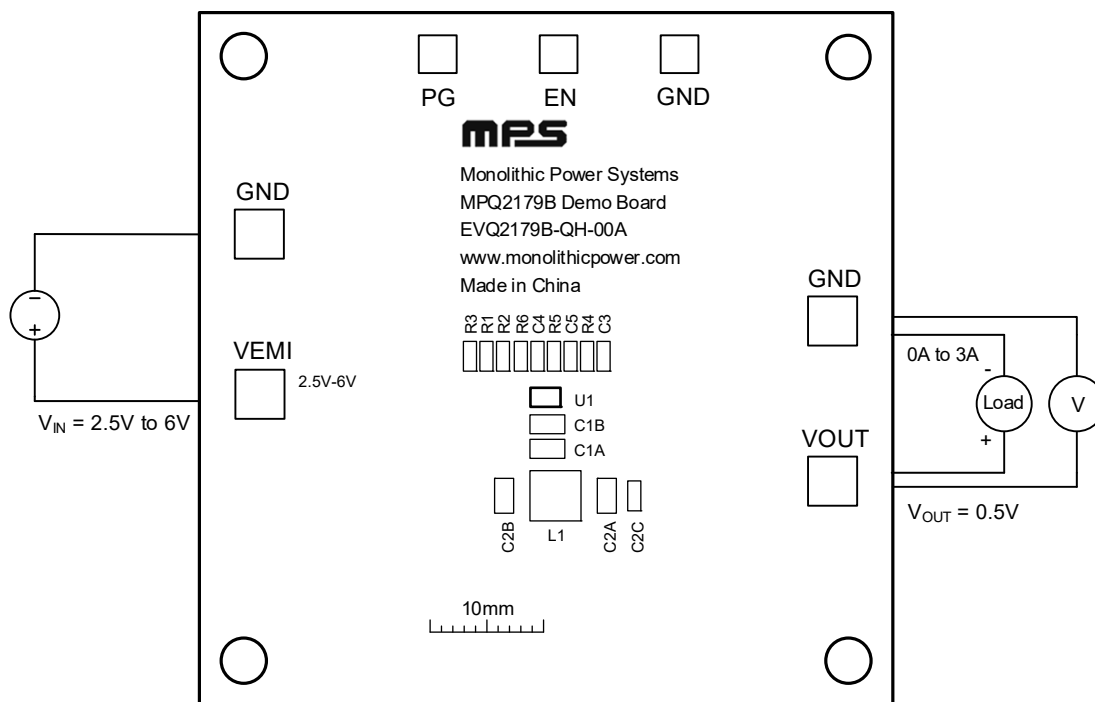


Figure 2: Measurement Equipment Set-Up

EVALUATION BOARD SCHEMATIC

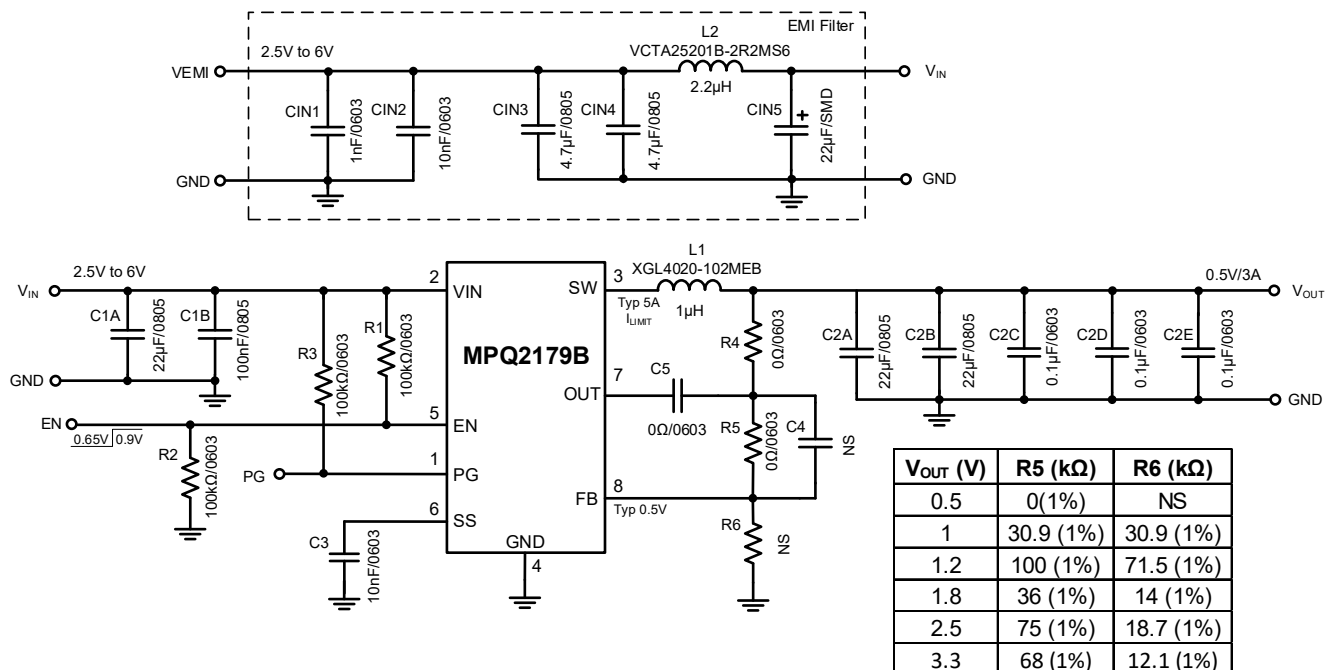
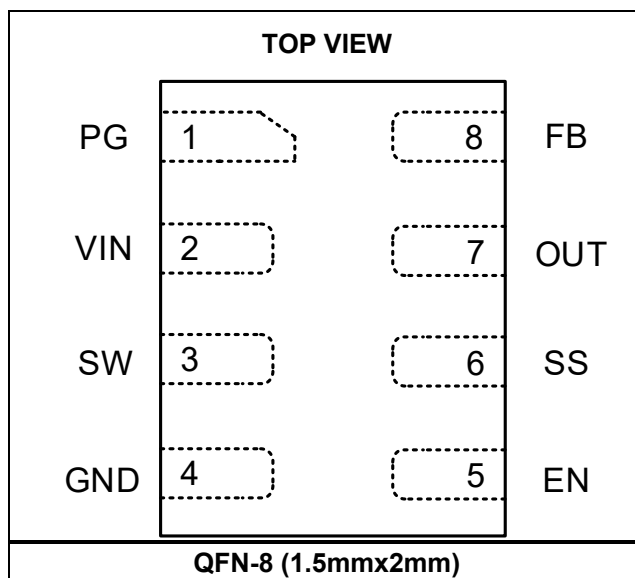


Figure 3: Evaluation Board Schematic

PACKAGE REFERENCE



EVQ2179B-QH-00A BILL OF MATERIALS

Qty	Ref	Value	Description	Package	Manufacturer	Manufacturer PN
1	CIN1	1nF	Ceramic capacitor, 50V, C0G	0603	Murata	GRM1885C1H102JA01D
1	CIN2	10nF	Ceramic capacitor, 50V, X7R	0603	Murata	GRM188R71H103KA01D
2	CIN3, CIN4	4.7μF	Ceramic capacitor, 16V, X7R	0805	Murata	GCM21BR71C475KA73L
1	CIN5	22μF	Electrical capacitor, 63V	SMD	Jianghai	VTD-63V22
1	C1A	22μF	Ceramic capacitor, 16V, X5R	0805	Murata	GRM21BR61C226ME44L
1	C1B	100nF	Ceramic capacitor, 16V, X7R	0805	Murata	GRM219R71C104KA01D
3	C2C, C2D, C2E	0.1μF	Ceramic capacitor, 16V, X7R	0603	Murata	GRM188R71C104KA01D
2	C2A, C2B	22μF	Ceramic capacitor, 6.3V, X5R	0805	Murata	GRM21BR60J226ME39L
1	C3	10nF	Ceramic capacitor, 50V, X7R	0603	Murata	GRM188R71H103KA01D
2	C4, R6	NS				
3	C5, R4, R5	0Ω	Film resistor, 1%;	0603	Yageo	RC0603FR-070RL
3	R1, R2, R3	100kΩ	Film resistor, 1%	0603	Yageo	RC0603FR-07100KL
1	L1	1μH	Inductor, 8.2mΩ, 8.8A	SMD	Coilcraft	XGL4020-102MEB
1	L2	2.2μH	Inductor, 70mΩ, 3.5A	SMD	Cyntec	VCTA25201B-2R2MS6
4	VEMI, GND, VOUT, GND	2mm	Golden pin	DIP	Custom ⁽¹⁾	
3	EN, PG, GND	1mm	Golden pin	DIP	Custom ⁽¹⁾	
1	U1	MPQ2179B-AEC1	3A, 6V, step-down converter	QFN-8 (1.5mmx2mm)	MPS	MPQ2179BGQHE-AEC1

Note:

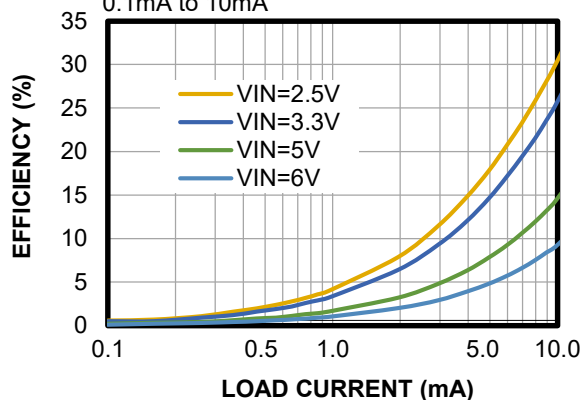
1) MPS custom-produces these pins. Contact an MPS FAE for more information.

EVB TEST RESULTS

Performance curves and waveforms are tested on the evaluation board. $V_{IN} = 3.3V$, $V_{OUT} = 0.5V$, $T_A = 25^\circ C$, unless otherwise noted.

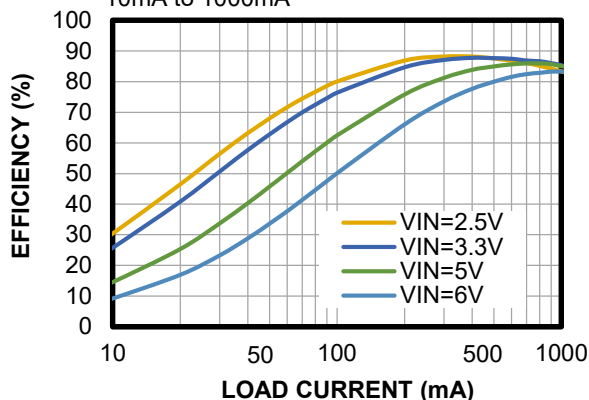
Efficiency vs. Load Current

FCCM, $V_{OUT} = 0.5V$, $f_{SW} = 710kHz$,
 $L = 1\mu H$ (DCR = $8.2m\Omega$),
 0.1mA to 10mA



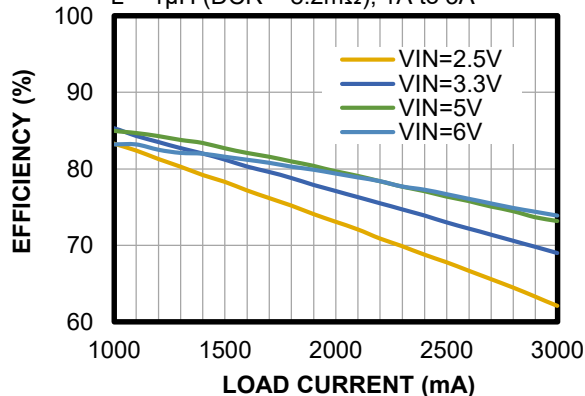
Efficiency vs. Load Current

FCCM, $V_{OUT} = 0.5V$, $f_{SW} = 710kHz$,
 $L = 1\mu H$ (DCR = $8.2m\Omega$),
 10mA to 1000mA



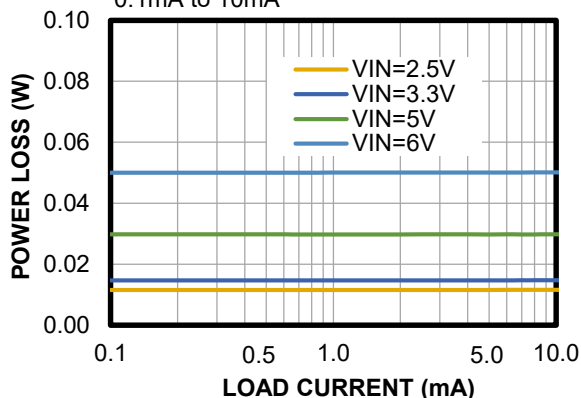
Efficiency vs. Load Current

FCCM, $V_{OUT} = 0.5V$, $f_{SW} = 710kHz$,
 $L = 1\mu H$ (DCR = $8.2m\Omega$), 1A to 3A



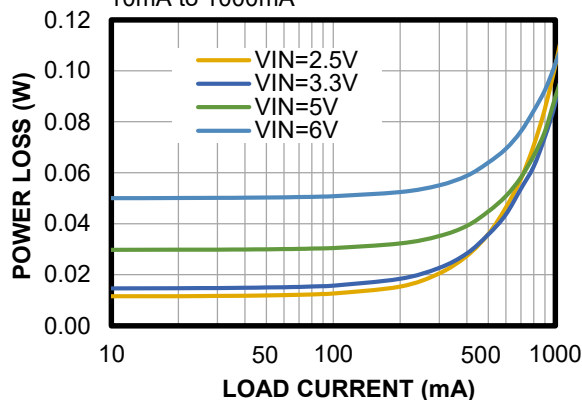
Power Loss vs. Load Current

FCCM, $V_{OUT} = 0.5V$, $f_{SW} = 710kHz$,
 $L = 1\mu H$ (DCR = $8.2m\Omega$),
 0.1mA to 10mA



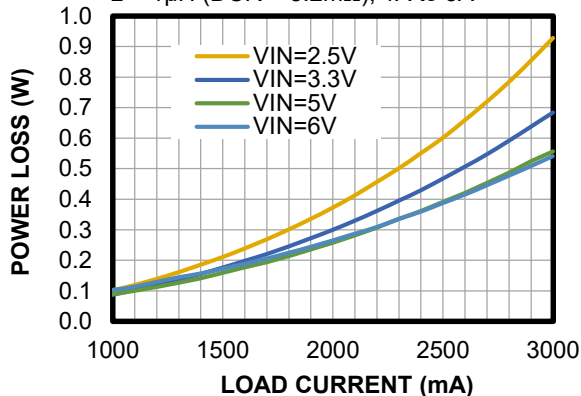
Power Loss vs. Load Current

FCCM, $V_{OUT} = 0.5V$, $f_{SW} = 710kHz$,
 $L = 1\mu H$ (DCR = $8.2m\Omega$),
 10mA to 1000mA



Power Loss vs. Load Current

FCCM, $V_{OUT} = 0.5V$, $f_{SW} = 710kHz$,
 $L = 1\mu H$ (DCR = $8.2m\Omega$), 1A to 3A

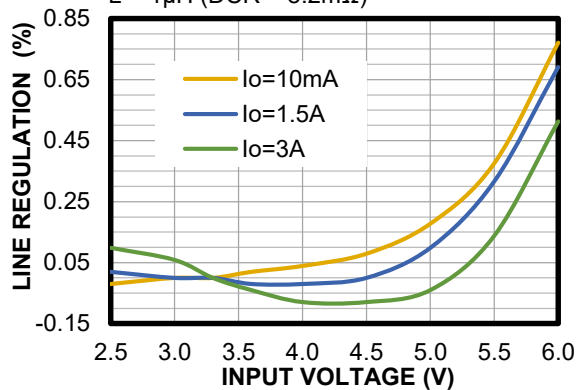


EVB TEST RESULTS *(continued)*

Performance curves and waveforms are tested on the evaluation board. $V_{IN} = 3.3V$, $V_{OUT} = 0.5V$, $T_A = 25^{\circ}C$, unless otherwise noted.

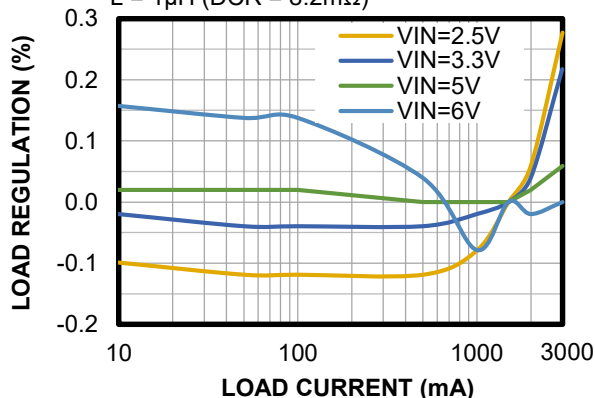
Line Regulation

FCCM, $V_{OUT} = 0.5V$, $f_{SW} = 710kHz$,
 $L = 1\mu H$ (DCR = $8.2m\Omega$)



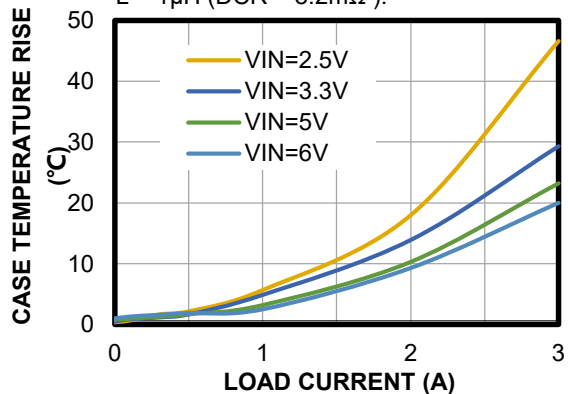
Load Regulation

FCCM, $V_{OUT} = 0.5V$, $f_{SW} = 710kHz$,
 $L = 1\mu H$ (DCR = $8.2m\Omega$)



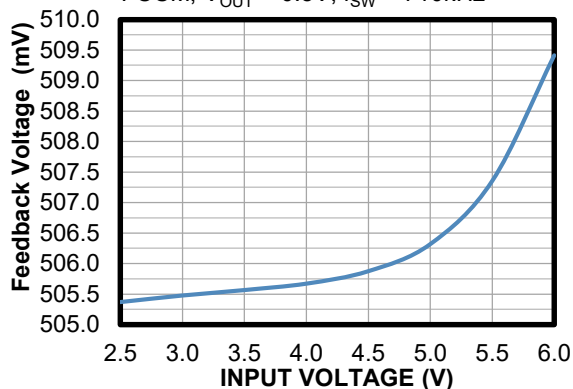
Case Temperature Rise

FCCM, $V_{OUT} = 0.5V$, $f_{SW} = 710kHz$,
 $L = 1\mu H$ (DCR = $8.2m\Omega$)



Feedback Voltage vs. Input Voltage

FCCM, $V_{OUT} = 0.5V$, $f_{SW} = 710kHz$

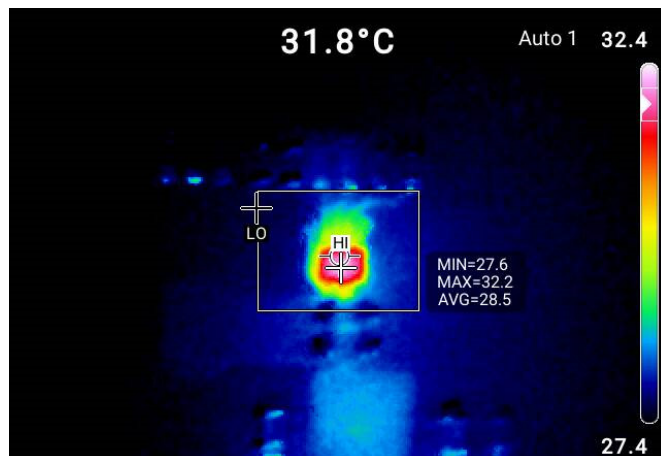


EVB TEST RESULTS *(continued)*

Performance curves and waveforms are tested on the evaluation board. $V_{IN} = 3.3V$, $V_{OUT} = 0.5V$, $T_A = 25^{\circ}C$, unless otherwise noted.

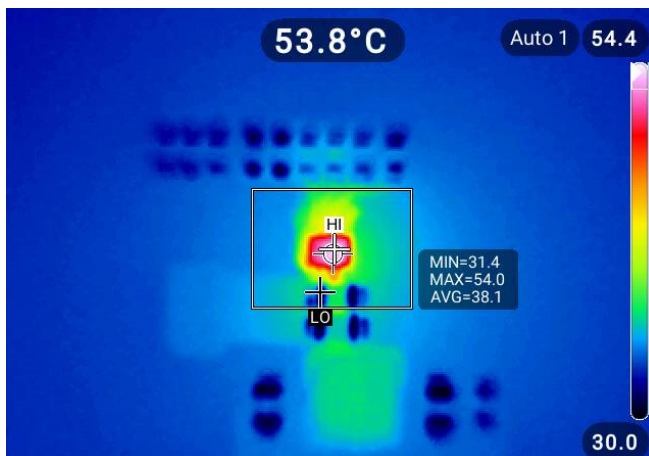
Thermal Performance

$I_{OUT} = 1.5A$, no forced airflow, $T_{CASE} = 32.2^{\circ}C$



Thermal Performance

$I_{OUT} = 3A$, no forced airflow, $T_{CASE} = 54^{\circ}C$

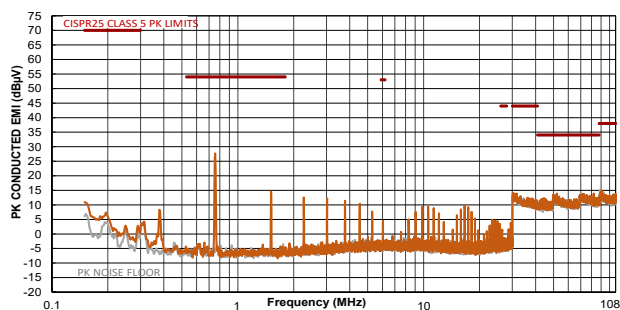


EVB TEST RESULTS (continued)

Performance curves and waveforms are tested on the evaluation board. $V_{IN} = 6V$, $V_{OUT} = 0.5V$, $T_A = 25^{\circ}C$, unless otherwise noted.

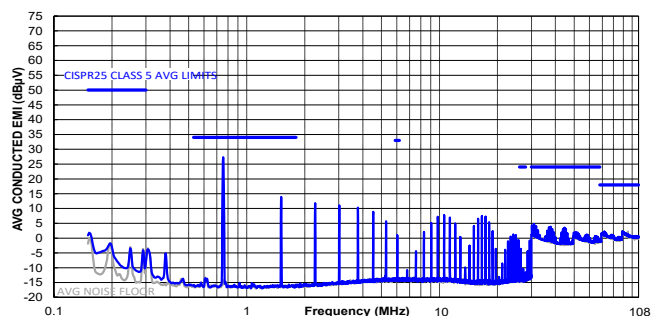
CISPR25 Class 5 Peak Conducted Emissions

150kHz to 108MHz



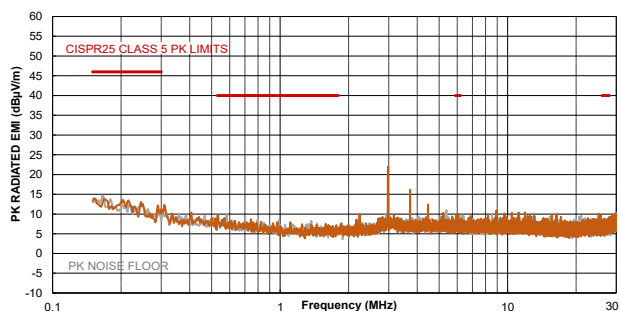
CISPR25 Class 5 Average Conducted Emissions

150kHz to 108MHz



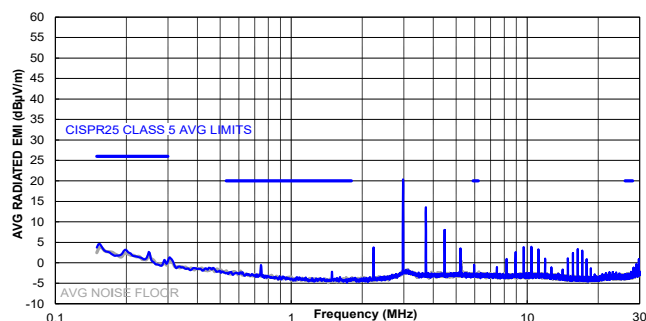
CISPR25 Class 5 Peak Radiated Emissions

150kHz to 30MHz



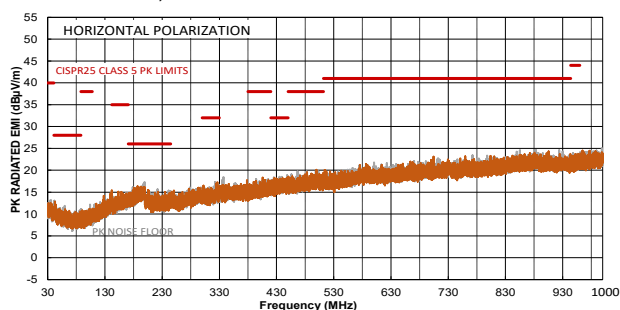
CISPR25 Class 5 Average Radiated Emissions

150kHz to 30MHz



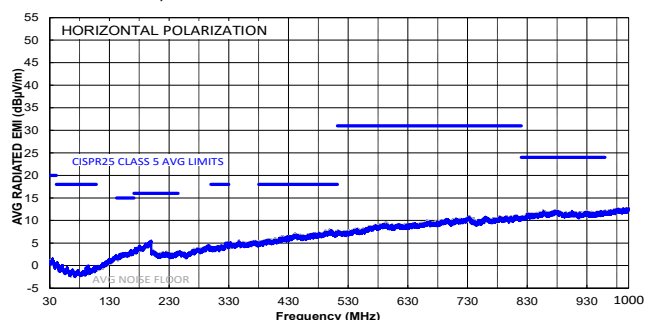
CISPR25 Class 5 Peak Radiated Emissions

Horizontal, 30MHz to 1GHz



CISPR25 Class 5 Average Radiated Emissions

Horizontal, 30MHz to 1GHz

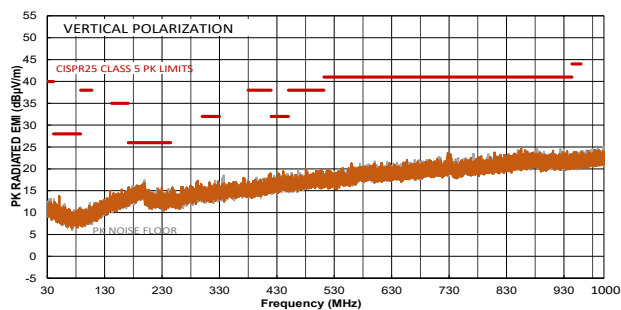


EVB TEST RESULTS *(continued)*

Performance curves and waveforms are tested on the evaluation board. $V_{IN} = 6V$, $V_{OUT} = 0.5V$, $T_A = 25^{\circ}C$, unless otherwise noted.

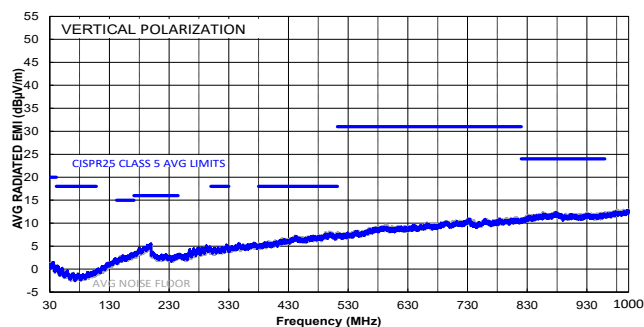
CISPR25 Class 5 Peak Radiated Emissions

Vertical, 30MHz to 1GHz



CISPR25 Class 5 Average Radiated Emissions

Vertical, 30MHz to 1GHz

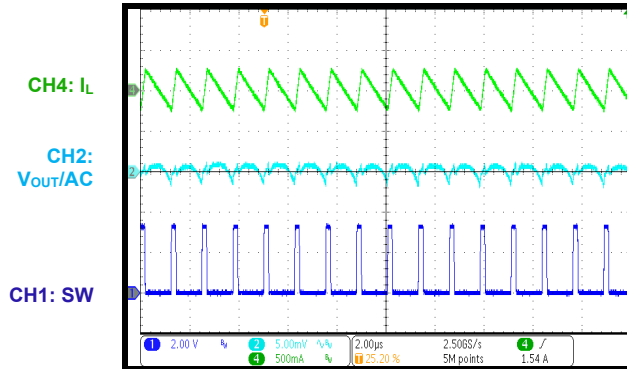


EVB TEST RESULTS *(continued)*

Performance curves and waveforms are tested on the evaluation board. $V_{IN} = 3.3V$, $V_{OUT} = 0.5V$, $T_A = 25^{\circ}C$, unless otherwise noted.

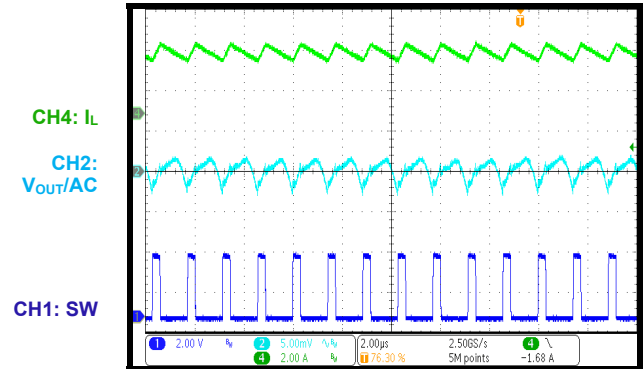
Steady State

$I_{OUT} = 0A$, FCCM



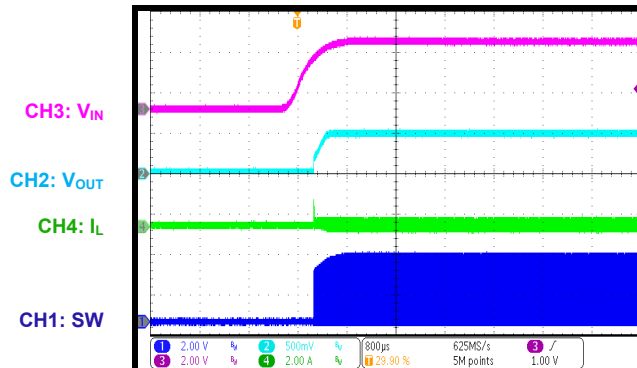
Steady State

$I_{OUT} = 3A$



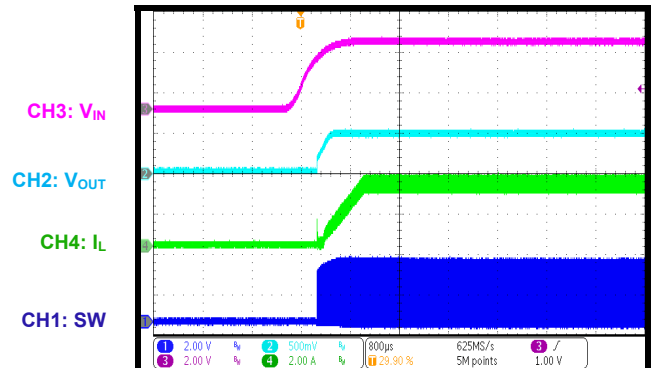
Start-Up through VIN

$I_{OUT} = 0A$, FCCM



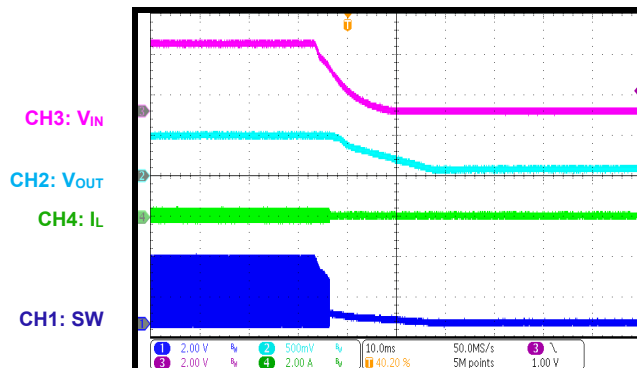
Start-Up through VIN

$I_{OUT} = 3A$



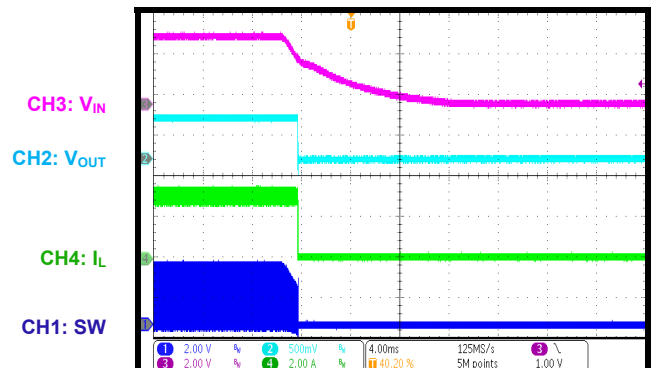
Shutdown through VIN

$I_{OUT} = 0A$, FCCM



Shutdown through VIN

$I_{OUT} = 3A$

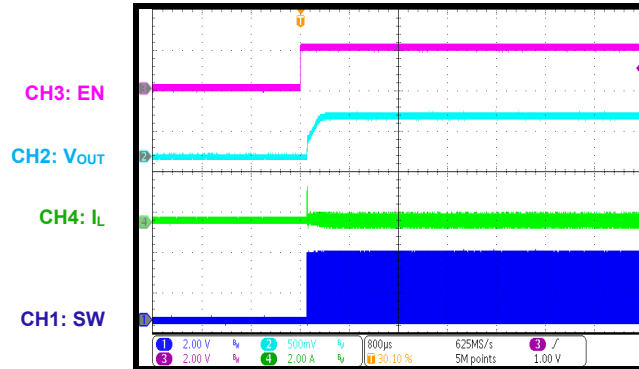


EVB TEST RESULTS *(continued)*

Performance curves and waveforms are tested on the evaluation board. $V_{IN} = 3.3V$, $V_{OUT} = 0.5V$, $T_A = 25^{\circ}C$, unless otherwise noted.

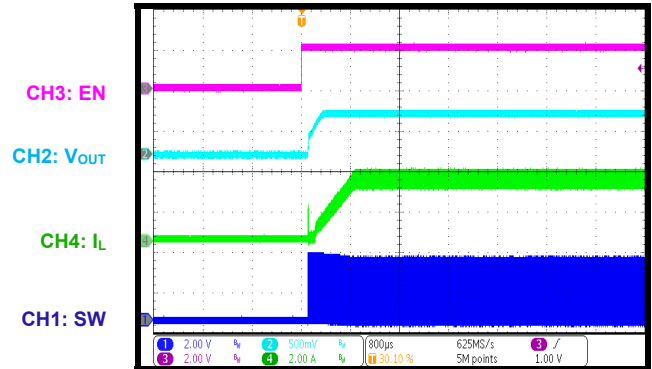
Start-Up through EN

$I_{OUT} = 0A$, FCCM



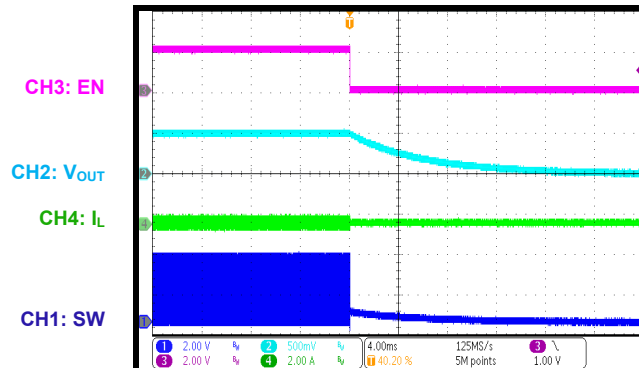
Start-Up through EN

$I_{OUT} = 3A$



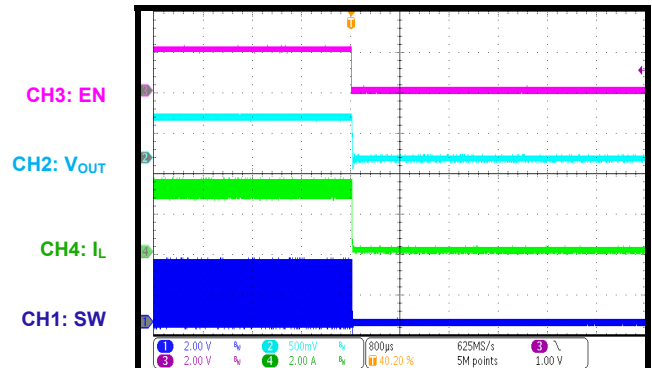
Shutdown through EN

$I_{OUT} = 0A$, FCCM



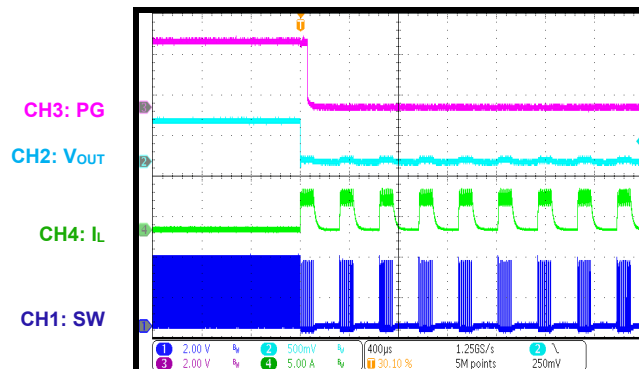
Shutdown through EN

$I_{OUT} = 3A$



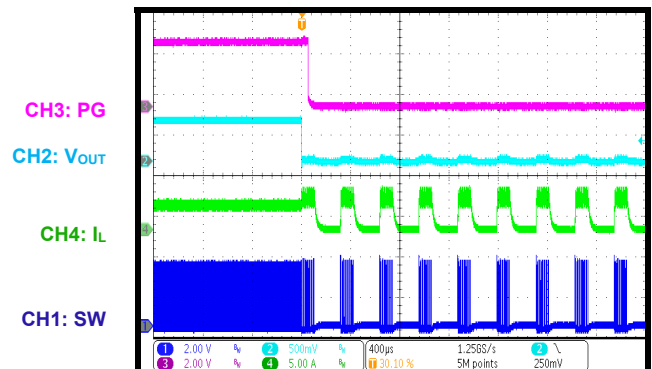
SCP Entry

$I_{OUT} = 0A$, FCCM



SCP Entry

$I_{OUT} = 3A$

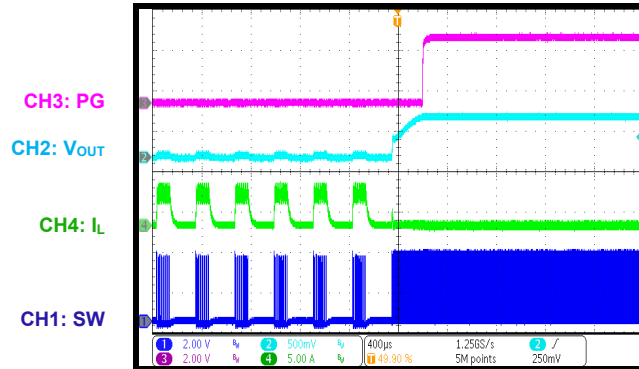


EVB TEST RESULTS *(continued)*

Performance curves and waveforms are tested on the evaluation board. $V_{IN} = 3.3V$, $V_{OUT} = 0.5V$, $T_A = 25^\circ C$, unless otherwise noted.

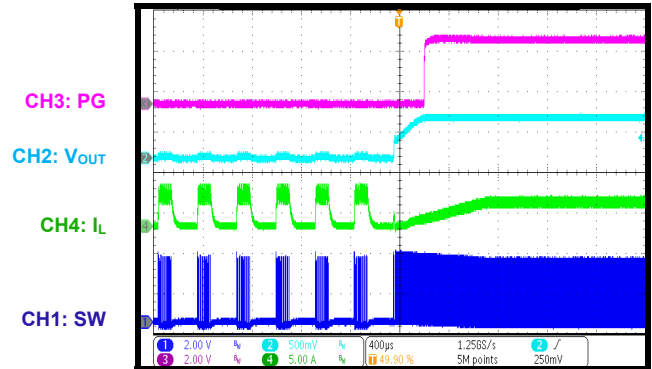
SCP Recovery

$I_{OUT} = 0A$, FCCM

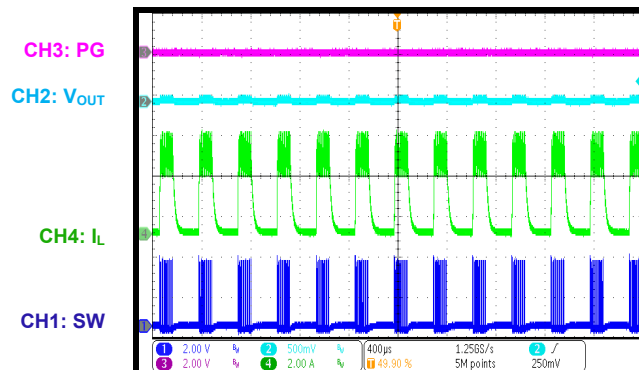


SCP Recovery

$I_{OUT} = 3A$

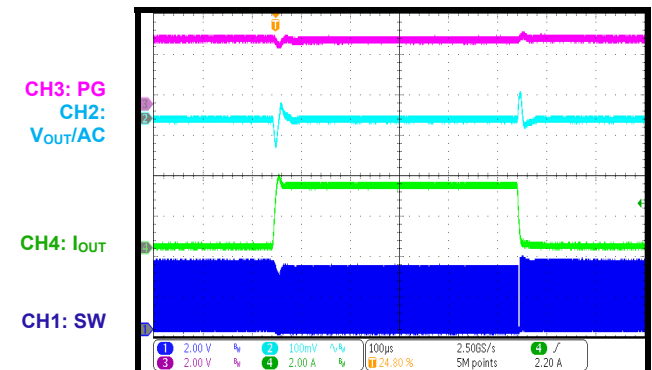


SCP Steady State



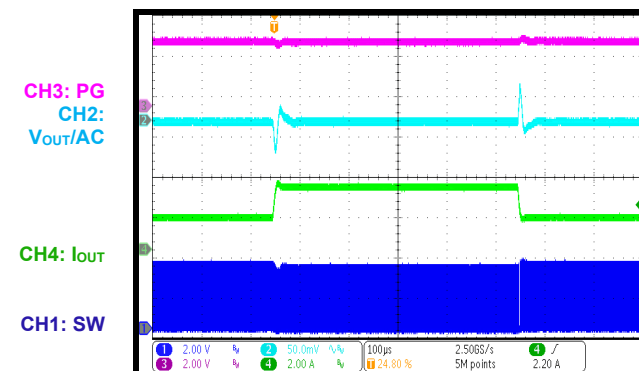
Load Transient

$I_{OUT} = 0A$ to $3A$, $1.6A/\mu s$



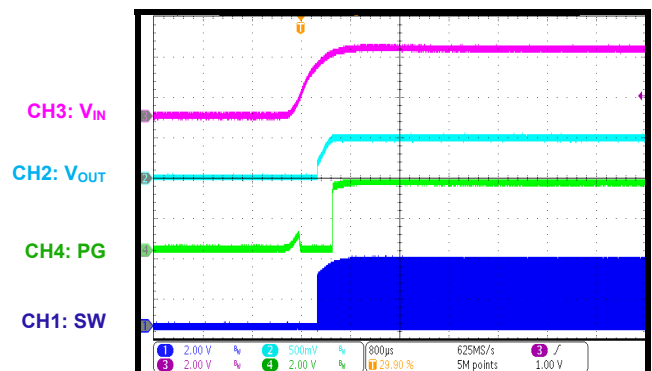
Load Transient

$I_{OUT} = 1.5A$ to $3A$, $1.6A/\mu s$



PG Start-Up through VIN

$I_{OUT} = 0A$, FCCM

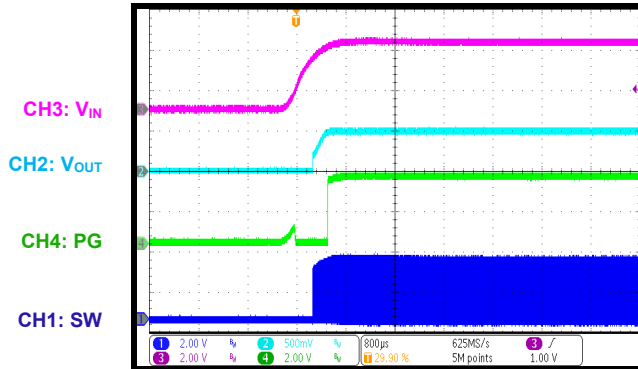


EVB TEST RESULTS *(continued)*

Performance curves and waveforms are tested on the evaluation board. $V_{IN} = 3.3V$, $V_{OUT} = 0.5V$, $T_A = 25^\circ C$, unless otherwise noted.

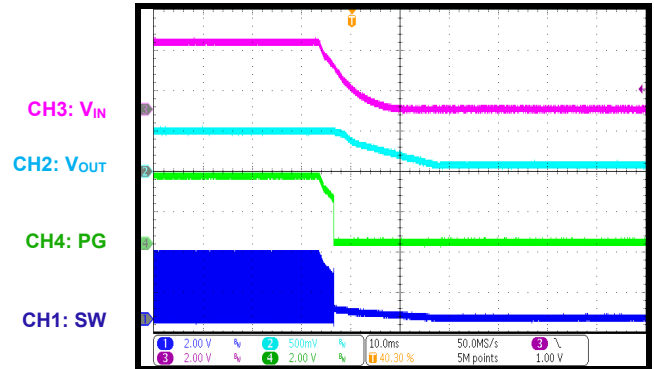
PG Start-Up through VIN

$I_{OUT} = 3A$



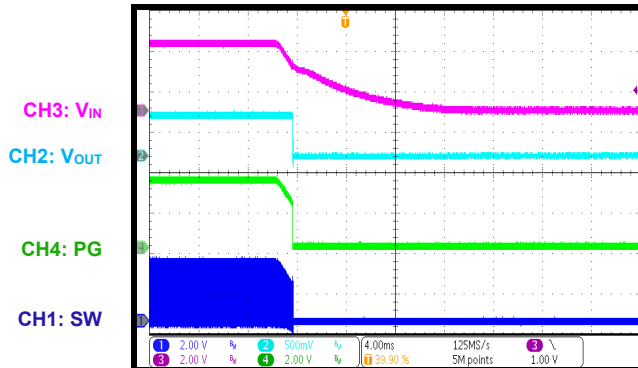
PG Shutdown through VIN

$I_{OUT} = 0A$, FCCM



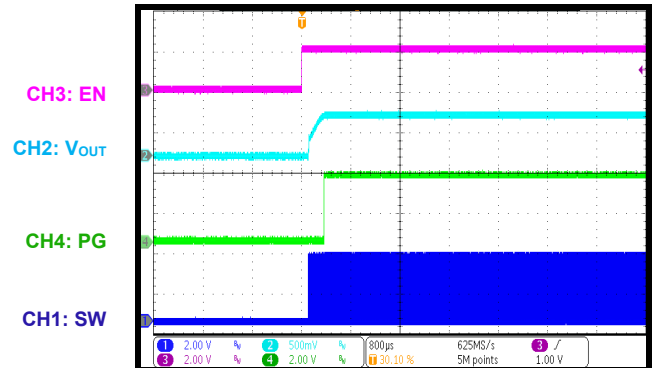
PG Shutdown through VIN

$I_{OUT} = 3A$



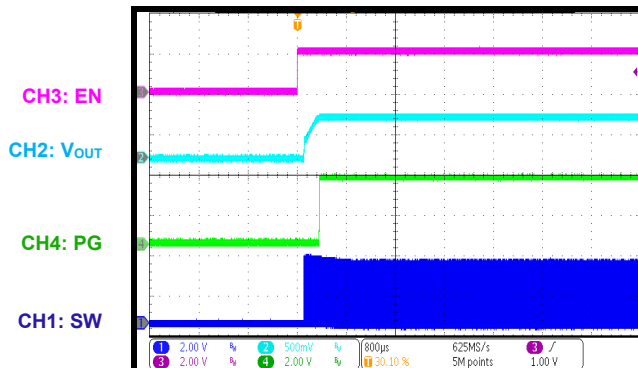
PG Start-Up through EN

$I_{OUT} = 0A$, FCCM



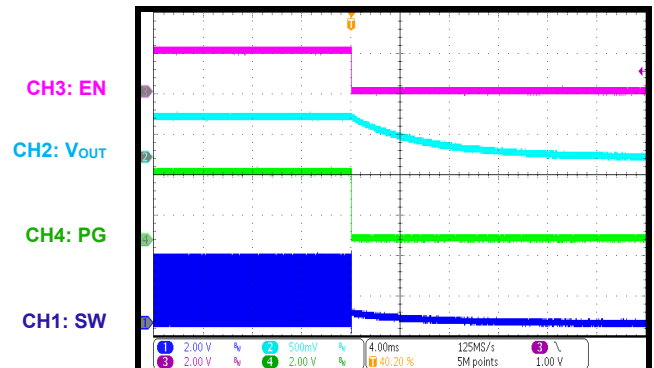
PG Start-Up through EN

$I_{OUT} = 3A$



PG Shutdown through EN

$I_{OUT} = 0A$, FCCM

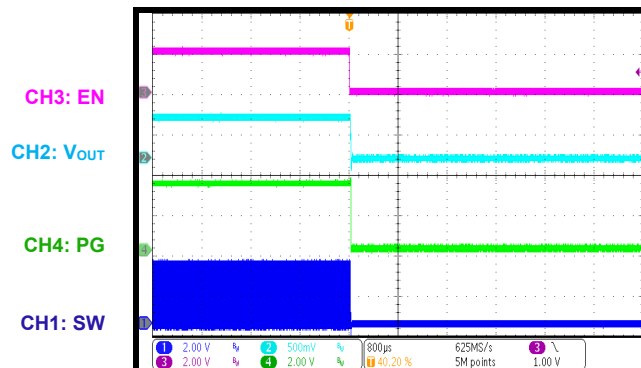


EVB TEST RESULTS *(continued)*

Performance curves and waveforms are tested on the evaluation board. $V_{IN} = 3.3V$, $V_{OUT} = 0.5V$, $T_A = 25^{\circ}C$, unless otherwise noted.

PG Shutdown through EN

$I_{OUT} = 3A$



PCB LAYOUT (2)

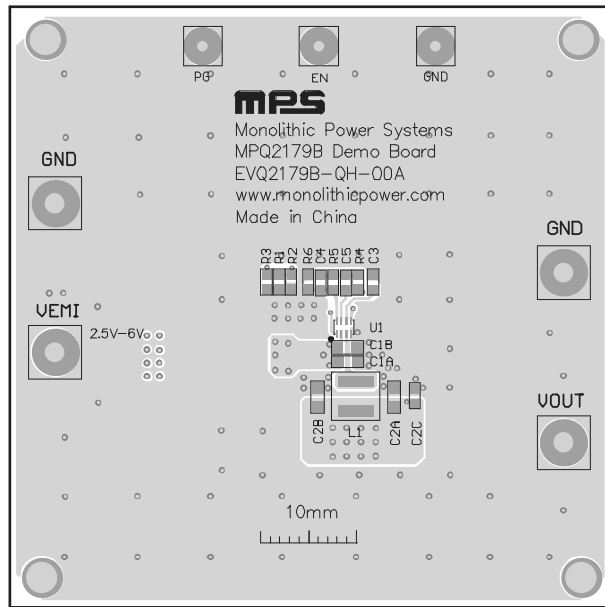


Figure 4: Top Silk and Top Layer

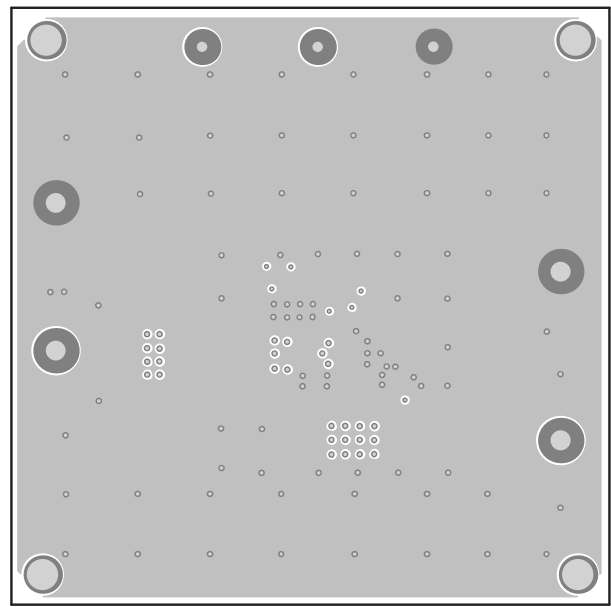


Figure 5: Mid-Layer 1

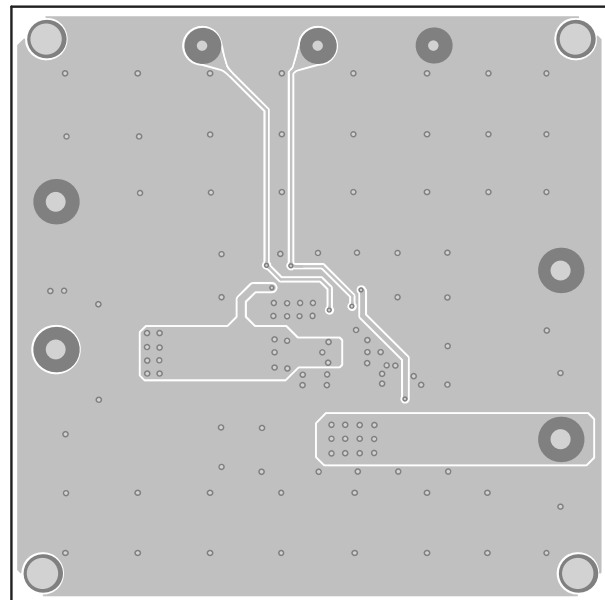


Figure 6: Mid-Layer 2

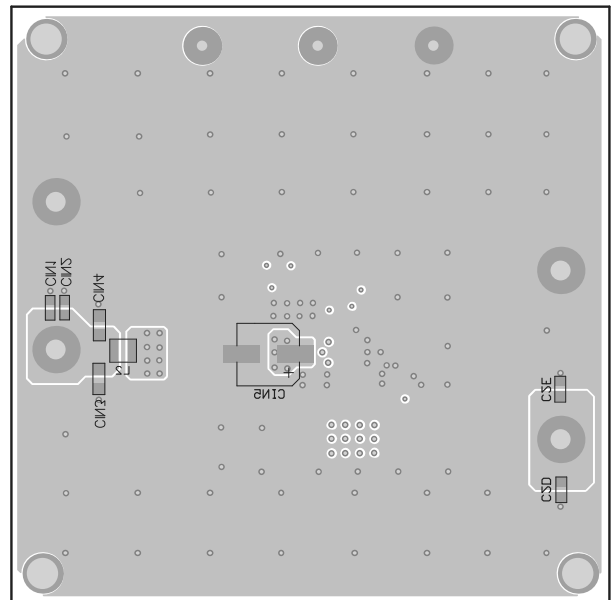


Figure 7: Bottom Layer and Bottom Silk

Note:

2) The copper thickness is 2oz.

REVISION HISTORY

Revision #	Revision Date	Description	Pages Updated
1.0	1/16/2024	Initial Release	-

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