EV2410-J-00A

High-Efficiency, Synchronous Step-Down LED Driver Evaluation Board

The Future of Analog IC Technology

DESCRIPTION

The EV2410-J-00A Evaluation Board is designed to demonstrate the capabilities of MP2410GJ. The MP2410GJ is a 24V monolithic synchronous step-down LED driver with a built-in power MOSFET and rectifier. It achieves up to 2A continue output current with excellent load and line regulation in a tiny TSOT23-6 package. Peak current mode operation provides fast transient response and eases loop stabilization.

The EV2410-J-00A is typically designed for driving 2 WLEDs in series ($5.9V_{TYP}$) LED load with 1.5A LED current at wide 8V to 24V input range.

The EV2410-J-00A has high performances in efficiency, line/load regulation and deep analog dimming. Fault condition protection includes cycle-by-cycle peak current limiting, output short circuit protection, open LED protection and thermal shutdown.

ELECTRICAL SPECIFICATION

Parameter	Symbol	Value	Units
Input Voltage	V_{IN}	8 to 24	V
Output Voltage	V_{OUT}	5.9	V
LED Current	I_{LED}	1.5	A

FEATURES

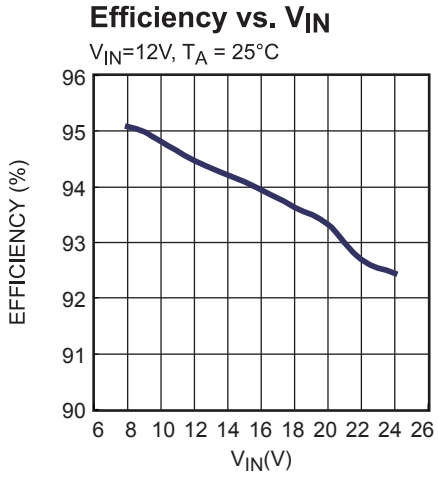
- 8V to 24V Wide Input Range
- Synchronous Step-Down Converter
- 100mΩ Internal High-side Power MOSFET
- 80mΩ Internal Low-side Synchronous Rectifier
- Peak Current Mode Control
- 1.5A Continue Output Current
- 100mV Feedback Voltage
- Up to 95% Efficiency
- Fixed 1MHz Switching Frequency
- Analog Dimming
- Cycle-by-Cycle Current Limit
- Inherent LED Open Protection
- Output Short Circuit Protection
- Thermal Shutdown
- Auto-Restart Function

APPLICATIONS

- Infrared LED Driver
- General LED Driver
- Flashlight
- Handheld Computers Backlight

EV2410-J-00A EVALUATION BOARD

(L x W x H) 46mm x 46mm x 6mm	
Board Number	**MPS IC Number**
EV2410-J-00A	MP2410GJ



EVALUATION BOARD SCHEMATIC

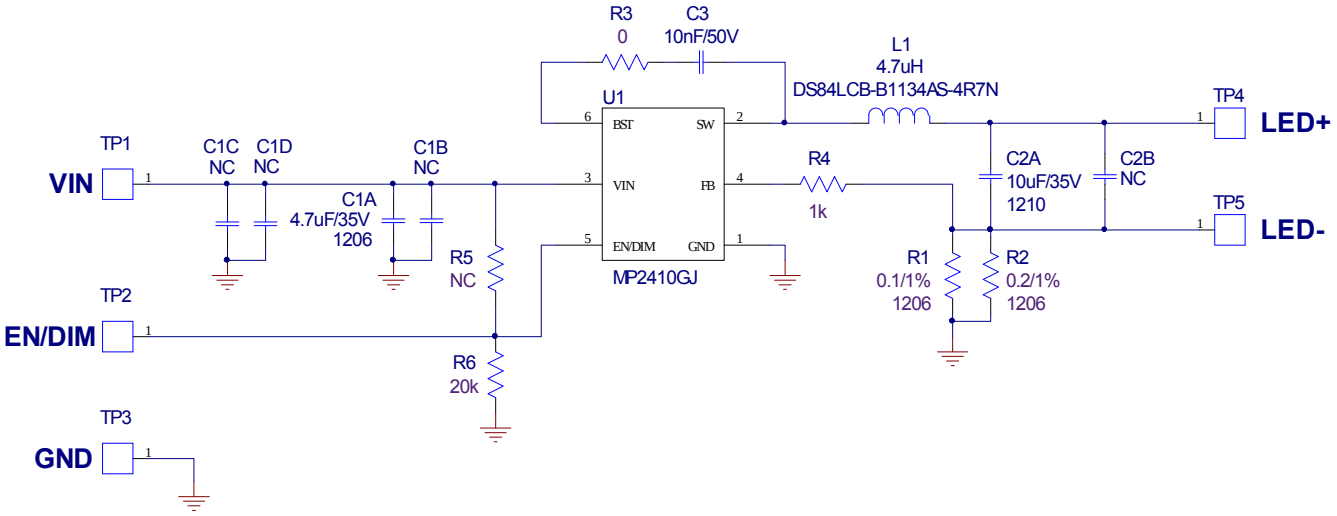


Figure 1 - Schematic

EV2410-J-00A BILL OF MATERIALS

Qty	Ref	Value	Description	Package	Manufacturer	Manufacturer _P/N
1	C1A	4.7μF/35V	Ceramic Cap, 35V, X7R	1206	Taiyo Yuden	GMK316A7475KL-T
4	C1B,C1C C1D,C2B	NC				
1	C2A	10μF/35V	Ceramic Cap, 35V, X7R	1210	muRata	GRM32ER7YA106KA12L
1	C3	10nF/50V	Ceramic Cap, 50V, X7R	0603	muRata	GRM188R71H103KA01D
1	L1	4.7μH	Inductor, 4.7μH, 3.9A	SMD	TOKO	DS84LCB-B1134AS-4R7N
1	R1	100mΩ	Thick Film Chip RES, 1%	1206	CYNTEC	RL1632H-R100-FN
1	R2	200mΩ	Thick Film Chip RES, 1%	1206	Yageo	RL1206FR-070R2L
1	R3	0Ω	Thick Film Chip RES, 1%	0603	Yageo	RC0603FR-070RL
1	R4	1kΩ	Thick Film Chip RES, 1%	0603	Yageo	RC0603FR-071KL
1	R5	NC				
1	R6	20kΩ	Thick Film Chip RES, 1%	0603	Yageo	RC0603FR-0720KL
1	U1	MP2410	Sync Step-down LED Driver	TSOT23-6	MPS	MP2410GJ-Z

PRINTED CIRCUIT BOARD LAYOUT (DOUBLE-SIDED)

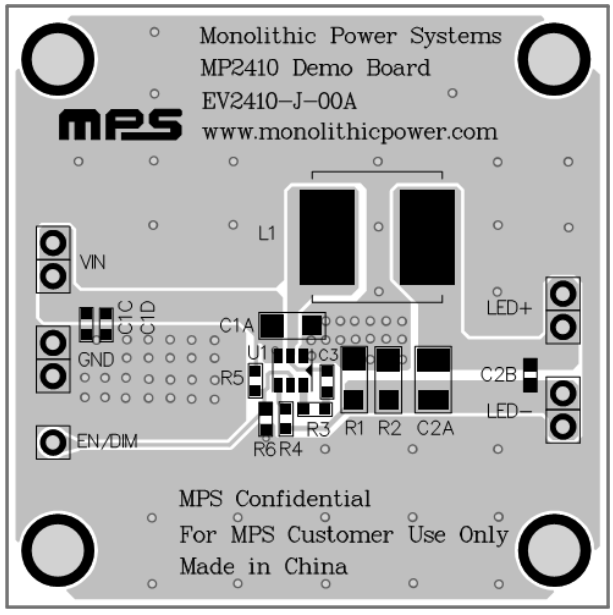


Figure 2 - Top Layer

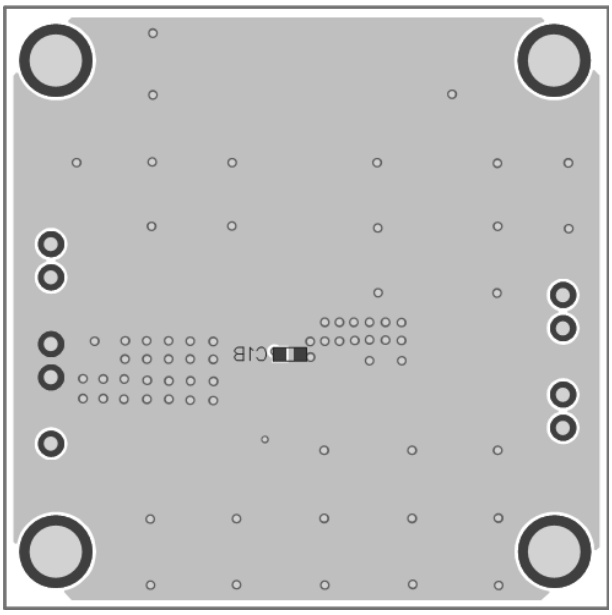


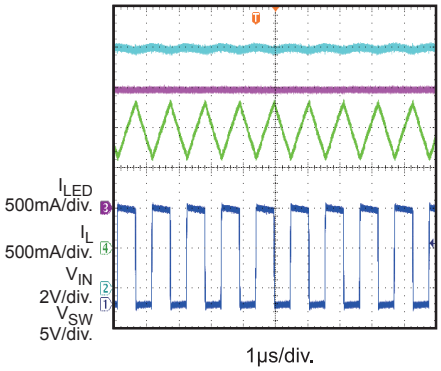
Figure 3 - Bottom Layer

EVB TEST RESULTS

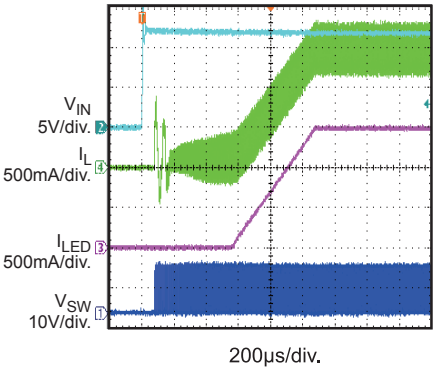
Performance waveforms are tested on the evaluation board.

V_{IN}=12V, 2 WLEDs in series, V_{OUT}=5.9V, I_{LED}=1.5A, L=4.7µH, T_A = 25°C, unless otherwise noted.

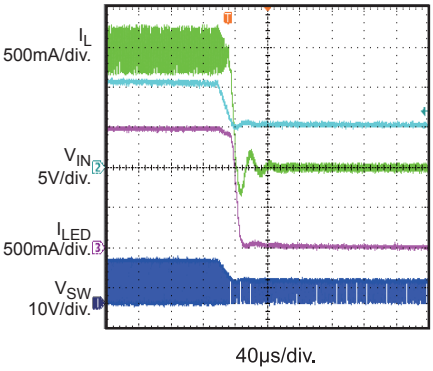
Steady State



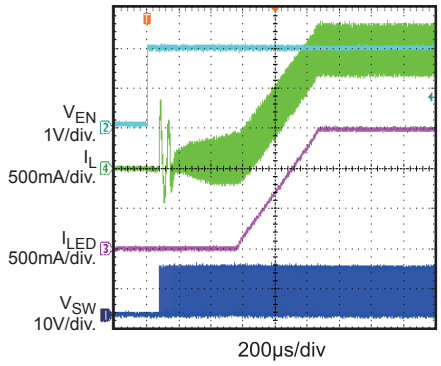
V_{IN} Start-Up



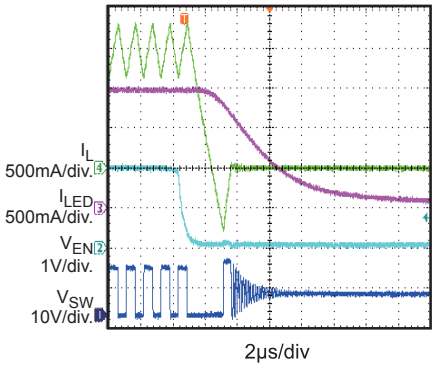
V_{IN} Shutdown



EN Start-Up

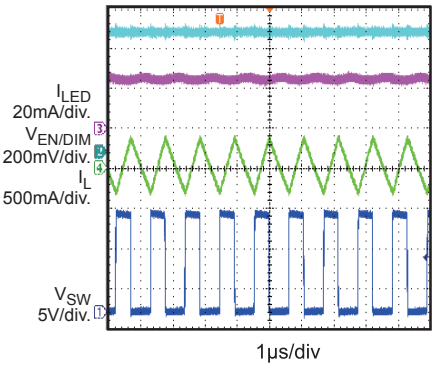


EN Shutdown

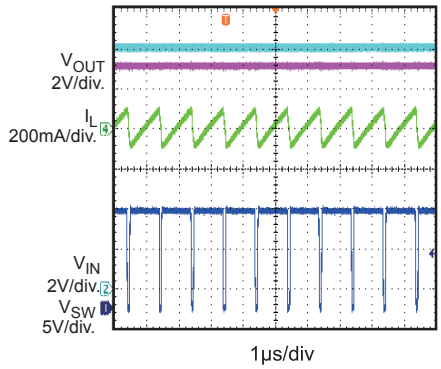


Minimum Analog Dimming

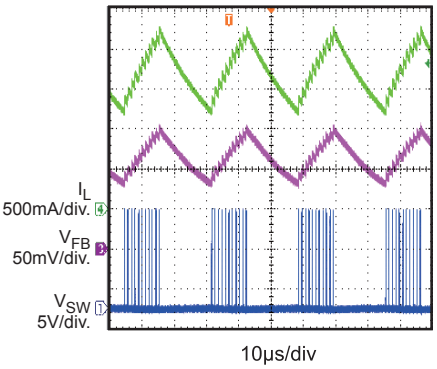
$V_{EN/DIM}$=0.6V



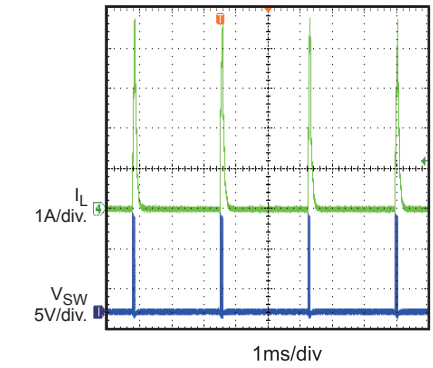
Open LED Protection



Short LED+ to LED- Protection



Short LED+ to GND Protection

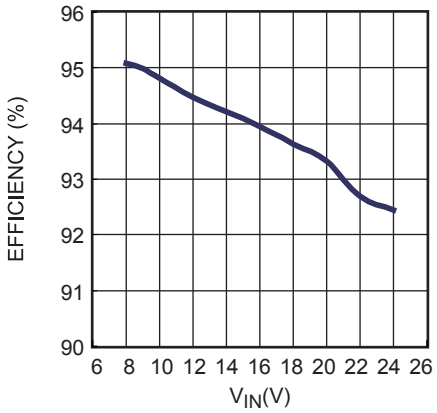


EVB TEST RESULTS *(continued)*

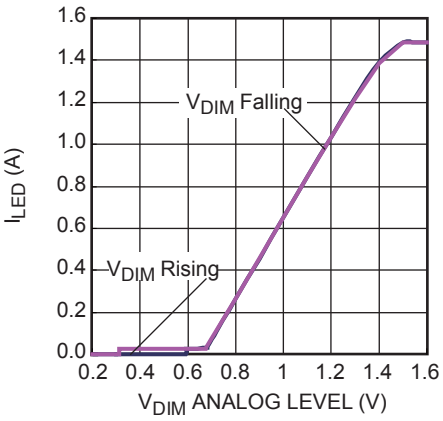
Performance waveforms are tested on the evaluation board.

V_{IN}=12V, 2 WLEDs in series, V_{OUT}=5.9V, I_{LED}=1.5A, L=4.7µH, T_A = 25°C, unless otherwise noted.

Efficiency vs. V_{IN}



Analog Dimming Curve



QUICK START GUIDE

1. Preset DC Power Supply output to 8V to 24V and turn off Power Supply.
2. Connect the positive terminal of Power Supply output to the VIN pin and the negative terminal to the GND pin.
3. Connect the LED load between “LED+” (anode of LED string) and “LED-“(cathode of LED string).
4. Turn on Power Supply.
5. Apply the Enable voltage to the EN/DIM pin and drive Enable high to turn on the chip. When Enable voltage is less than 0.3V, the chip is turned off.
6. To apply analog dimming, a variable DC signal (0.6V to 1.5V) is required. Connect the positive and negative terminals of DC signal to the EN/DIM and GND pins, respectively. By adjusting the voltage level from 0.6V to 1.5V, the LED current changes from the min scale to full scale of the maximum LED current. If the dimming voltage is higher than 1.5V, the maximum LED current is generated.
7. C1B, C1C, C1D and C2B are as option to improve EMI performance, the 10nF or 22nF low ESR ceramic cap is recommended to filter the noise.